

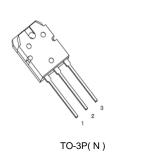
N-Channel 200 V (D-S) 175 °C MOSFET

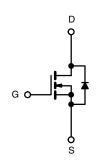
PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.)	
200	0.0138 at V _{GS} = 10 V	96	64 nC	
	$0.0141 \text{ at V}_{GS} = 7.5 \text{ V}$	90	04110	

FEATURES

- Thunder power MOSFET
- Maximum 175 °C junction temperature
- \bullet 100 % R_g and UIS tested







N-Channel MOSFET

APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	200	V			
Gate-Source Voltage	V _{GS}	± 20	v			
Continuous Dunis Comment /T 150 °C\	T _C = 25 °C		96			
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C	I _D	75	A		
Pulsed Drain Current (t = 100 μs)		I _{DM}	I _{DM} 240			
Avalanche Current	L = 0.1 mH	I _{AS}	60			
Single Avalanche Energy ^a	L = U.1 IIIII	E _{AS}	180	mJ		
Mariana Baran Biotanian 3	T _C = 25 °C		375 b	14/		
Maximum Power Dissipation ^a	T _C = 125 °C	P _D	125 ^b	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C		

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.4		

Notes

- a. Duty cycle ≤ 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200	-	-	v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	2	-	4	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA
		V _{DS} = 200 V, V _{GS} = 0 V	-	-	1	μА
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_J = 125 °C	-	-	150	
		$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	5	mA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	90	-	=	Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 30 A	-	0.0138	-	Ω
	R _{DS(on)}	$V_{GS} = 7.5 \text{ V}, I_D = 30 \text{ A}$	-	0.0141	-	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	-	75	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 100 V, f = 1 MHz	-	4132	-	pF
Output Capacitance	C _{oss}		-	246	-	
Reverse Transfer Capacitance	C _{rss}		-	21	-	
Total Gate Charge ^c	Q_g		-	64	96	nC
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	-	16.7	=	
Gate-Drain Charge ^c	Q_{gd}		-	16.9	-	
Gate Resistance	R_g	f = 1 MHz	1.5	3	5	Ω
Turn-On Delay Time ^c	t _{d(on)}		-	13	26	ns
Rise Time ^c	t _r	V_{DD} = 100 V, R_L = 1.66 Ω $I_D \cong$ 60 A, V_{GEN} = 10 V, R_g = 1 Ω	-	112	200	
Turn-Off Delay Time ^c	t _{d(off)}		-	35	70	
Fall Time ^c	t _f		-	80	150	
Drain-Source Body Diode Ratings at	nd Characteri	stics ^b (T _C = 25 °C)				
Pulsed Current (t = 100 μs)	I _{SM}		-	-	240	Α
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V	-	0.8	1.2	V
Reverse Recovery Time	t _{rr}		-	160	320	ns
Peak Reverse Recovery Charge	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/μs	-	11	20	Α
Reverse Recovery Charge	Q _{rr}		-	0.9	1.8	μC

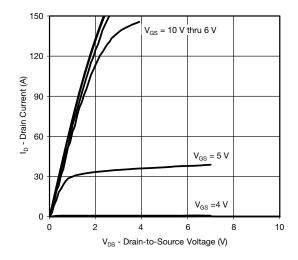
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

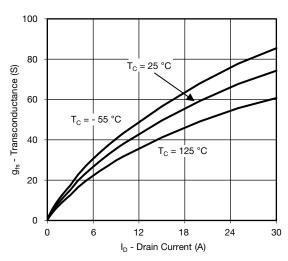
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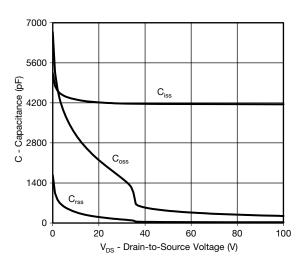
TYPICAL CHARACTERISTICS ($T_A = 25 \, ^{\circ}\text{C}$, unless otherwise noted)



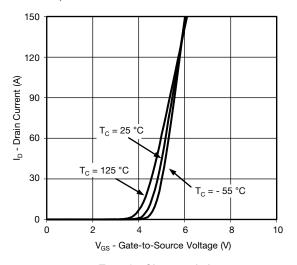
Output Characteristics



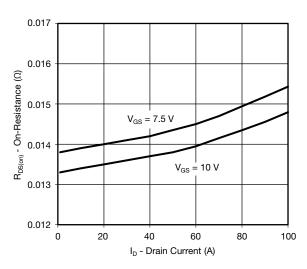
Transconductance



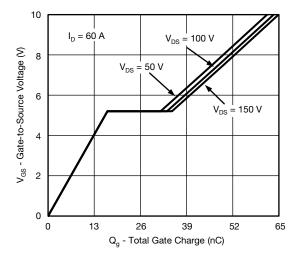
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current

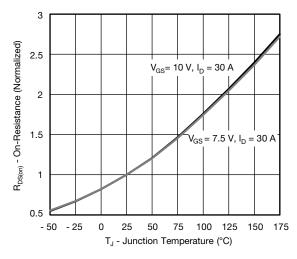


Gate Charge

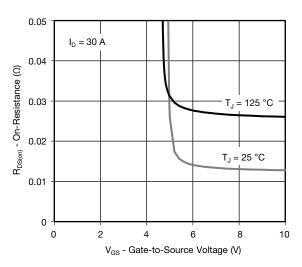
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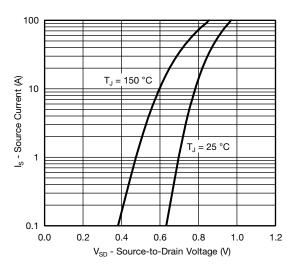
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



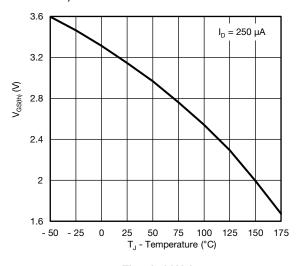
On-Resistance vs. Junction Temperature



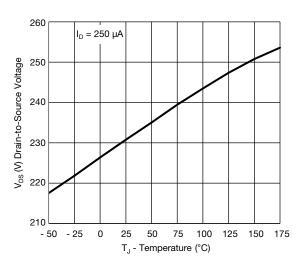
On-Resistance vs. Gate-to-Source Voltage



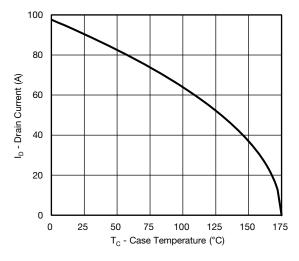
Source Drain Diode Forward Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

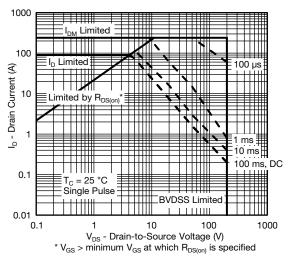


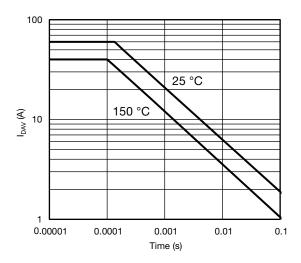
Current De-rating

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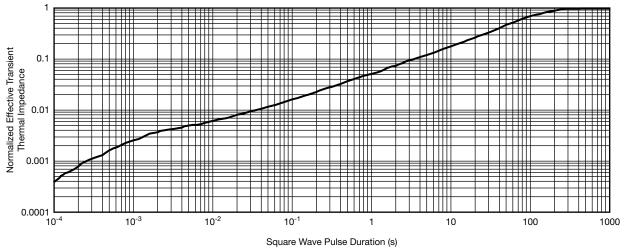
THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)





Safe Operating Area

Single Pulse Avalanche Current Capability vs. Time

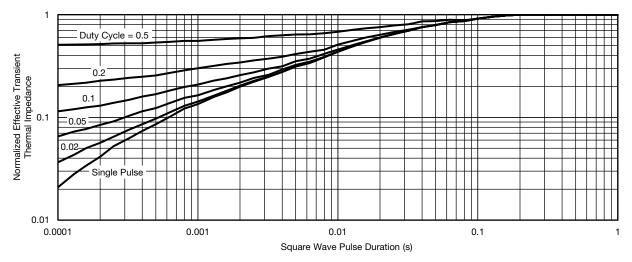


Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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