

VBQF2658 Datasheet P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ)		
- 60	0.060 at V_{GS} = - 10 V	- 11	36		
- 00	0.075 at V _{GS} = - 4.5 V	- 8	30		

FEATURES

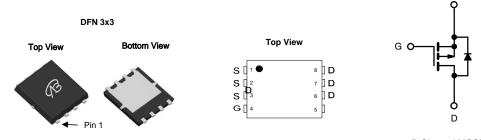
- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

• High Side Switch for Full Bridge Converter

s

• DC/DC Converter for LCD Display



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise note)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	- 60	V			
Gate-Source Voltage	V _{GS}	± 20	- V			
Continuous Drain Current ($T_{.1} = 150 \text{ °C}$)	T _C = 25 °C	1-	- 11			
Continuous Diain Current (1) = 150 C)	T _C = 100 °C	I _D	- 8.8	А		
Pulsed Drain Current		I _{DM}	- 33			
Avalanche Current, Single Pulse	L = 0.1 mH	I _{AS}	- 22			
Repetitive Avalanche Energy, Single Pulse ^a	L = 0.1 IIIH	E _{AS}	24.2	mJ		
Power Dissinction	T _C = 25 °C	PD	38.5 ^c	w		
Power Dissipation	T _A = 25 °C		2.3 ^{b, c}	vv		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marine Incelling to Angleing B	t ≤ 10 s	R _{thJA}	17	21	°C/W
Maximum Junction-to-Ambient ^b	Steady State		45	55	
Maximum Junction-to-Case	•	R _{thJC}	2.7	3.25	
Notes:					

a. Duty cycle \leq 1 %.

b. When mounted on 1" square PCB (FR-4 material).

c. See SOA curve for voltage derating.

d. Based up on $T_C = 25 \degree C$.



Available

Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_{D} = -250 \mu A$	- 60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	I _{DSS}	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current		V_{DS} = - 48 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	- 50 μA - 125	
		V_{DS} = - 48 V, V_{GS} = 0 V, T_{J} = 150 $^{\circ}$ C			- 125		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	- 20			А	
		V _{GS} = - 10 V, I _D = - 10 A		0.060			
Drain-Source On-State Resistance ^a	Base	V_{GS} = - 10 V, I _D = - 10 A, T _J = 125 °C		0.090		Ω	
Drain-Source On-State Resistance*	R _{DS(on)}	V_{GS} = - 10 V, I _D = - 10 A, T _J = 150 °C		0.102			
		V _{GS} = - 4.5 V, I _D = - 5 A		0.075			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 10 A		22		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1600		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$, $V_{DS} = -25 V$, f = 1 MHz		130			
Reverse Transfer Capacitance	C _{rss}			90			
Total Gate Charge ^c	Qg			36	40	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = -30$ V, $V_{GS} = -10$ V, $I_{D} = -10$ A		4.5			
Gate-Drain Charge ^c	Q _{gd}			7		1	
Gate Resistance	Rg	f = 1 MHz		7		Ω	
Turn-On Delay Time ^c	t _{d(on)}			8	15		
Rise Time ^c	t _r	V_{DD} = - 30 V, R_L = 3 Ω		9	15		
Turn-Off Delay Time ^c	t _{d(off)}	$\text{I}_\text{D}\cong$ - 19 A, V_GEN = - 10 V, R_g = 2.5 Ω		65	100	ns	
Fall Time ^c	t _f			30	45		
Drain-Source Body Diode and Charact	eristics (T _C = 2	5 °C) ^b					
Continuous Current	I _S				- 11		
Pulsed Current	I _{SM}				- 33	A	
Forward Voltage ^a	V _{SD}	I _F = - 19 A, V _{GS} = 0 V		- 1	- 1.5	V	
Reverse Recovery Time	t _{rr}	I _F = - 19 A, di/dt = 100 A/μs		41	61	ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

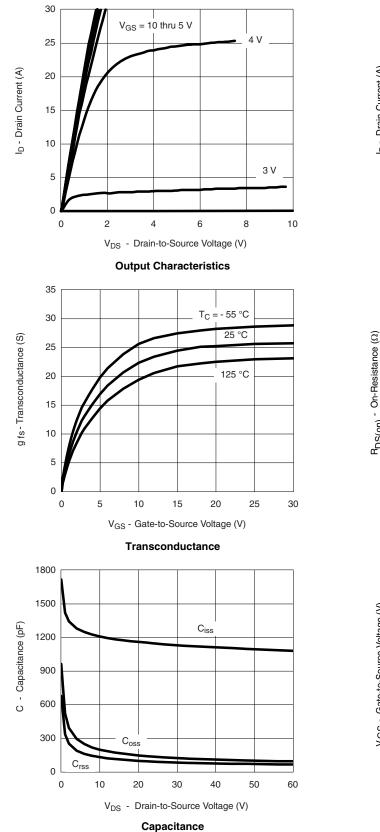
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

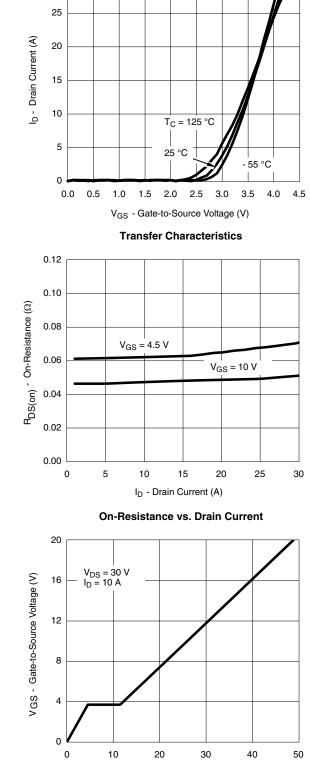
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

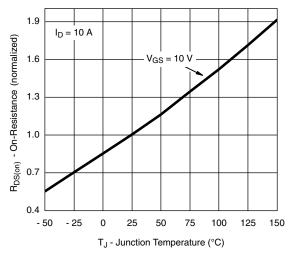


Qg - Total Gate Charge (nC)

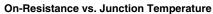
Gate Charge

30

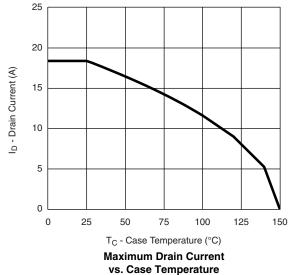


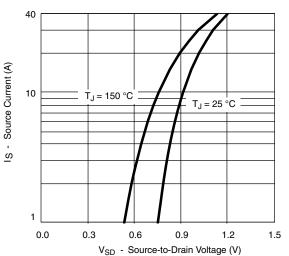


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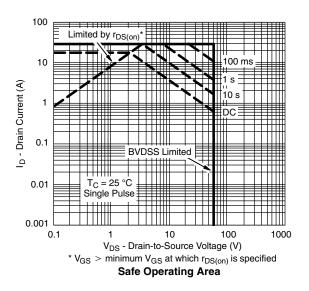


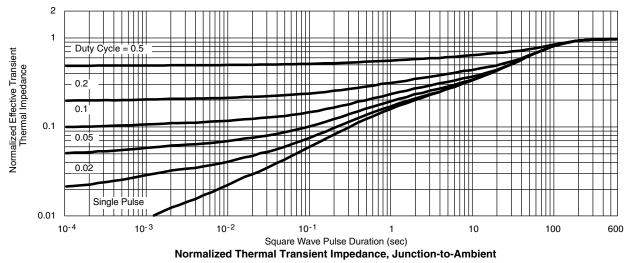






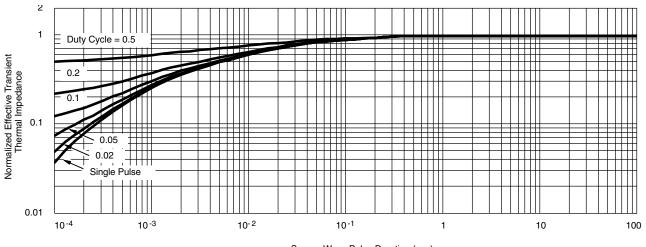
Source-Drain Diode Forward Voltage







THERMAL RATINGS

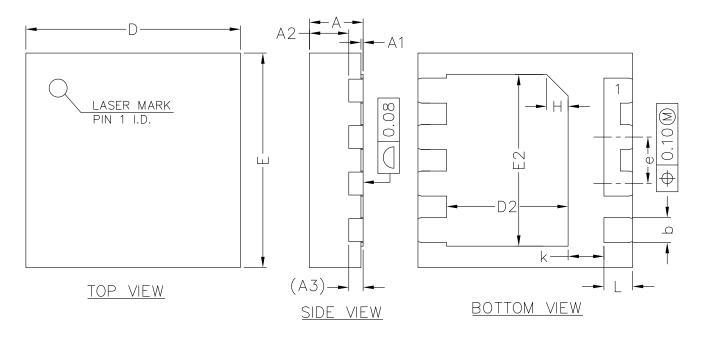


Square Wave Pulse Duration (sec)

Normalized Thermal Transient Impedance, Junction-to-Case



DFN3x3





<u>SIDE VIEW</u>

	N 41 N 1				
SYMBOL	MIN	NOM	MAX		
А	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
A3	0.20REF				
b	0.30	0.35	0.40		
D	2.90	3.00	3.10		
E	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
L	0.35	0.40	0.45		

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



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