

Power MOSFET

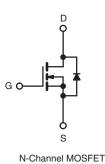
| PRODUCT SUMMARY | | | | | |
|----------------------------|----------------------------|--|--|--|--|
| V _{DS} (V) | 850 | | | | |
| $R_{DS(on)}(\Omega)$ | V _{GS} = 10 V 1.6 | | | | |
| Q _g (Max.) (nC) | 130 | | | | |
| Q _{gs} (nC) | 17 | | | | |
| Q _{gd} (nC) | 72 | | | | |
| Configuration | Single | | | | |

FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Isolated central mounting hole
- · Fast switching
- Ease of paralleling
- Simple drive requirements







| PARAMETER | | | SYMBOL | LIMIT | UNIT | |
|---|----------------------------|---|-----------------------------------|-------------|----------|--|
| Drain-Source Voltage | | | V _{DS} | 850 | | |
| Gate-Source Voltage | | | V_{GS} | ± 20 | V | |
| Continuous Drain Current | V_{GS} at 10 V $T_{C} =$ | $T_C = 25 ^{\circ}C$ $T_C = 100 ^{\circ}C$ | | 7.0 | А | |
| Continuous Drain Current | V _{GS} at 10 V | T _C = 100 °C | I _D | 4.2 | | |
| Pulsed Drain Current ^a | I _{DM} | 24 | | | | |
| Linear Derating Factor | | | | 1.2 | W/°C | |
| Single Pulse Avalanche Energy ^b | | | E _{AS} | 490 | mJ | |
| Repetitive Avalanche Current a | | I _{AR} | 5.4 | Α | | |
| Repetitive Avalanche Energy ^a | | E _{AR} | 15 | mJ | | |
| Maximum Power Dissipation $T_C = 25 ^{\circ}\text{C}$ | | | P_{D} | 150 | W | |
| Peak Diode Recovery dV/dt ^c | | dV/dt | 2.0 | V/ns | | |
| Operating Junction and Storage Temperature Range | | | T _J , T _{stg} | -55 to +150 | °C | |
| Soldering Recommendations (Peak Temperature) ^d | for | 10 s | | 300 | | |
| Maunting Tayous | 6-32 or M3 screw | | | 10 | lbf ⋅ in | |
| Mounting Torque | | | | 1.1 | N⋅m | |

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. $V_{DD} = 50$ V, starting $T_J = 25$ °C, L = 31 mH, $R_g = 25$ Ω , $I_{AS} = 5.4$ A (see fig. 12). c. $I_{SD} \le 5.4$ A, $I_{AS} = 5.4$
- d. 1.6 mm from case.

服务热线:400-655-8788

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| THERMAL RESISTANCE RATINGS | | | | | |
|-------------------------------------|-------------------|------|------|------|--|
| PARAMETER | SYMBOL | TYP. | MAX. | UNIT | |
| Maximum Junction-to-Ambient | R _{thJA} | = | 40 | | |
| Case-to-Sink, Flat, Greased Surface | R _{thCS} | 0.24 | - | °C/W | |
| Maximum Junction-to-Case (Drain) | R _{thJC} | - | 0.83 | | |

| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNIT |
|---|-----------------------|---|---|------|------|------------------|------|
| Static | | | | | ļ. | ļ | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$ | | 850 | - | - | V |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | Reference t | to 25 °C, I _D = 1 mA | - | 0.98 | - | V/°C |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V$ | ' _{GS} , I _D = 250 μA | 2.0 | - | 4.0 | V |
| Gate-Source Leakage | I _{GSS} | V _G | _S = ± 20 V | - | - | ± 100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | | 50 V, V _{GS} = 0 V / _{GS} = 0 V, T _J = 125 °C | - | - | 100 500 | μΑ |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} = 10 V | I _D = 3.2 A ^b | - | 1.6 | - | Ω |
| Forward Transconductance | 9 _{fs} | | 00 V, I _D = 3.2 A ^b | 3.0 | - | - | S |
| Dynamic | | | | | I. | • | |
| Input Capacitance | C _{iss} | $V_{GS} = 0 V$, | | - | 1900 | - | |
| Output Capacitance | C _{oss} | | $_{OS} = 25 \text{ V},$ | 1 | 470 | - | pF |
| Reverse Transfer Capacitance | C _{rss} | f = 1.0 | MHz, see fig. 5 | - | 280 | - | |
| Total Gate Charge | Qg | | | 1 | - | 130 | |
| Gate-Source Charge | Q_{gs} | V _{GS} = 10 V | $I_D = 5.4 \text{ A}, V_{DS} = 400 \text{ V},$ see fig. 6 and 13 b | - | - | 17 | nC |
| Gate-Drain Charge | Q _{gd} | 1 | See lig. 0 and 15 | - | - | 72 | |
| Turn-On Delay Time | t _{d(on)} | | | | 16 | - | - ns |
| Rise Time | t _r | $V_{DD} = 400 \text{ V, } I_D = 5.4 \text{ A,}$ $R_g = 9.1 \Omega, R_D = 75 \Omega, \text{ see fig. } 10^{\text{ b}}$ | | - | 36 | - | |
| Turn-Off Delay Time | t _{d(off)} | | | ı | 100 | - | |
| Fall Time | t _f | | | - | 32 | - | |
| Internal Drain Inductance | L _D | Between lead, 6 mm (0.25") from package and center of die contact | | - | 5.0 | - | |
| Internal Source Inductance | L _S | | | - | 13 | - | nH |
| Drain-Source Body Diode Characteristic | s | | | | | | |
| Continuous Source-Drain Diode Current | I _S | MOSFET symbo | MOSFET symbol showing the | | - | 5.4 | A |
| Pulsed Diode Forward Current ^a | I _{SM} | integral reverse p - n junction diode | | - | - | 22 | |
| Body Diode Voltage | V _{SD} | T _J = 25 °C, I _S | $_{S} = 5.4 \text{ A}, V_{GS} = 0 \text{ V}^{b}$ | - | - | 1.8 | V |
| Body Diode Reverse Recovery Time | t _{rr} | T = 25 °C | E 4.A. dl/dt = 100.A/::a.b | - | 550 | 830 | ns |
| Body Diode Reverse Recovery Charge | Q _{rr} | $T_J = 25 ^{\circ}\text{C}, I_F = 5.4 \text{A}, \text{dI/dt} = 100 \text{A/}\mu\text{s}^{\text{b}}$ | | - | 2.4 | 3.6 | μC |
| Forward Turn-On Time | t _{on} | Intrinsic turn-on time is negligible (turn-on is dominated by L _S and L _D) | | | | L _D) | |

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width $\leq 300~\mu s;$ duty cycle $\leq 2~\%.$



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

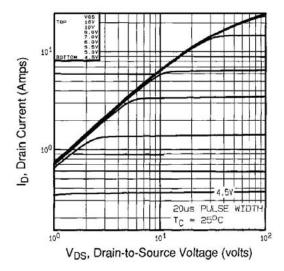


Fig. 1 - Typical Output Characteristics, $T_C = 25$ °C

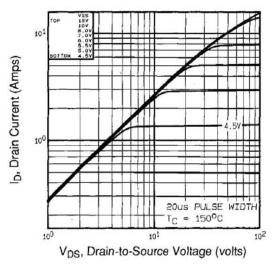


Fig. 2 - Typical Output Characteristics, $T_C = 150$ °C

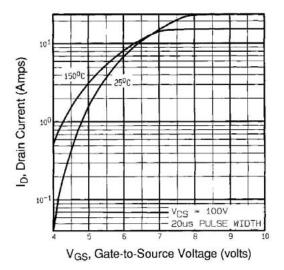


Fig. 3 - Typical Transfer Characteristics

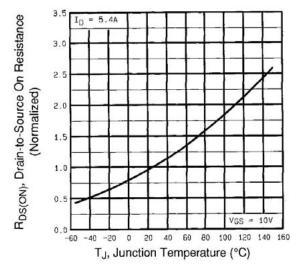


Fig. 4 - Normalized On-Resistance vs. Temperature



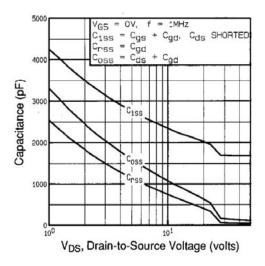


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

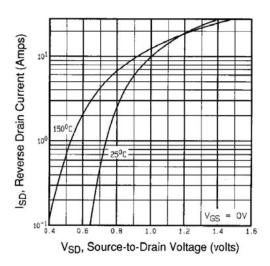


Fig. 7 - Typical Source-Drain Diode Forward Voltage

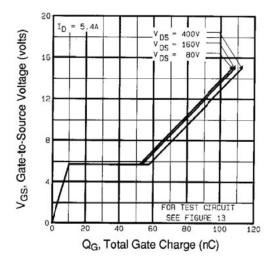


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

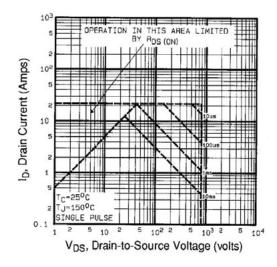


Fig. 8 - Maximum Safe Operating Area



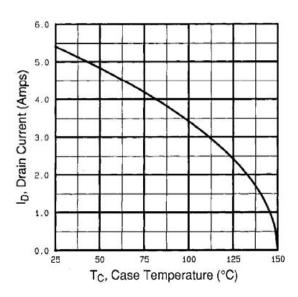


Fig. 9 - Maximum Drain Current vs. Case Temperature

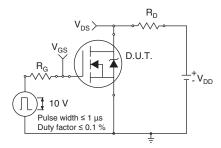


Fig. 10a - Switching Time Test Circuit

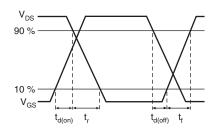


Fig. 10b - Switching Time Waveforms

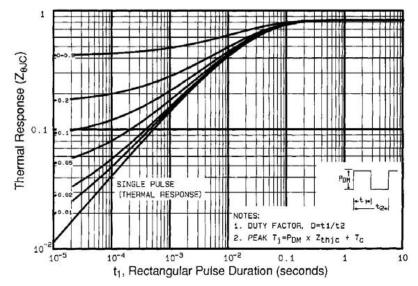
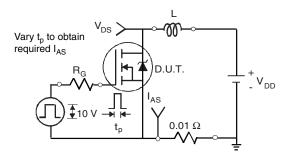
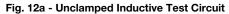


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case







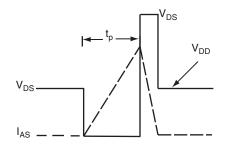


Fig. 12b - Unclamped Inductive Waveforms

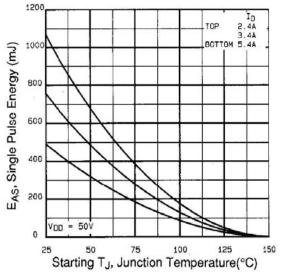


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

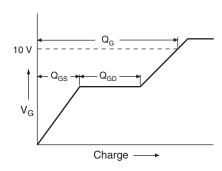


Fig. 13a - Basic Gate Charge Waveform

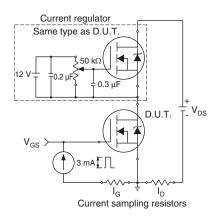
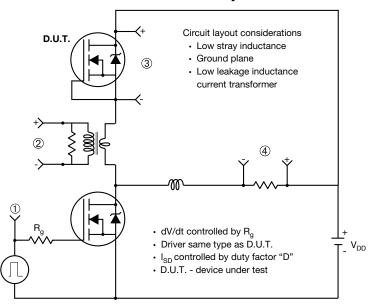


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



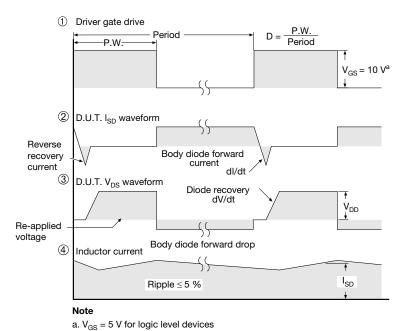
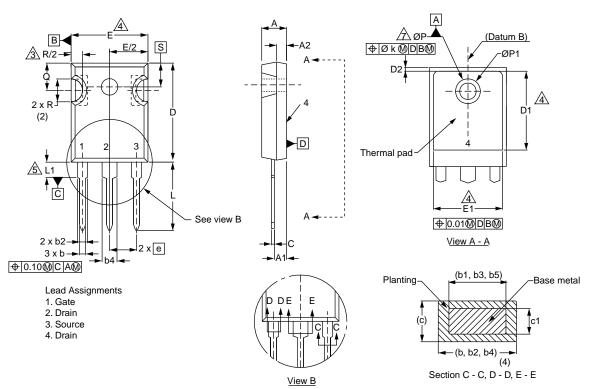


Fig. 14 - For N-Channel



TO-247AC (High Voltage)



| | MILLIMETERS | | INC | HES |
|------|-------------|-------|-------|-------|
| DIM. | MIN. | MAX. | MIN. | MAX. |
| Α | 4.58 | 5.31 | 0.180 | 0.209 |
| A1 | 2.21 | 2.59 | 0.087 | 0.102 |
| A2 | 1.17 | 2.49 | 0.046 | 0.098 |
| b | 0.99 | 1.40 | 0.039 | 0.055 |
| b1 | 0.99 | 1.35 | 0.039 | 0.053 |
| b2 | 1.53 | 2.39 | 0.060 | 0.094 |
| b3 | 1.65 | 2.37 | 0.065 | 0.093 |
| b4 | 2.42 | 3.43 | 0.095 | 0.135 |
| b5 | 2.59 | 3.38 | 0.102 | 0.133 |
| С | 0.38 | 0.86 | 0.015 | 0.034 |
| c1 | 0.38 | 0.76 | 0.015 | 0.030 |
| D | 19.71 | 20.82 | 0.776 | 0.820 |
| D1 | 13.08 | - | 0.515 | _ |

| | MILLIM | IETERS | INC | HES | |
|------|----------|----------|-------|-----------|--|
| DIM. | MIN. | MAX. | MIN. | MAX. | |
| D2 | 0.51 | 1.30 | 0.020 | 0.051 | |
| E | 15.29 | 15.87 | 0.602 | 0.625 | |
| E1 | 13.72 | - | 0.540 | - | |
| е | 5.46 | 5.46 BSC | | 0.215 BSC | |
| Øk | 0.254 | | 0.0 |)10 | |
| L | 14.20 | 16.25 | 0.559 | 0.640 | |
| L1 | 3.71 | 4.29 | 0.146 | 0.169 | |
| N | 7.62 BSC | | 0.300 | BSC | |
| ØΡ | 3.51 | 3.66 | 0.138 | 0.144 | |
| Ø P1 | - | 7.39 | - | 0.291 | |
| Q | 5.31 | 5.69 | 0.209 | 0.224 | |
| R | 4.52 | 5.49 | 0.178 | 0.216 | |
| S | 5.51 BSC | | 0.217 | 'BSC | |



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