

### **Power MOSFET**

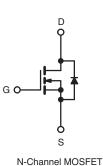
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	550				
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = 10 V 3.0				
Q <sub>g</sub> (Max.) (nC)	18				
Q <sub>gs</sub> (nC)	3.0				
Q <sub>gd</sub> (nC)	8.9				
Configuration	Single				

#### **FEATURES**

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Available in Tape and Reel
- · Fast Switching
- · Ease of Paralleling







PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V <sub>DS</sub>	550		
Gate-Source Voltage			$V_{GS}$	± 20	V	
Continuous Drain Current	V <sub>GS</sub> at 10 V	$T_C = 25 ^{\circ}C$ $T_C = 100 ^{\circ}C$		2.0		
Continuous Drain Current	T <sub>C</sub> = 100 °C	I <sub>D</sub>	1.3	Α		
Pulsed Drain Current <sup>a</sup>	I <sub>DM</sub>	8.0				
Linear Derating Factor				0.33	W/°C	
Linear Derating Factor (PCB Mount)e				0.020		
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	74	mJ	
Repetitive Avalanche Currenta			I <sub>AR</sub>	2.0	А	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	4.2	mJ	
Maximum Power Dissipation $T_C = 25  ^{\circ}C$			P <sub>D</sub>	42	W	
Maximum Power Dissipation (PCB Mount) <sup>e</sup> T <sub>A</sub> = 25 °C				2.5		
Peak Diode Recovery dV/dt <sup>c</sup>			dV/dt	3.0	V/ns	
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	
Soldering Recommendations (Peak Temperature) <sup>d</sup> for 10 s				260		

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b.  $V_{DD} = 50 \text{ V}$ , starting  $T_J = 25 \text{ °C}$ , L = 37 mH,  $R_g = 25 \Omega$ ,  $I_{AS} = 2.0 \text{ A}$  (see fig. 12). c.  $I_{SD} \le 2.0 \text{ A}$ ,  $dI/dt \le 40 \text{ A/µs}$ ,  $V_{DD} \le V_{DS}$ ,  $T_J \le 150 \text{ °C}$ . d. 1.6 mm from case.

- e. When mounted on 1" square PCB (FR-4 or G-10 material).



THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	-	110		
Maximum Junction-to-Ambient (PCB Mount) <sup>a</sup>	R <sub>thJA</sub>	-	-	50	°C/W	
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-	-	3.0		

#### Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static				L	L		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		550	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = 1 mA	-	0.88	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	· V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	,	V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	less	V <sub>DS</sub> =	$V_{DS} = 550 \text{ V}, V_{GS} = 0 \text{ V}$		-	100	Δ
Zero Gate Voltage Drain Gurrent	I <sub>DSS</sub>	$V_{DS} = 440 \text{ V}$	V <sub>DS</sub> = 440 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-	500	μA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 1.2 A <sup>b</sup>	-	3.0	-	Ω
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> :	= 50 V, I <sub>D</sub> = 1.2 A	1.4		-	S
Dynamic							
Input Capacitance	$C_{iss}$		$V_{GS} = 0 \text{ V}.$	-	350	-	
Output Capacitance	$C_{oss}$		$V_{DS} = -25 \text{ V},$		48	-	pF
Reverse Transfer Capacitance	$C_{rss}$	f = 1.0 MHz, see fig. 5		-	8.6	-	
Total Gate Charge	Qg	$V_{GS} = 10 \text{ V}$ $I_D = 2.0 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13 <sup>b</sup>		-		18	nC
Gate-Source Charge	Q <sub>gs</sub>			-	-	3.0	
Gate-Drain Charge	Q <sub>gd</sub>			-	-	8.9	
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD}$ = 300 V, $I_{D}$ = 2.0 A, $R_{g}$ = 18 $\Omega$ , $R_{D}$ = 135 $\Omega$ , see fig. 10 <sup>b</sup>		-	10	-	ns
Rise Time	t <sub>r</sub>			-	23	-	
Turn-Off Delay Time	t <sub>d(off)</sub>			-	30	-	
Fall Time	t <sub>f</sub>			-	25	-	
Internal Drain Inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nЦ
Internal Source Inductance	L <sub>S</sub>			-	7.5	-	- nH
<b>Drain-Source Body Diode Characteristic</b>	s						
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	=	2.0	^
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	8.0	A
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C	$T_J = 25  ^{\circ}\text{C},  I_S = 2.0  \text{A},  V_{GS} = 0  \text{V}^{\text{b}}$			1.6	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	T 05 %C 1	0.0 A d1/d+ 100 A/:h	-	290	580	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$T_J = 25  ^{\circ}\text{C}, I_F = 2.0  \text{A}, dI/dt = 100  \text{A/} \mu \text{s}^{\text{b}}$		-	0.67	1.3	μC
Forward Turn-On Time	t <sub>on</sub>	t <sub>on</sub> Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )					

#### Notes

2

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq$  300  $\mu$ s; duty cycle  $\leq$  2 %.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

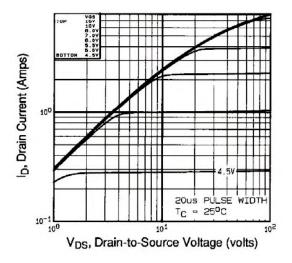


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

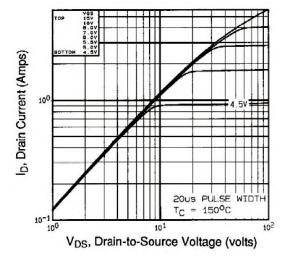


Fig. 2 - Typical Output Characteristics,  $T_C$  = 150 °C

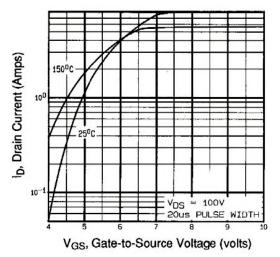


Fig. 3 - Typical Transfer Characteristics

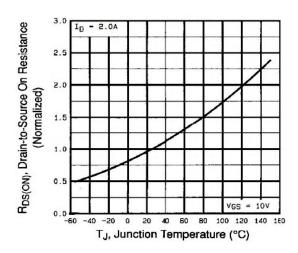


Fig. 4 - Normalized On-Resistance vs. Temperature



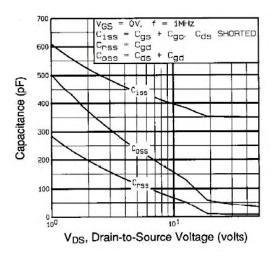


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

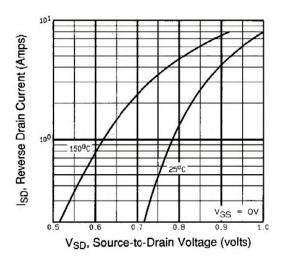


Fig. 7 - Typical Source-Drain Diode Forward Voltage

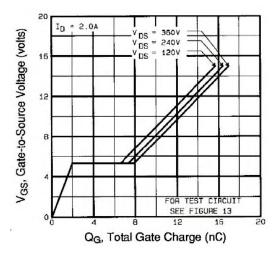


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

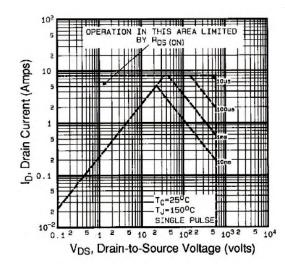


Fig. 8 - Maximum Safe Operating Area



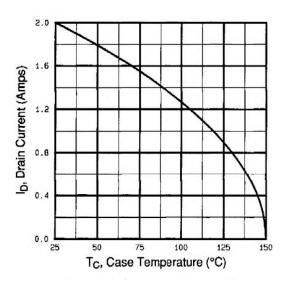


Fig. 9 - Maximum Drain Current vs. Case Temperature

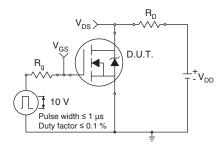


Fig. 10a - Switching Time Test Circuit

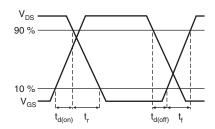


Fig. 10b - Switching Time Waveforms

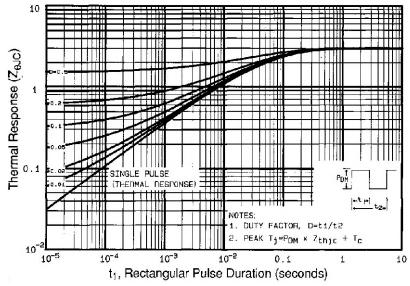


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



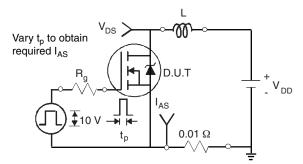


Fig. 12a - Unclamped Inductive Test Circuit

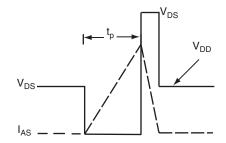


Fig. 12b - Unclamped Inductive Waveforms

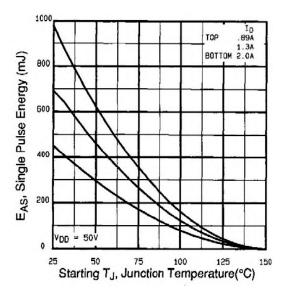


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

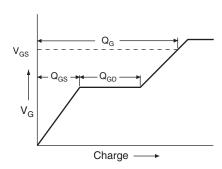


Fig. 13a - Basic Gate Charge Waveform

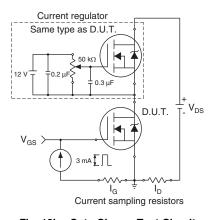
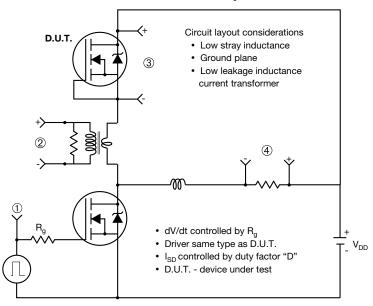


Fig. 13b - Gate Charge Test Circuit



#### Peak Diode Recovery dV/dt Test Circuit



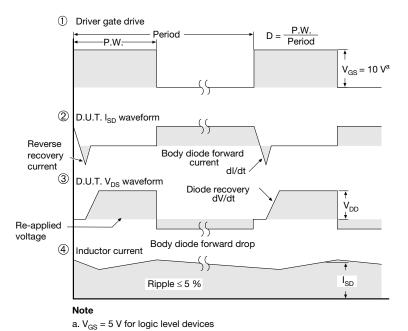
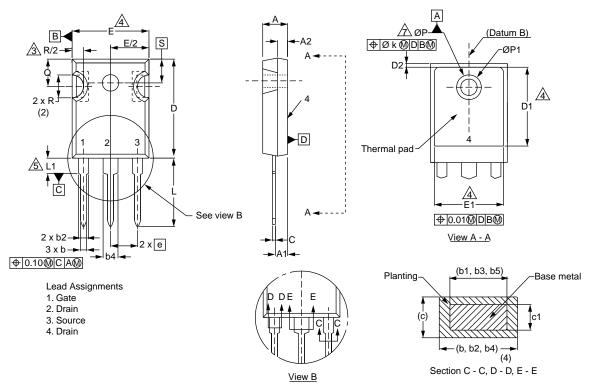


Fig. 14 - For N-Channel

服务热线:400-655-8788 7



## **TO-247AC (High Voltage)**



	MILLIN	IETERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
Α	4.58	5.31	0.180	0.209
A1	2.21	2.59	0.087	0.102
A2	1.17	2.49	0.046	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.53	2.39	0.060	0.094
b3	1.65	2.37	0.065	0.093
b4	2.42	3.43	0.095	0.135
b5	2.59	3.38	0.102	0.133
С	0.38	0.86	0.015	0.034
c1	0.38	0.76	0.015	0.030
D	19.71	20.82	0.776	0.820
D1	13.08	-	0.515	-

	MILLIM	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215 BSC		
Øk	0.254		0.010		
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0.300 BSC		
ØΡ	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217 BSC		



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