

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)		
100	0.035 at V _{GS} = 10 V	85		

FEATURES

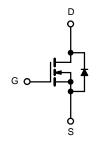
- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % Rg Tested

APPLICATIONS

• Isolated DC/DC Converters

S G D S

TO-247AC



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS} ± 20		V	
Continuous Drain Current ($T_1 = 175 ^{\circ}C$)	T _C = 25 °C	1-	85		
Continuous Drain Current $(T_j = T/5 C)$	T _C = 125 °C	- I _D -	60	_	
Pulsed Drain Current		I _{DM}	150	A	
Avalanche Current L = 0.1 mH		I _{AS}	39		
Single Pulse Avalanche Energy ^b	L = 0.1 mm	E _{AS}	61	mJ	
Mariana Diasiasiash	T _C = 25 °C	Р	375 ^c	w	
Maximum Power Dissipation ^b	T _A = 25 °C ^d	- P _D -	3.75	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)		R _{thJC}	0.4	0,10	

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS T _J = 25 °C, unless otherwise noted Parameter Symbol Test Conditions Min.					Max.	Unit
Static	Symbol	Test conditions	WIIII.	Тур.	Wax.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 µA	100			
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$	1		± 100	nA
Cate Dody Leakage	1655	$V_{\rm DS} = 0.0$ V, $V_{\rm GS} = 0.0$ V			1	11/1
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50	μA
Zero Cale Vollage Drain Carrent	.022	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 123 \text{ C}$ $V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 \text{ °C}$			250	۳A
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 V, V_{GS} = 10 V$	40		200	A
	·D(01)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-10	0.035		
Drain-Source On-State Resistance ^a	r _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}, \text{ T}_{J} = 125 \text{ °C}$		0.063		Ω
Drain-Source On-State Resistance	.03(01)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}, \text{ T}_{J} = 175 \text{ °C}$		0.084		22
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	25	0.004		S
Dynamic ^b	915		20			
Input Capacitance	C _{iss}			5100		
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		480		pF
Reverse Transfer Capacitance	C _{rss}			210		P'
Total Gate Charge ^c	Q _g			90		
Gate-Source Charge ^c	Q _{gs}	V _{DS} = 100 V, V _{GS} = 10 V, I _D = 65 A		23		nC
Gate-Drain Charge ^c	Q _{gd}			34		
Gate Resistance	Rg		0.5	1.7	3.3	Ω
Turn-On Delay Time ^c	t _{d(on)}			24	35	
Rise Time ^c	t _r	$V_{DD} = 100 \text{ V}, \text{ R}_1 = 1.5 \Omega$		220	330	ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 65 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		45	70	
Fall Time ^c	t _f	2 02.1 9		200	300	
Source-Drain Diode Ratings and Cha	<u> </u>	Γ _C = 25 °C ^b				
Continuous Current	I _S			95		
Pulsed Current	I _{SM}			85 150		A
Forward Voltage ^a	V _{SD}	I _F = 65 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}			130	200	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 50 A, di/dt = 100 A/μs		8	12	A
Reverse Recovery Charge				0.52	1.2	uC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi



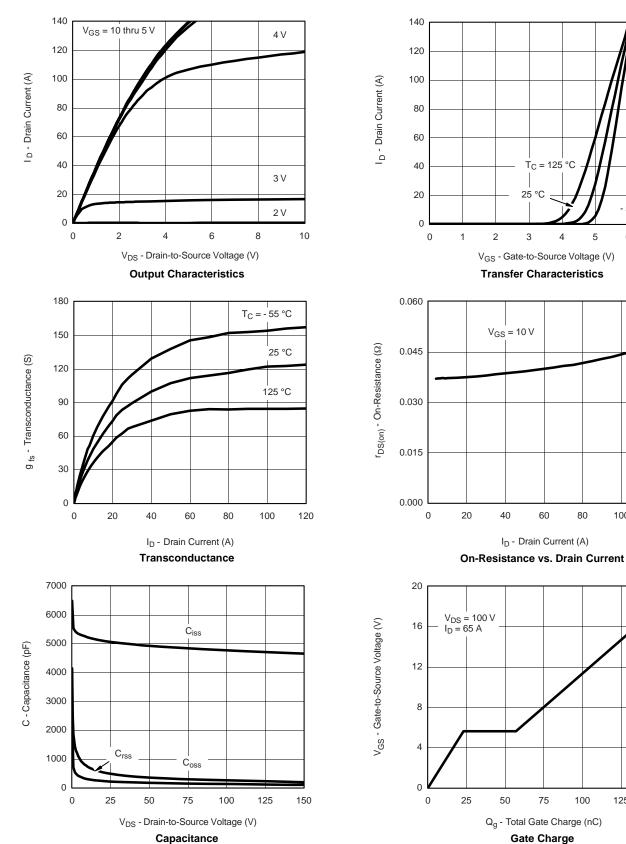
- 55 °C

T_C = 125 °C

25 °C

Gate Charge

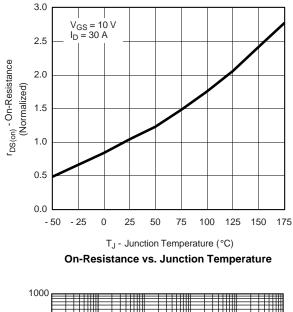
I_D - Drain Current (A)

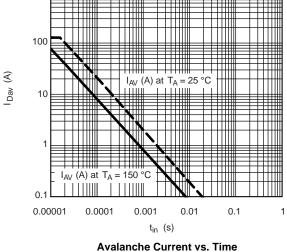


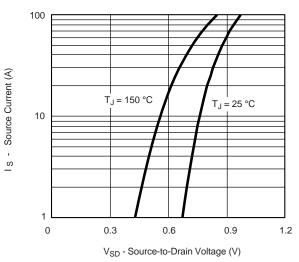
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



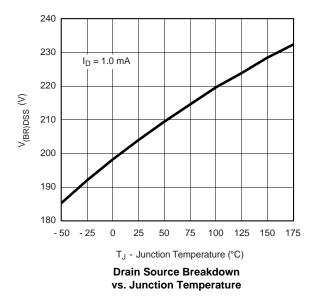
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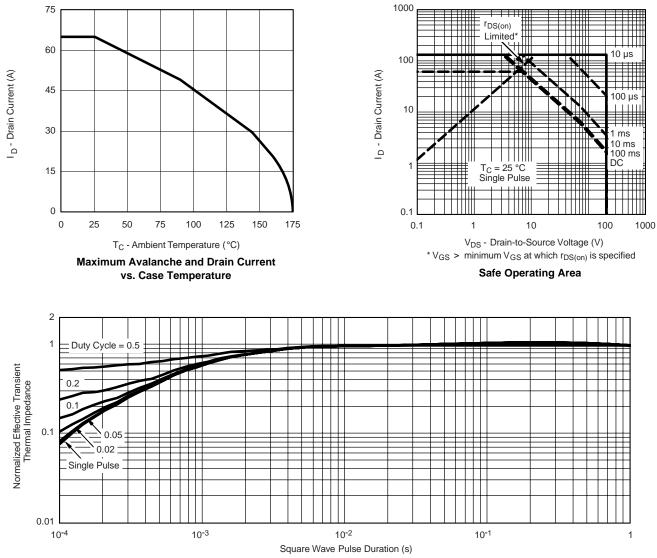


Source-Drain Diode Forward Voltage





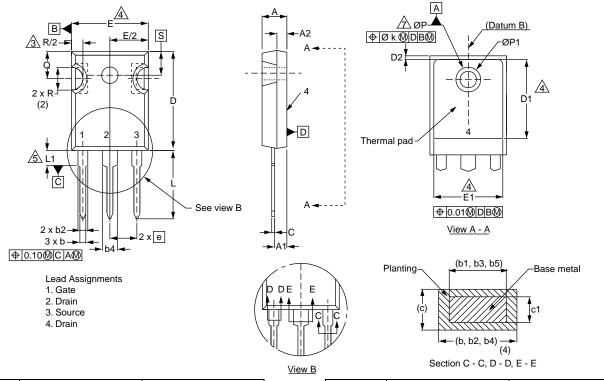
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



TO-247AC



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.58	5.31	0.180	0.209
A1	2.21	2.59	0.087	0.102
A2	1.17	2.49	0.046	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.53	2.39	0.060	0.094
b3	1.65	2.37	0.065	0.093
b4	2.42	3.43	0.095	0.135
b5	2.59	3.38	0.102	0.133
С	0.38	0.86	0.015	0.034
c1	0.38	0.76	0.015	0.030
D	19.71	20.82	0.776	0.820
D1	13.08	-	0.515	-
DI	10.00		0.010	

	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
Е	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215 BSC		
Øk	0.2	254	0.0	010	
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	7.62 BSC) BSC	
ØР	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC 0			7 BSC	



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