

RoHS

COMPLIANT

HALOGEN

FREE

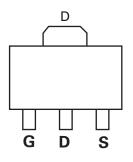
Available

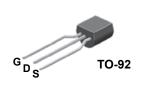
Power MOSFET

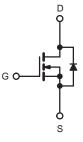
PRODUCT SUMMARY						
V _{DS} (V)	650					
R _{DS(on)} (Ω)	V _{GS} = 10 V 8					
Q _g (Max.) (nC)	18					
Q _{gs} (nC)	3.0					
Q _{gd} (nC)	8.9					
Configuration	Single					

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Surface Mount (IRFRC20, SiHFRC20)
- Straight Lead (IRFUC20, SiHFUC20)
- Available in Tape and Reel
- Fast Switching
- Ease of Paralleling
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage			V _{DS}	650	v	
Gate-Source Voltage			V _{GS}	± 20	V	
Continuous Drain Current	V _{GS} at 10 V	$T_{C} = 25 \text{ °C}$ $T_{C} = 100 \text{ °C}$		1.0		
Continuous Drain Current	VGS AL TO V		ID	1.0	A	
Pulsed Drain Current ^a			I _{DM}	1.0		
Linear Derating Factor		0.33	W/°C			
Linear Derating Factor (PCB Mount) ^e		0.020	W/ C			
Single Pulse Avalanche Energy ^b			E _{AS}	74	mJ	
Repetitive Avalanche Current ^a			I _{AR}	2.0	А	
Repetitive Avalanche Energy ^a			E _{AR} 4.2		mJ	
Maximum Power Dissipation	P	42				
Maximum Power Dissipation (PCB Mount) ^e	P _D	2.5	W			
Peak Diode Recovery dV/dtc	dV/dt	3.0	V/ns			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 150				
Soldering Recommendations (Peak Temperature)	Ū	260 ^d	°C			

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. $V_{DD} = 50 \text{ V}$, starting $T_J = 25 \text{ °C}$, L = 37 mH, $R_g = 25 \Omega$, $I_{AS} = 2.0 \text{ A}$ (see fig. 12). c. $I_{SD} \le 2.0 \text{ A}$, dl/dt $\le 40 \text{ A/}\mu\text{s}$, $V_{DD} \le V_{DS}$, $T_J \le 150 \text{ °C}$. d. 1.6 mm from case. e. When mounted on 1" square PCB (FR-4 or G-10 material).

* Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient	R _{thJA}	-	-	110			
Maximum Junction-to-Ambient (PCB Mount) ^a	R _{thJA}	-	-	50	°C/W		
Maximum Junction-to-Case (Drain)	R _{thJC}	-	-	3.0			

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} =	650	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference	e to 25 °C, I _D = 1 mA	-	0.88	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μΑ	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 20 V	-	-	± 100	nA
Zaus Osta Valta es Dusis Ouwest	I _{DSS}	V _{DS} =	-	-	100		
Zero Gate Voltage Drain Current		V _{DS} = 480 V	′, V _{GS} = 0 V, T _J = 125 °C	-	-	500	μA
Drain-Source On-State Resistance	R _{DS(on)}	$V_{GS} = 10 V$	I _D = 1.2 A ^b		8	-	Ω
Forward Transconductance	9 _{fs}	V _{DS} :	= 50 V, I _D = 1.2 A	1.4	-	-	S
Dynamic					•	•	
Input Capacitance	C _{iss}		$V_{GS} = 0 V$,	-	350	-	pF
Output Capacitance	Coss		$V_{\rm DS} = -25 \rm V,$	-	48	-	
Reverse Transfer Capacitance	C _{rss}	f = 1	.0 MHz, see fig. 5	-	8.6	-	
Total Gate Charge	Qg				-	18	1
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 V$	$V_{GS} = 10 \text{ V}$ $I_D = 2.0 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13^{b}			3.0	nC
Gate-Drain Charge	Q _{gd}		see ng. o and ro	-	-	8.9	1
Turn-On Delay Time	t _{d(on)}				10	-	- ns
Rise Time	t _r	V_{DD} = 300 V, I_D = 2.0 A, R_g = 18 $\Omega,~R_D$ = 135 $\Omega,$ see fig. 10^b		-	23	-	
Turn-Off Delay Time	t _{d(off)}			-	30	-	
Fall Time	t _f			-	25	-	1
Internal Drain Inductance	L _D	6 mm (0.25") f	Between lead, 6 mm (0.25") from			-	- 1
Internal Source Inductance	LS	package and die contact	center of	-	7.5	-	- nH
Drain-Source Body Diode Characteristic	S						
Continuous Source-Drain Diode Current	I _S	showing the	MOSFET symbol showing the		-	2.0	A
Pulsed Diode Forward Current ^a	I _{SM}	integral revers p - n junction	-	-	8.0		
Body Diode Voltage	V_{SD}	T _J = 25 °C	-	-	1.6	V	
Body Diode Reverse Recovery Time	t _{rr}	– T _J = 25 °C, I _F	-	290	580	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	$I_{\rm J} = 25$ C, $I_{\rm F}$	-	0.67	1.3	μC	
Forward Turn-On Time	t _{on}	Intrinsic tu	rn-on time is negligible (turn	-on is dor	ninated b	$v L_s$ and	L _D)

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. Pulse width \leq 300 µs; duty cycle \leq 2 %.





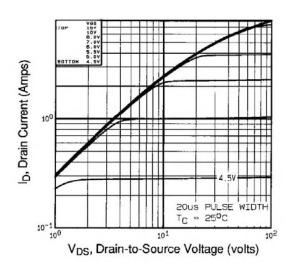


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

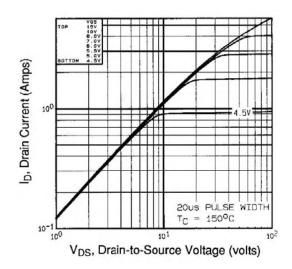


Fig. 2 - Typical Output Characteristics, $T_C = 150 \ ^{\circ}C$

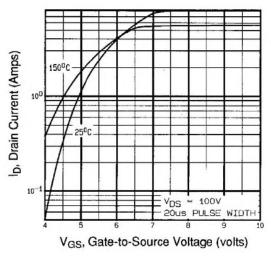


Fig. 3 - Typical Transfer Characteristics

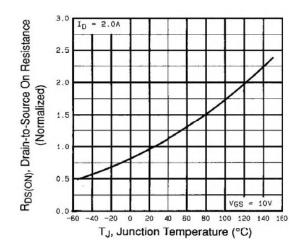


Fig. 4 - Normalized On-Resistance vs. Temperature



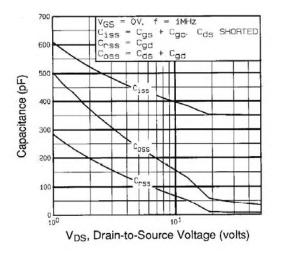
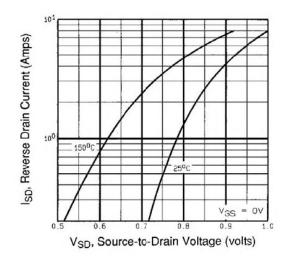


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage





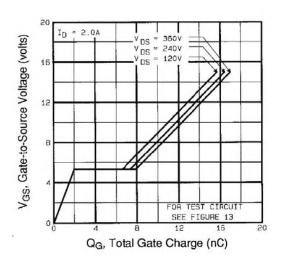


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

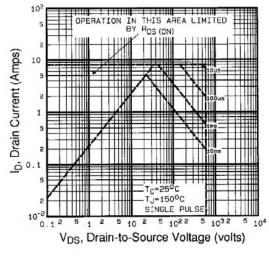


Fig. 8 - Maximum Safe Operating Area



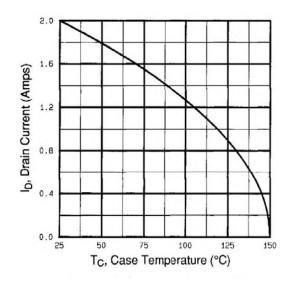


Fig. 9 - Maximum Drain Current vs. Case Temperature

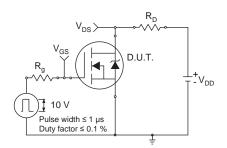


Fig. 10a - Switching Time Test Circuit

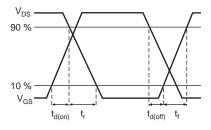
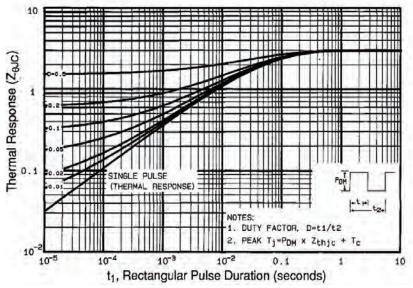


Fig. 10b - Switching Time Waveforms







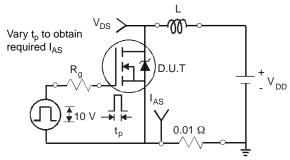


Fig. 12a - Unclamped Inductive Test Circuit

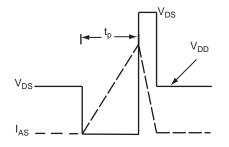


Fig. 12b - Unclamped Inductive Waveforms

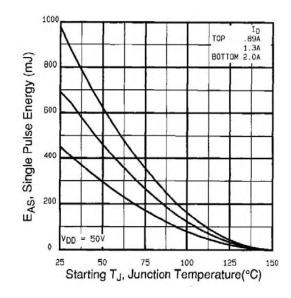


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

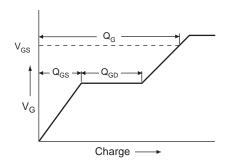


Fig. 13a - Basic Gate Charge Waveform

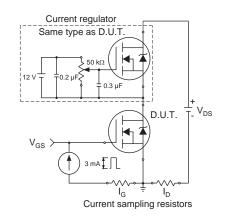
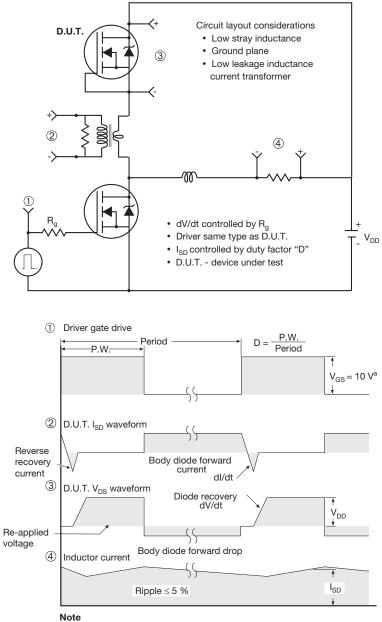


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit

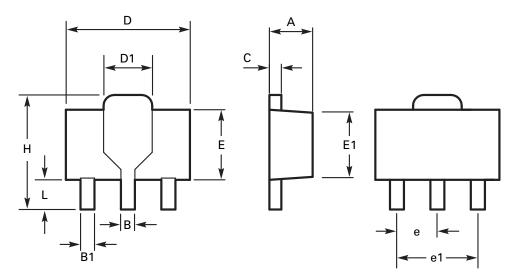


a. $V_{GS} = 5 \text{ V}$ for logic level devices

Fig. 14 - For N-Channel



Package outline - SOT89



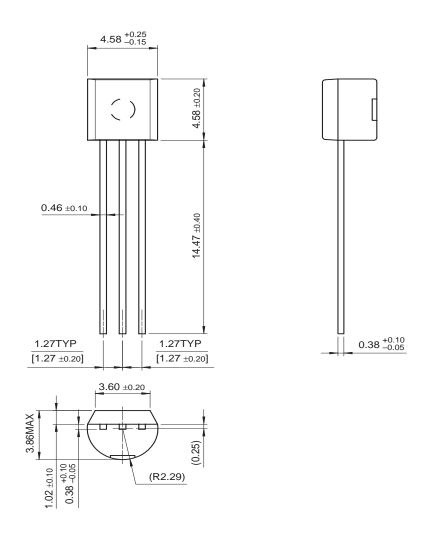
DIM	Millim	Millimeters		rs Inches		Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



Mechanical Dimensions

TO-92



Dimensions in Millimeters



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.