

## Dual N-Channel 60 V (D-S) MOSFET

### PRODUCT SUMMARY

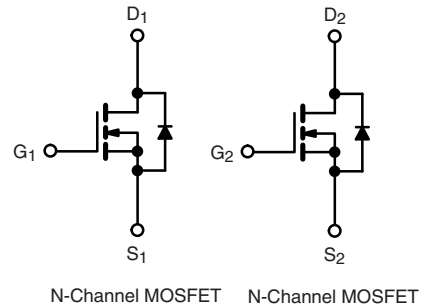
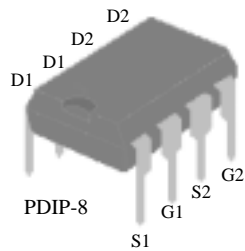
$V_{DS}$ (V)	60
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 10$ V	0.033
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5$ V	0.045
$I_D$ (A) per leg	7
Configuration	Dual

### FEATURES

- Trench power MOSFET
- 100 %  $R_g$  and UIS tested



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	7	A
		4	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	3.6	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	28	
Single Pulse Avalanche Current	$I_{AS}$	18	mJ
Single Pulse Avalanche Energy	$E_{AS}$	16.2	
Maximum Power Dissipation <sup>b</sup>	$P_D$	4	W
		1.3	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +175	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	110	$^\circ\text{C/W}$
Junction-to-Foot (Drain)	$R_{thJF}$	34	

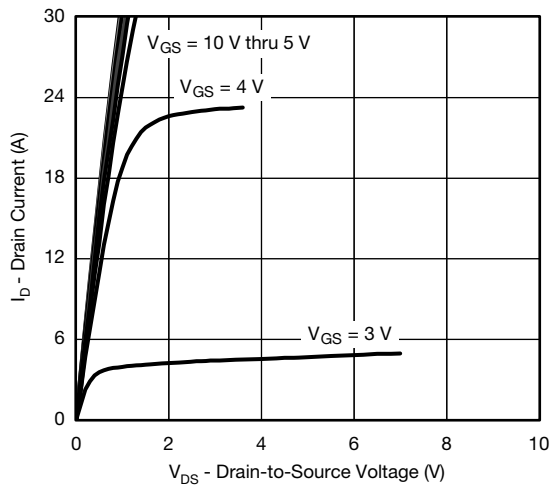
#### Notes

- Package limited.
- Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .
- When mounted on 1" square PCB (FR4 material).

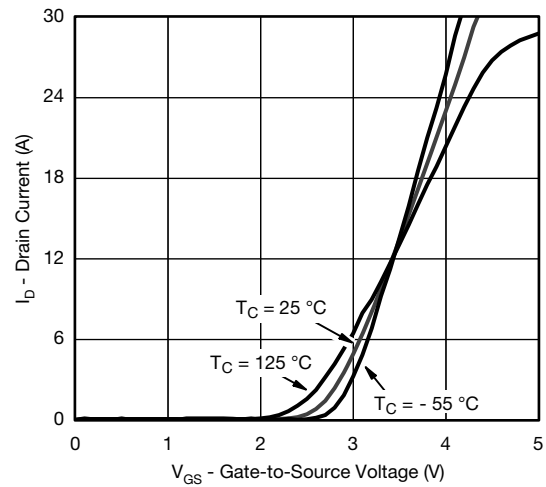
SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		60	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		1.5	2.0	2.5	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V	-	-	1	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≥ 5 V	20	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 4.5 A	-	0.033	-	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 4.5 A, T <sub>J</sub> = 125 °C	-	0.066	-	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 4.5 A, T <sub>J</sub> = 175 °C	-	0.081	-	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 4 A	-	0.045	-	
Forward Transconductance <sup>f</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4.5 A		-	15	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 25 V, f = 1 MHz	-	600	750	pF
Output Capacitance	C <sub>oss</sub>			-	110	140	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	50	62	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> = 30 V, I <sub>D</sub> = 5.3 A	-	11.7	18	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	1.8	2.7	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	2.8	4.2	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.3	-	6	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>L</sub> = 6.8 Ω I <sub>D</sub> ≅ 4.4 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		-	7	11	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			-	3.3	5	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	22.4	33.5	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	2.1	3.2	
Source-Drain Diode Ratings and Characteristics <sup>b</sup>							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	28	A
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 2 A, V <sub>GS</sub> = 0 V		-	0.75	1.1	V

**Notes**

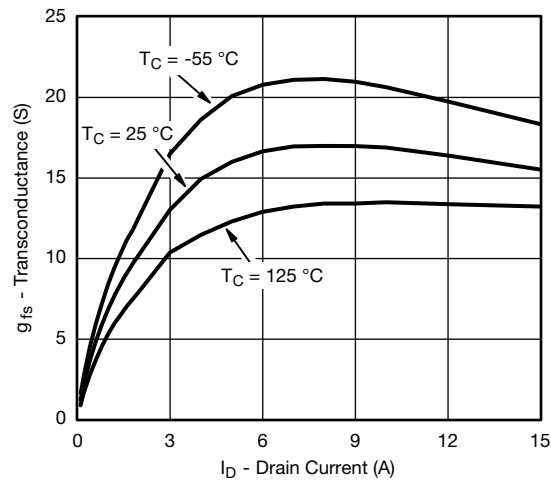
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

**TYPICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)


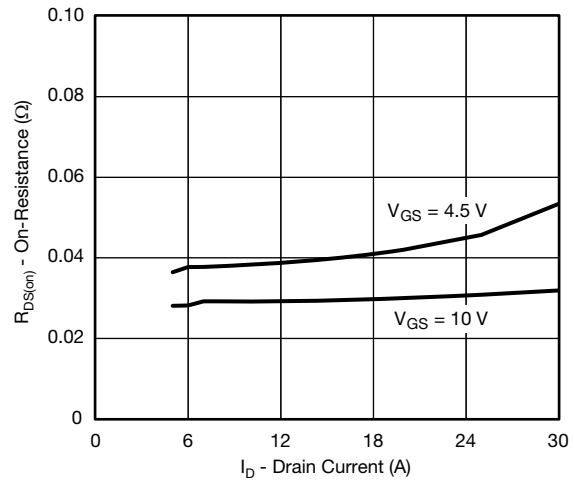
Output Characteristics



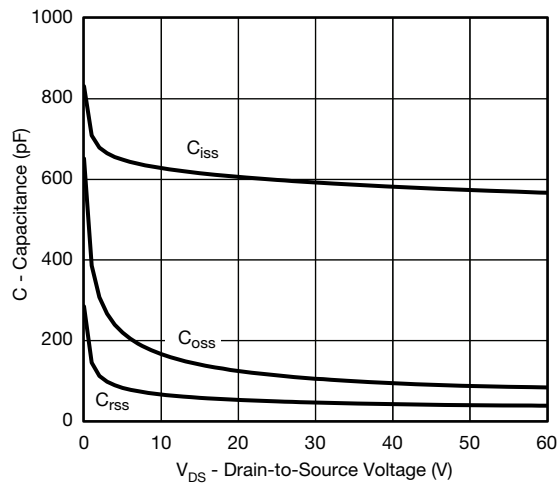
Transfer Characteristics



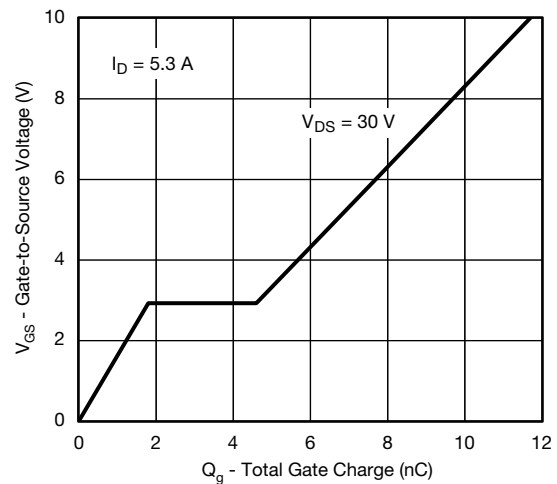
Transconductance



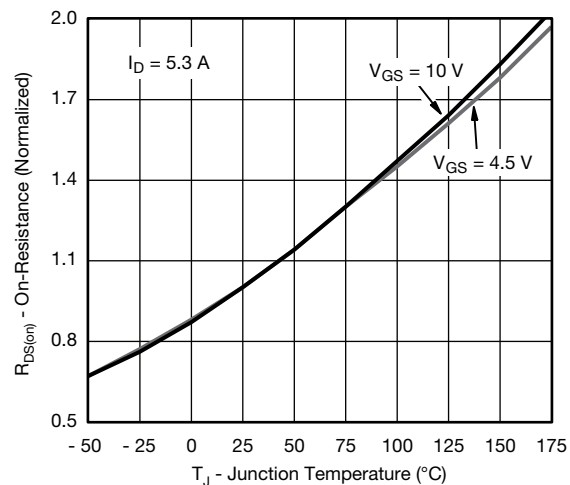
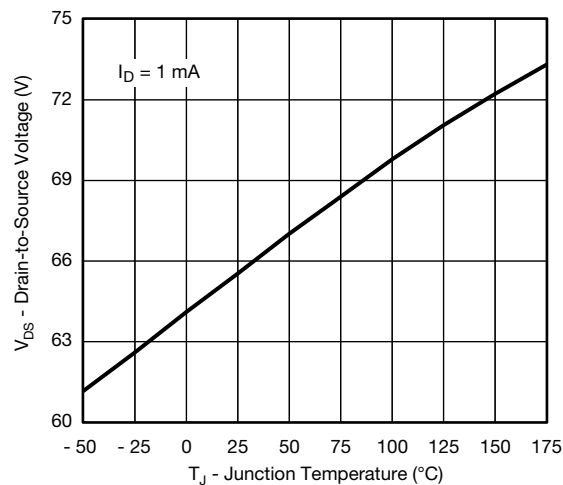
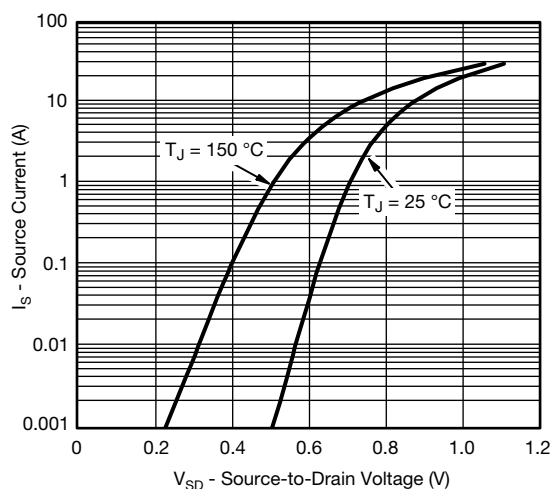
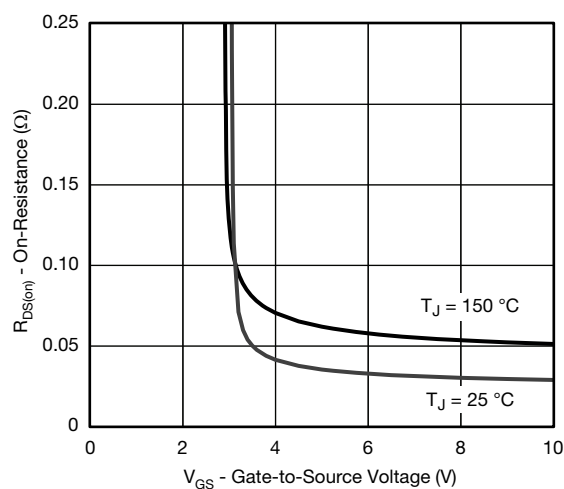
On-Resistance vs. Drain Current



Capacitance



Gate Charge

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

**On-Resistance vs. Junction Temperature**

**Drain Source Breakdown vs. Junction Temperature**

**Source Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Safe Operating Area**

$I_D$  - Drain Current (A)

$V_{DS}$  - Drain-to-Source Voltage (V)

$T_C = 25^\circ\text{C}$   
Single Pulse

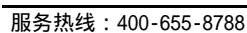
$I_{DM}$  Limited

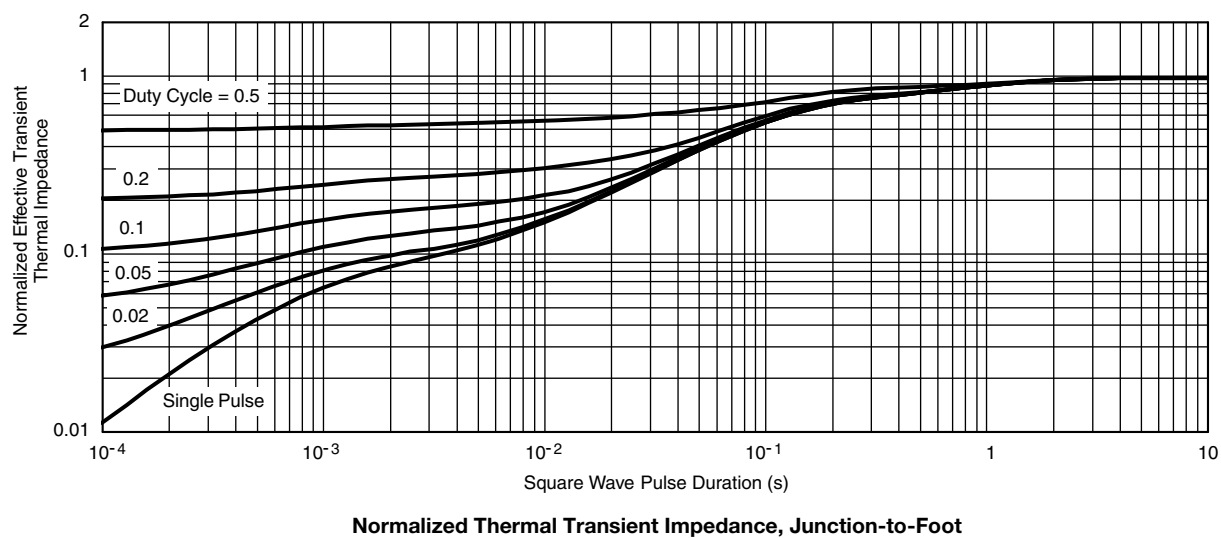
Limited by  $R_{DS(on)}$ \*

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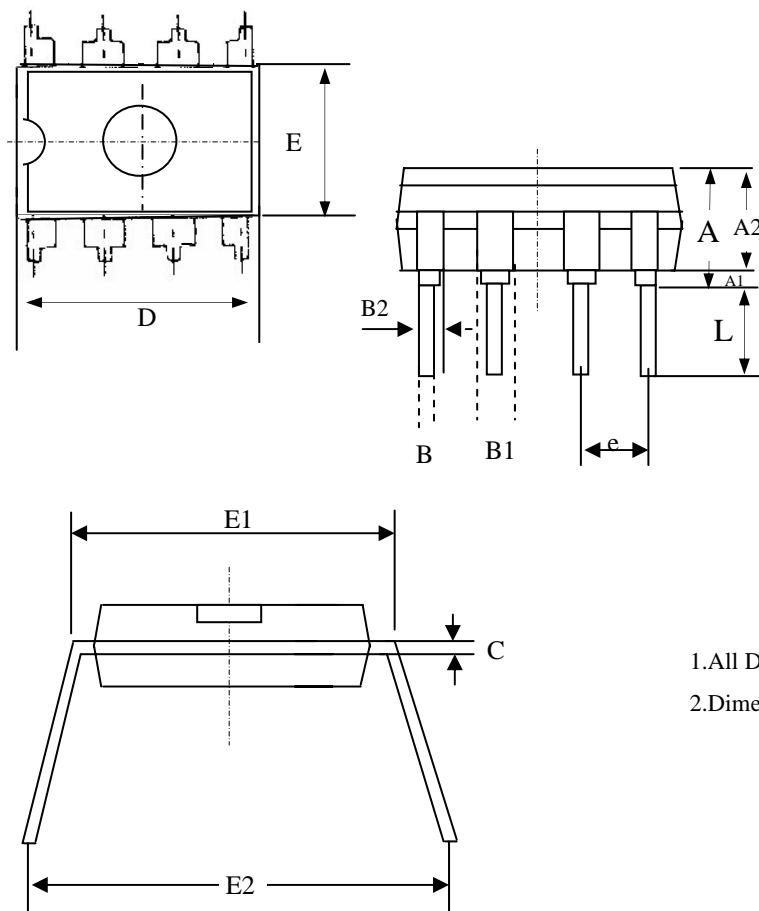
100  $\mu\text{s}$   
1 ms  
10 ms  
100 ms  
1 s  
10 s, DC

\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

## Package Outline : PDIP-8



SYMBOLS	Millimeters		
	MIN	NOM	MAX
<b>A</b>	3.60	4.50	5.40
<b>A1</b>	0.38	----	----
<b>A2</b>	2.90	3.95	5.00
<b>B</b>	0.36	0.46	0.56
<b>B1</b>	1.10	1.45	1.80
<b>B2</b>	0.76	0.98	1.20
<b>C</b>	0.20	0.28	0.36
<b>D</b>	9.00	9.60	10.20
<b>E</b>	6.10	6.65	7.20
<b>E1</b>	7.62	7.94	8.26
<b>E2</b>	8.30	9.65	11.00
<b>e</b>	2.540 BSC		
<b>L</b>	3.18	----	----

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

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