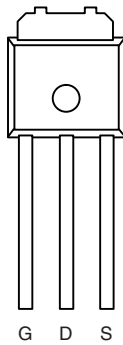


N-Channel 150 V (D-S) MOSFET

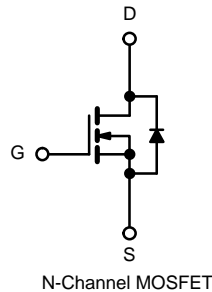
PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
150	0.060 at $V_{GS} = 10$ V	25.4	23 nC
	0.075 at $V_{GS} = 8$ V	22.5	

TO-251



Top View

Drain Connected to
Drain-Tab

N-Channel MOSFET

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Extremely Low Q_{gd} for Switching Losses
- 100 % R_g Tested
- 100 % Avalanche Tested
- Compliant to RoHS Directive 2002/95/EC


RoHS
 COMPLIANT
 HALOGEN
FREE
 Available

APPLICATIONS

- Primary Side Switch

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	I_D	25.4	A
		23.1	
		15.5 ^{b, c}	
		14.5 ^{b, c}	
Pulsed Drain Current	I_{DM}	50	mJ
Continuous Source-Drain Diode Current	I_S	4.5	
		2.6 ^{b, c}	
Single Pulse Avalanche Current	I_{AS}	20	
Single Pulse Avalanche Energy	E_{AS}	20	W
Maximum Power Dissipation	P_D	5.9	
		3.8	
		3.1 ^{b, c}	
		2 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, f}	R_{thJA}	33	40	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	R_{thJF}	17	21	

Notes:

- Based on $T_C = 25^\circ\text{C}$.
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$ s.
- Maximum under steady state conditions is 80°C/W .

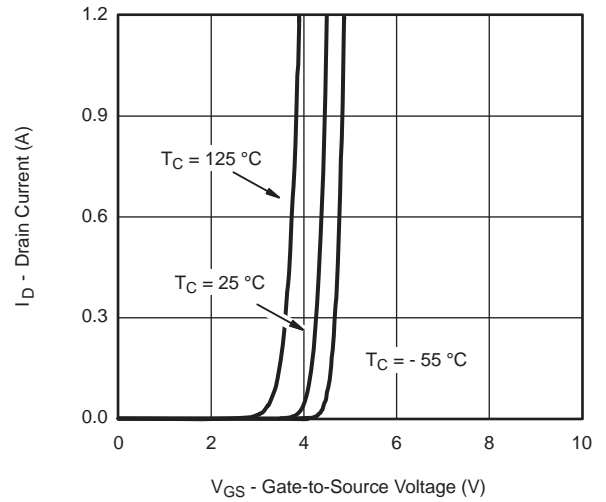
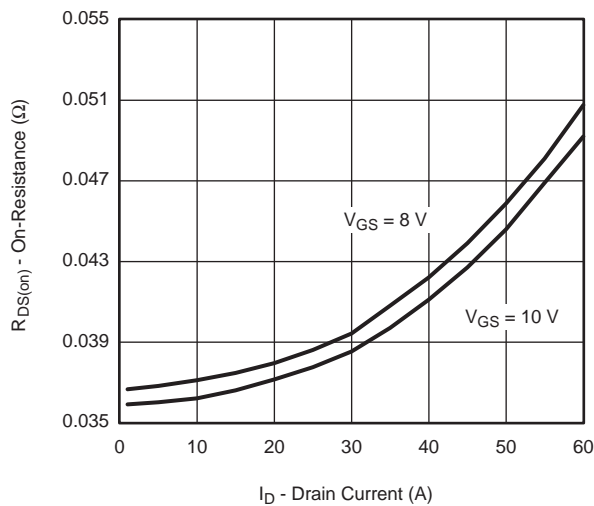
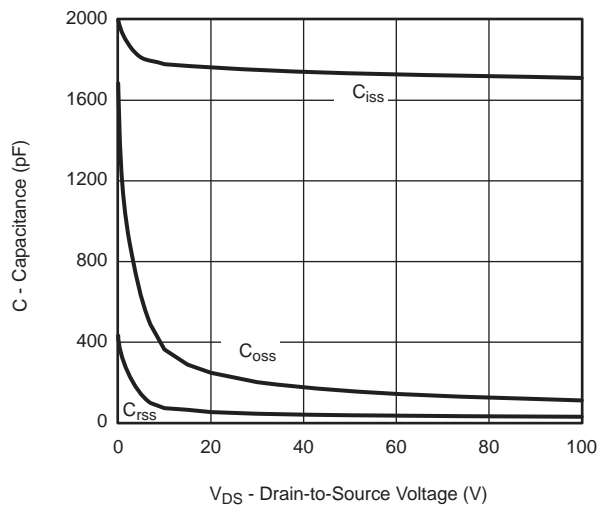
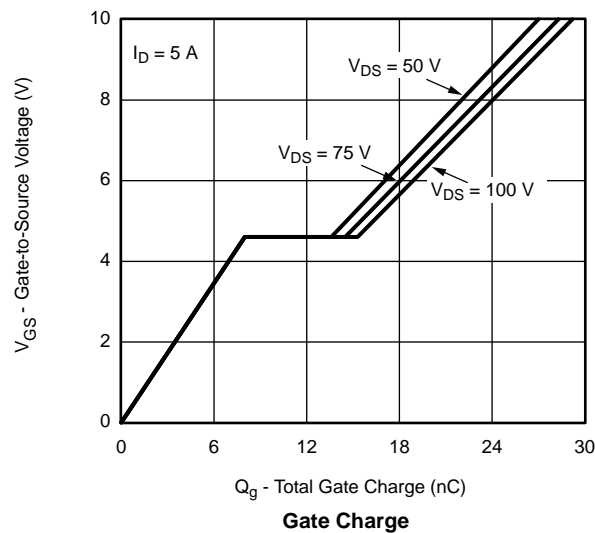
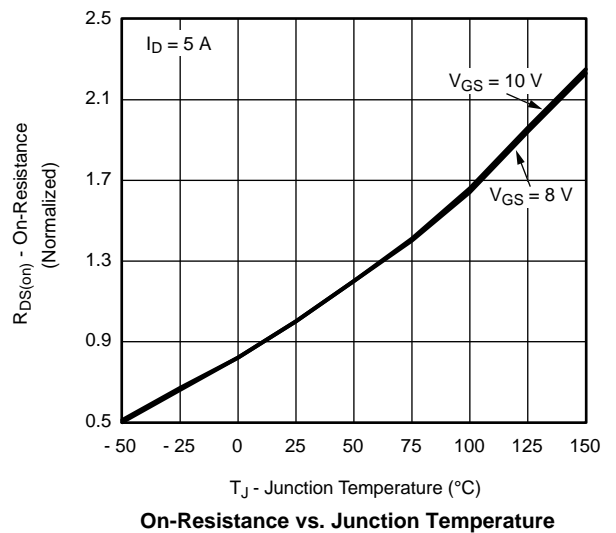
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	150			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		172		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 10		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.5		2.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A		0.060		Ω
		V _{GS} = 8 V, I _D = 5 A		0.075		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5 A		23		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		1735		pF
Output Capacitance	C _{oss}			160		
Reverse Transfer Capacitance	C _{rss}			37		
Total Gate Charge	Q _g	V _{DS} = 75 V, V _{GS} = 10 V, I _D = 5 A		28.5	43	nC
		V _{DS} = 75 V, V _{GS} = 8 V, I _D = 5 A		23	35	
Gate-Source Charge	Q _{gs}			8		
Gate-Drain Charge	Q _{gd}			6.5		
Gate Resistance	R _g	f = 1 MHz		0.85	1.3	Ω
Turn-on Delay Time	t _{d(on)}	V _{DD} = 50 V, R _L = 10 Ω I _D ≅ 5 A, V _{GEN} = 10 V, R _g = 1 Ω		14	21	ns
Rise Time	t _r			12	18	
Turn-Off Delay Time	t _{d(off)}			22	33	
Fall Time	t _f			6	10	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 50 V, R _L = 10 Ω I _D ≅ 5 A, V _{GEN} = 8 V, R _g = 1 Ω		16	24	
Rise Time	t _r			12	18	
Turn-Off Delay Time	t _{d(off)}			20	30	
Fall Time	t _f			7	12	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7.7	A
Pulse Diode Forward Current ^a	I _{SM}				50	
Body Diode Voltage	V _{SD}	I _S = 2.6 A		0.77	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 5 A, dI/dt = 100 A/μs, T _J = 25 °C		63	95	ns
Body Diode Reverse Recovery Charge	Q _{rr}			110	165	nC
Reverse Recovery Fall Time	t _a			49		ns
Reverse Recovery Rise Time	t _b			14		

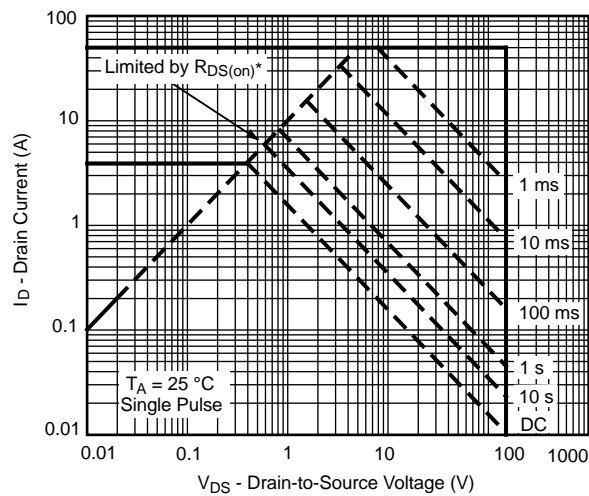
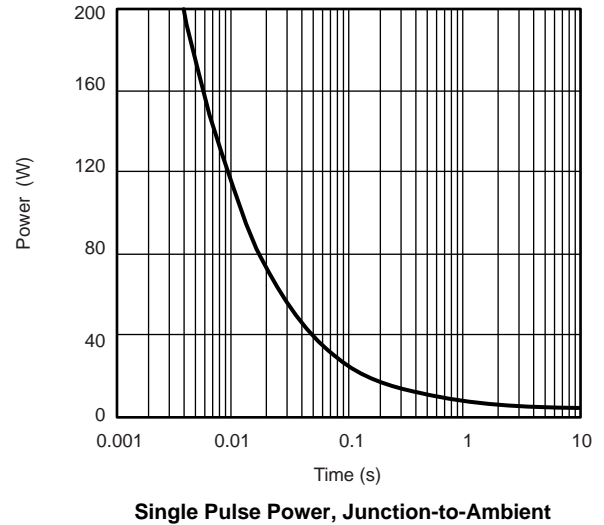
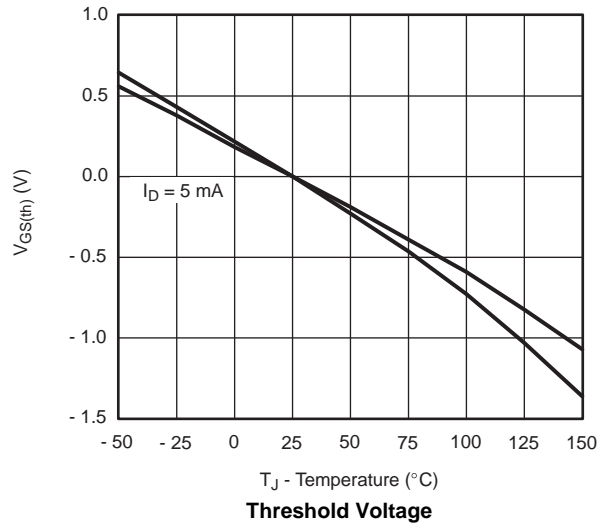
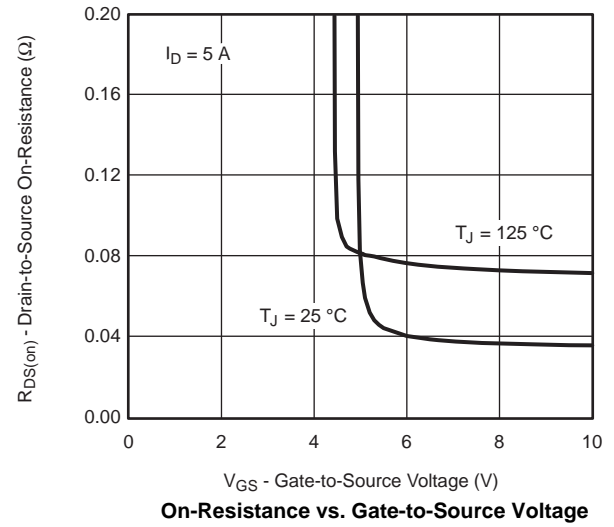
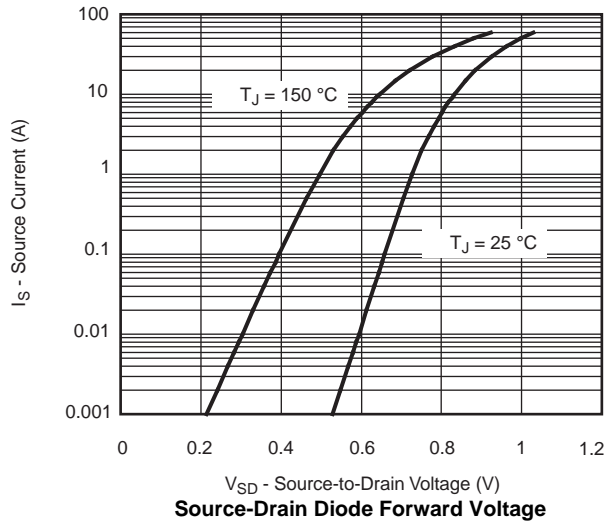
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
 a. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

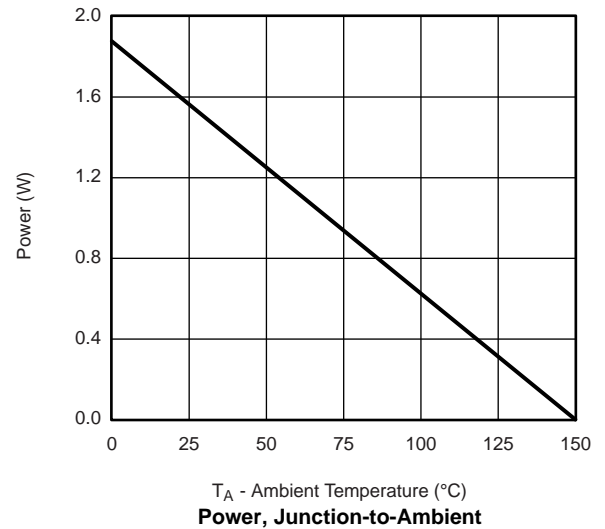
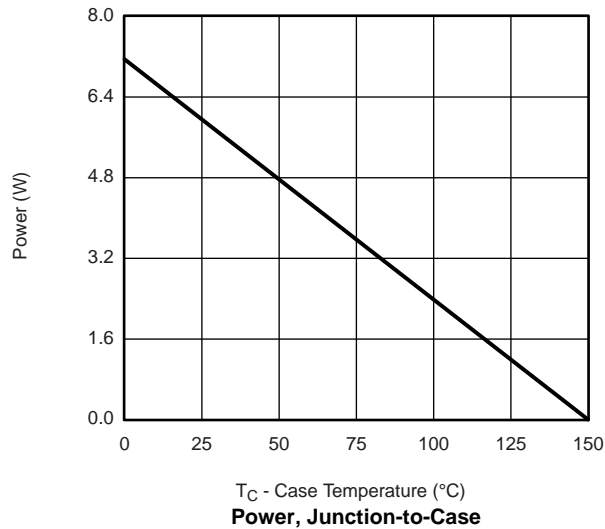
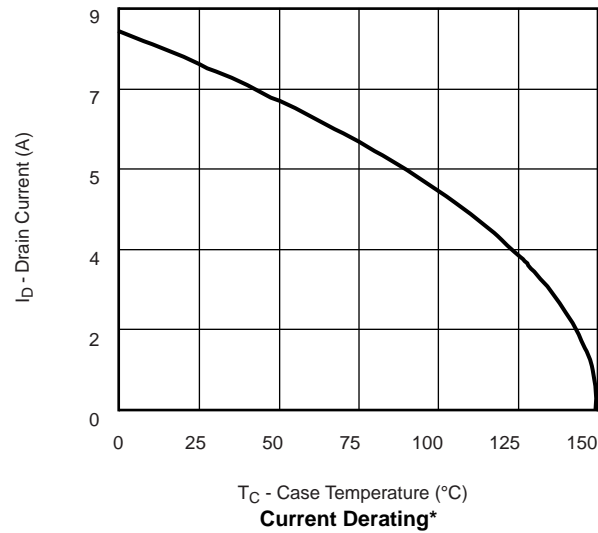
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current and Gate Voltage

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


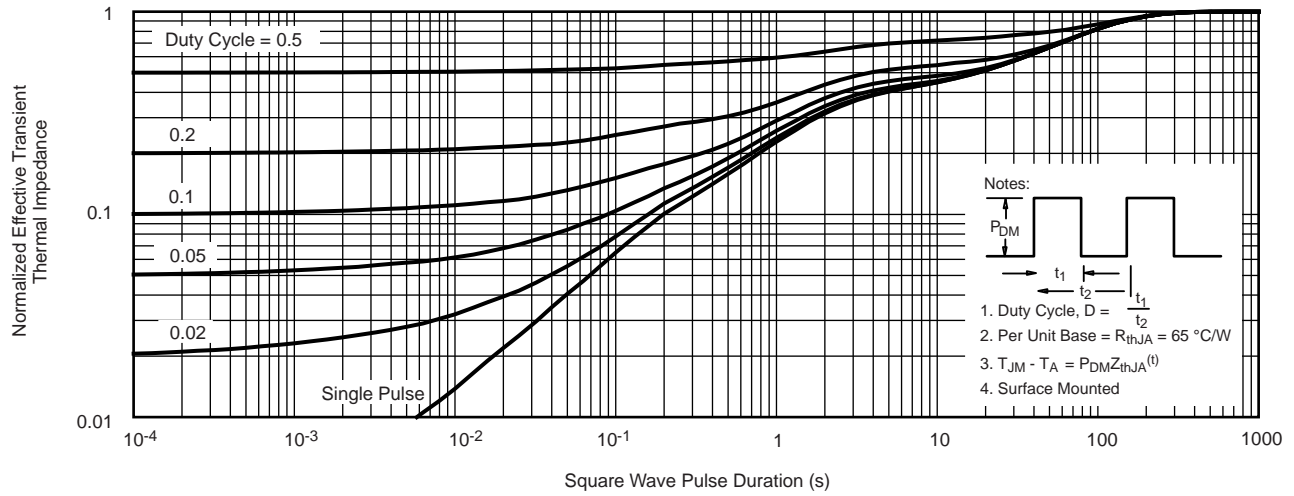
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

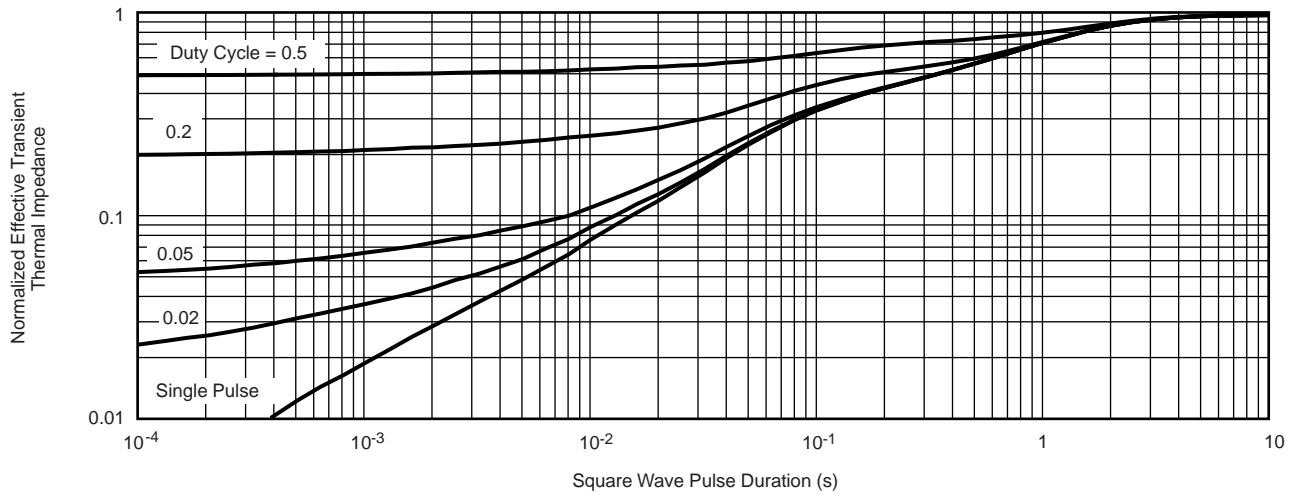
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

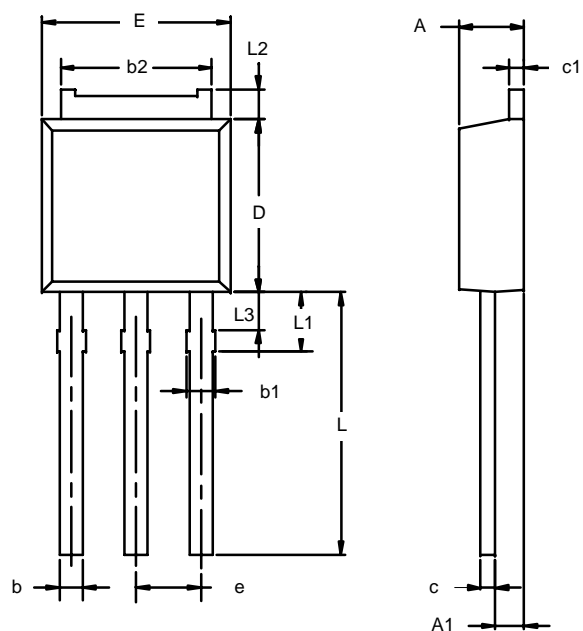


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

TO-251AA



Note: Dimension L3 is for reference only.

Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
c	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
E	6.48	6.73	0.255	0.265
e	2.28 BSC		0.090 BSC	
L	3.89	9.53	0.153	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060

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