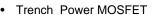
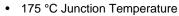


N-Channel 200 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	200				
R _{DS(on)} (Ω)	V _{GS} = 10 V	0.85			
Q _g (Max.) (nC)	13				
Q _{gs} (nC)	3.0				
Q _{gd} (nC)	7.9				
Configuration	Single				

FEATURES



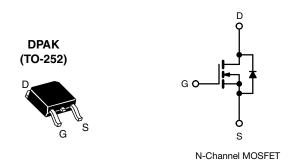


- **PWM Optimized**
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

· Primary Side Switch



PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	200	V	
Gate-Source Voltage			V _{GS}	± 20		
Continuous Drain Current	V _{GS} at 10 V	T _C = 25 °C T _C = 100 °C	I-	5.0	А	
Continuous Diain Guirent	VGS at 10 V	T _C = 100 °C	I _D	4.0		
Pulsed Drain Current ^a			I _{DM}	20		
Linear Derating Factor				0.33	W/°C	
Linear Derating Factor (PCB Mount) e		0.020) VV/ C			
Single Pulse Avalanche Energy b			E _{AS}	161	mJ	
Repetitive Avalanche Current a			I _{AR}	4.8	А	
Repetitive Avalanche Energy ^a			E _{AR}	4.2	mJ	
Maximum Power Dissipation	T _C = 25 °C		Б	42	W	
Maximum Power Dissipation (PCB mount) ^e	T _A = 25 °C		P _D	2.5	7 vv	
Peak Diode Recovery dV/dt ^c			dV/dt	5.0	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +150	- °C	
Soldering Recommendations (Peak temperature) d	for 10 s			260	7	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. $V_{DD}=50~V$, starting $T_J=25~^{\circ}C$, L=14~mH, $R_g=25~\Omega$, $I_{AS}=4.8~A$ (see fig. 12). c. $I_{SD}\leq5.2~A$, $I_{AS}=4.8~A$ (see fig. 12).

- d. 1.6 mm from case.
- e. When mounted on 1" square PCB (FR-4 or G-10 material).

服务热线:400-655-8788

1



THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	-	110	
Maximum Junction-to-Ambient (PCB mount) ^a	R _{thJA}	-	-	50	°C/W
Maximum Junction-to-Case (Drain)	R _{thJC}	-	-	3.0	

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					ı		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} :	= 0 V, I _D = 250 μA	200	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I _D = 1 mA	-	0.29	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		-	4.0	V
Gate-Source Leakage	I _{GSS}		$V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =	V _{DS} = 200 V, V _{GS} = 0 V			25	
		V _{DS} = 160 V	V _{DS} = 160 V, V _{GS} = 0 V, T _J = 125 °C		-	250	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 2.9 A ^b	-	0.85	-	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	V _{DS} = 50 V, I _D = 2.9 A ^b		-	-	S
Dynamic							
Input Capacitance	C_{iss}		$V_{GS} = 0 V$	-	185	-	
Output Capacitance	C _{oss}		$V_{DS} = 25 \text{ V},$	-	100	-	pF
Reverse Transfer Capacitance	C_{rss}	f = 1	.0 MHz, see fig. 5	-	30	-	
Total Gate Charge	Qg			-	-	13.0	
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 \text{ V}$	V_{GS} = 10 V I_{D} = 4.8 A, V_{DS} = 160 V, see fig. 6 and 13 b			3.0	nC
Gate-Drain Charge	Q _{gd}				-	7.9	
Turn-On Delay Time	t _{d(on)}			-	7.2	-	
Rise Time	t _r	V_{DD} = 100 V, I_D = 4.8 A, R_G = 18 Ω , R_D = 20 Ω , see fig. 10 b		-	22	-	ns
Turn-Off Delay Time	t _{d(off)}			-	19	-	
Fall Time	t _f			-	13	-	
Internal Drain Inductance	L _D	6 mm (0.25")	Between lead, 6 mm (0.25") from		4.5	-	nH
Internal Source Inductance	L _S	package and center of die contact		-	7.5	-) IIII
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	4.8	А
Pulsed Diode Forward Current ^a	I _{SM}			-	-	19	
Body Diode Voltage	V_{SD}	$T_J = 25 ^{\circ}\text{C}, I_S = 4.8 \text{A}, V_{GS} = 0 \text{V}^{ \text{b}}$		-		1.8	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 4.8 A, dl/dt = 100 A/µs b		-	150	300	ns
Body Diode Reverse Recovery Charge	Q _{rr}			-	0.91	1.8	μC
Forward Turn-On Time	t _{on}	Intrinsic tu	rn-on time is negligible (turn	-on is dor	ninated b	v Ls and	Ln)

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. Pulse width \leq 300 μs ; duty cycle \leq 2 %.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

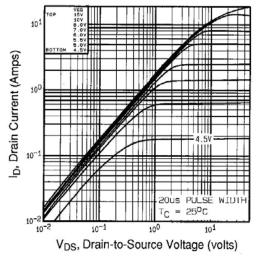


Fig. 1 - Typical Output Characteristics, $T_C = 25$ °C

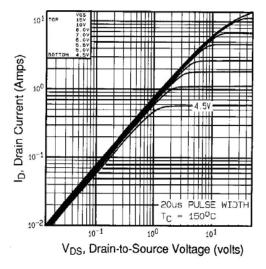


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

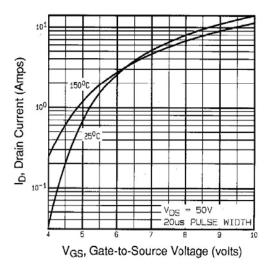


Fig. 3 - Typical Transfer Characteristics

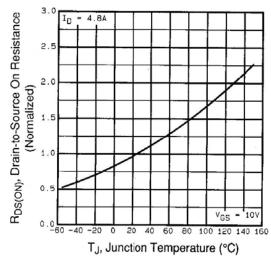


Fig. 4 - Normalized On-Resistance vs. Temperature



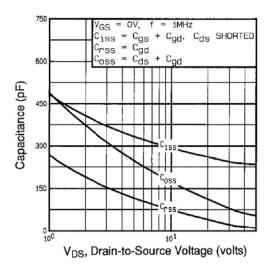


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

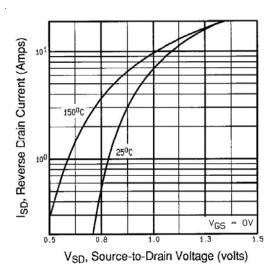


Fig. 7 - Typical Source-Drain Diode Forward Voltage

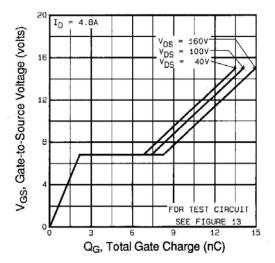


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

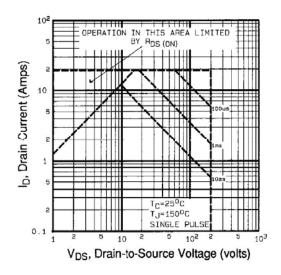


Fig. 8 - Maximum Safe Operating Area



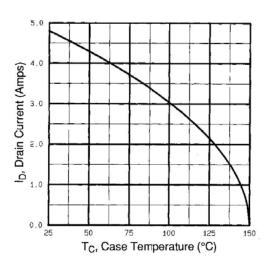


Fig. 9 - Maximum Drain Current vs. Case Temperature

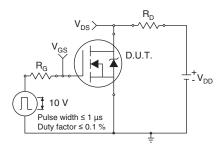


Fig. 10a - Switching Time Test Circuit

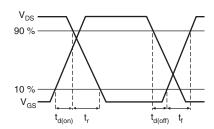


Fig. 10b - Switching Time Waveforms

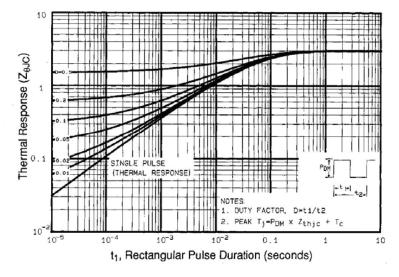


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



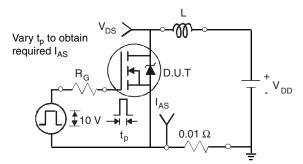


Fig. 12a - Unclamped Inductive Test Circuit

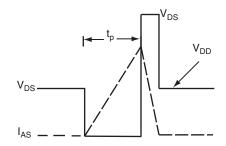


Fig. 12b - Unclamped Inductive Waveforms

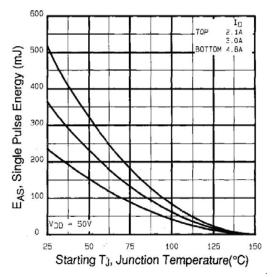


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

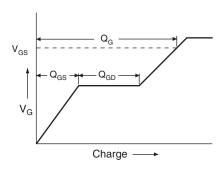


Fig. 13a - Basic Gate Charge Waveform

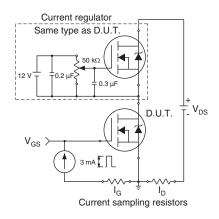
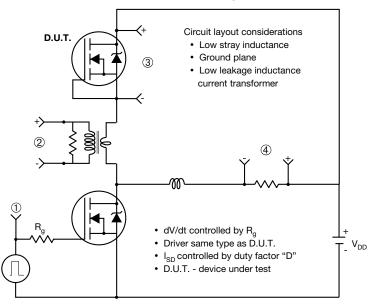


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



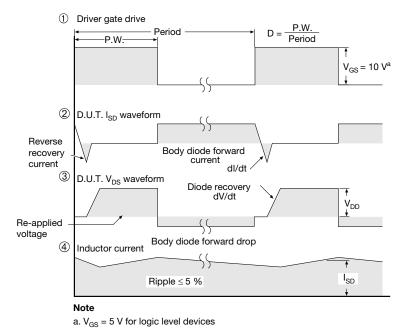
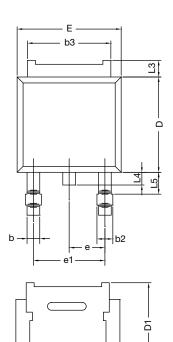


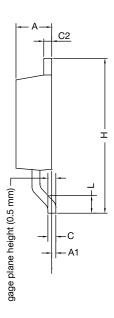
Fig. 14 - For N-Channel

服务热线:400-655-8788 7



TO-252AA Case Outline





	MILLIN	METERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
ECN: T16-0236-Rev. P, 16-May-16					

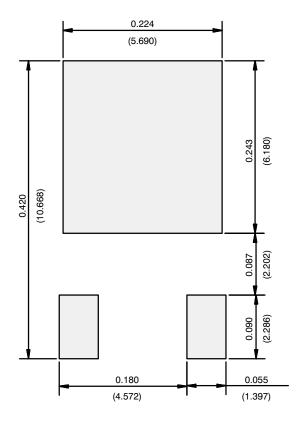
DWG: 5347

Notes

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.