

## Dual N-Channel 20 V MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
20	0.0038 at $V_{GS} = 10$ V	19.8 <sup>a</sup>	14.5
	0.0047 at $V_{GS} = 4.5$ V	17.3 <sup>a</sup>	

### FEATURES

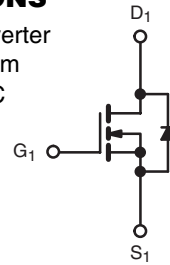
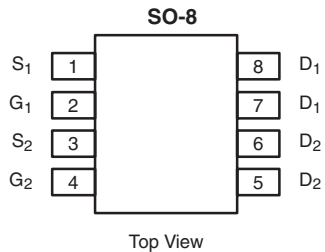
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 %  $R_g$  Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



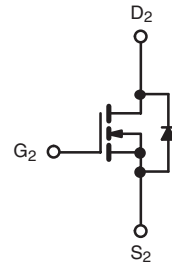
**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- DC/DC Converter
- Fixed Telecom
- Notebook PC



N-Channel MOSFET



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		$V_{DS}$	20	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150\text{ }^{\circ}\text{C}$ )	$T_C = 25\text{ }^{\circ}\text{C}$	$I_D$	19.8	A
	$T_C = 70\text{ }^{\circ}\text{C}$		15.9	
	$T_A = 25\text{ }^{\circ}\text{C}$		15.5 <sup>b, c</sup>	
	$T_A = 70\text{ }^{\circ}\text{C}$		12.2 <sup>b, c</sup>	
Pulsed Drain Current (10 $\mu\text{s}$ Pulse Width)		$I_{DM}$	50	
Source-Drain Current Diode Current	$T_C = 25\text{ }^{\circ}\text{C}$	$I_S$	2.7	
	$T_A = 25\text{ }^{\circ}\text{C}$		1.6 <sup>b, c</sup>	
Pulsed Source-Drain Current		$I_{SM}$	50	
Single Pulse Avalanche Current	L = 0.1 mH	$I_{AS}$	20	
Single Pulse Avalanche Energy		$E_{AS}$	20	
Maximum Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	$P_D$	3.25	W
	$T_C = 70\text{ }^{\circ}\text{C}$		2.10	
	$T_A = 25\text{ }^{\circ}\text{C}$		2.0 <sup>b, c</sup>	
	$T_A = 70\text{ }^{\circ}\text{C}$		1.25 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 150	$^{\circ}\text{C}$

### THERMAL RESISTANCE RATINGS

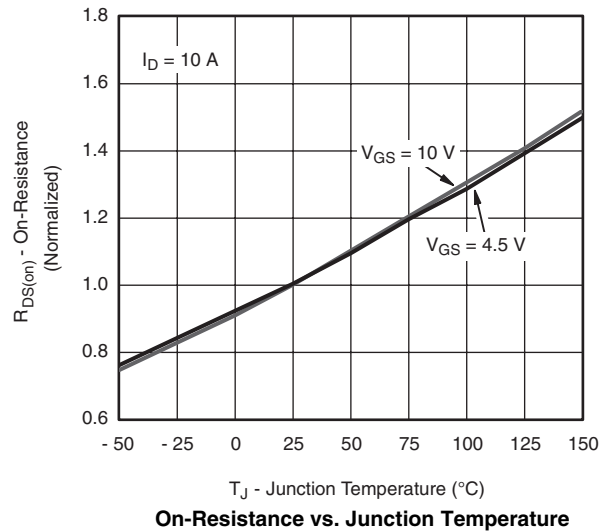
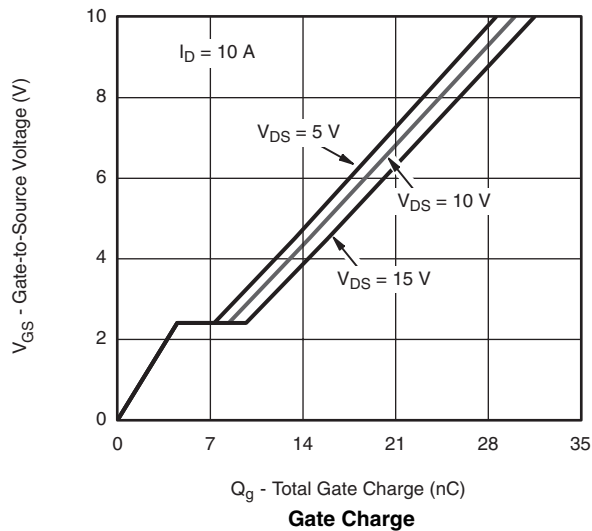
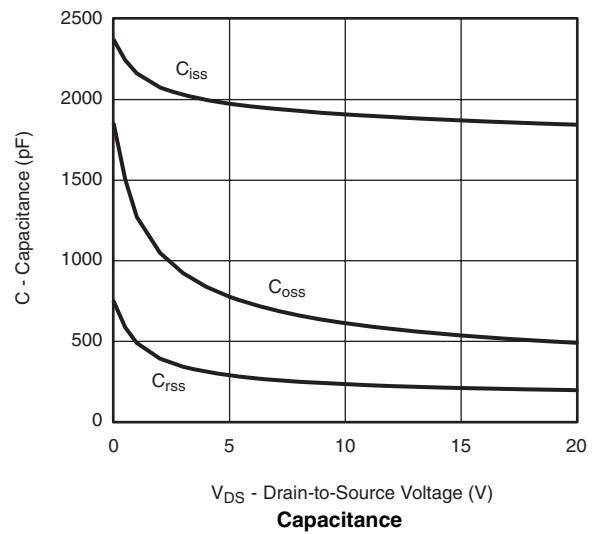
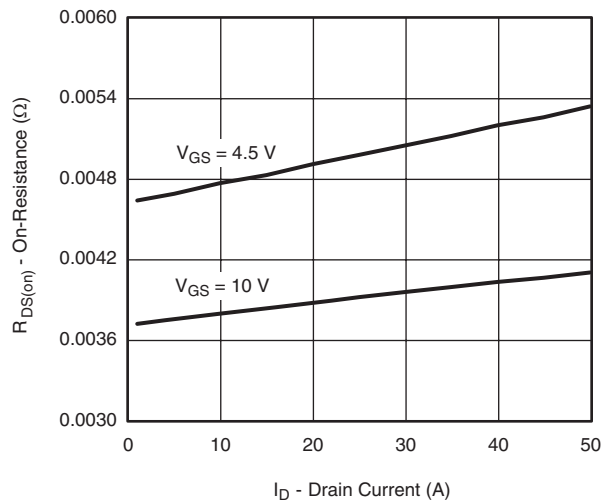
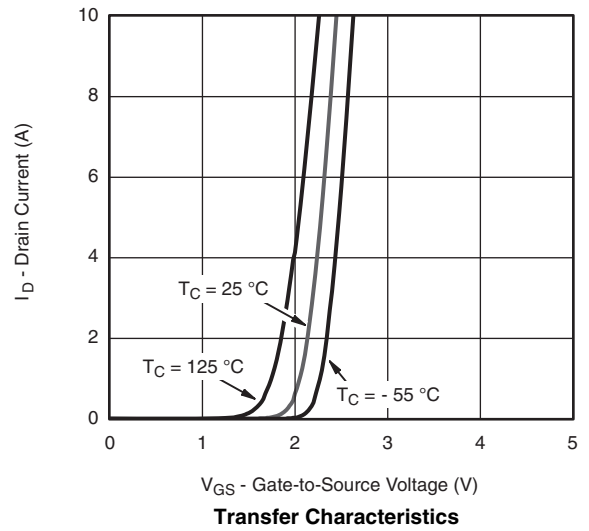
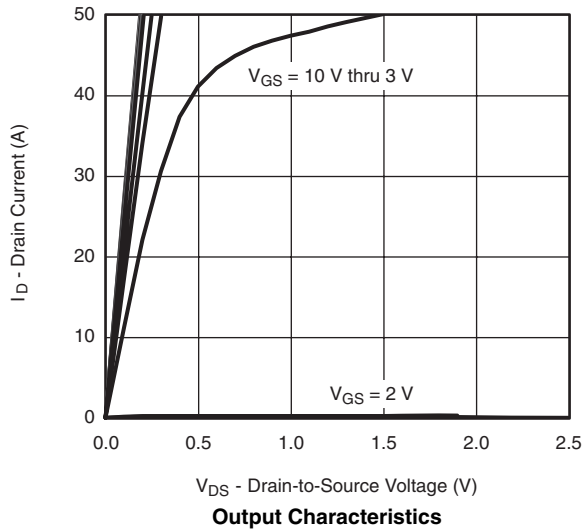
Parameter	Symbol	Typ.	Max.	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	45	62.5	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	29	38	

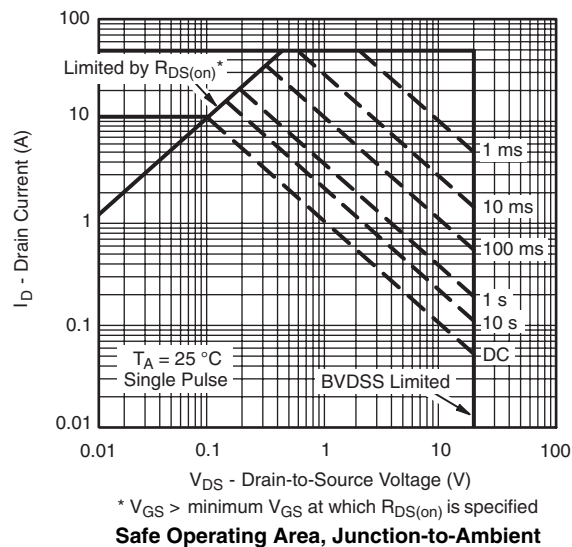
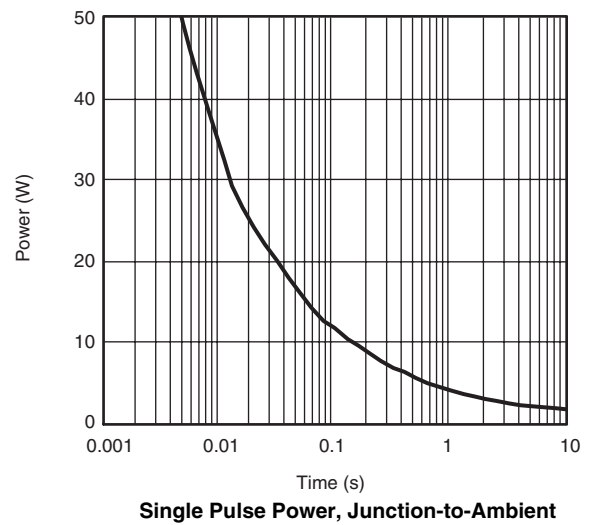
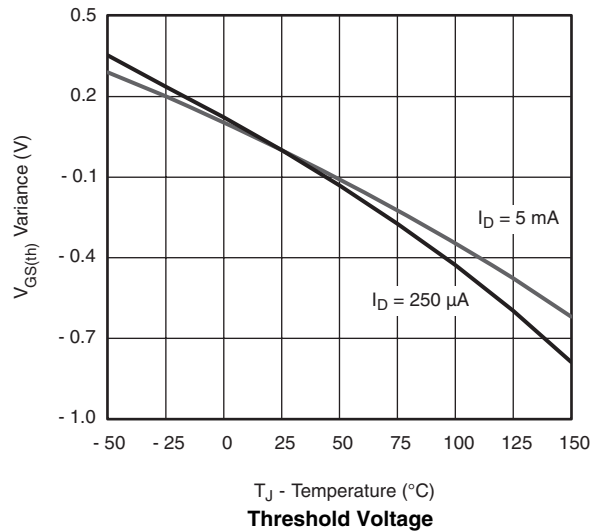
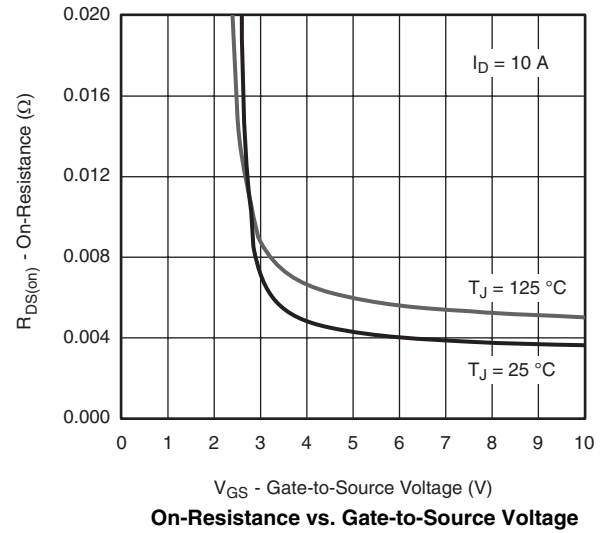
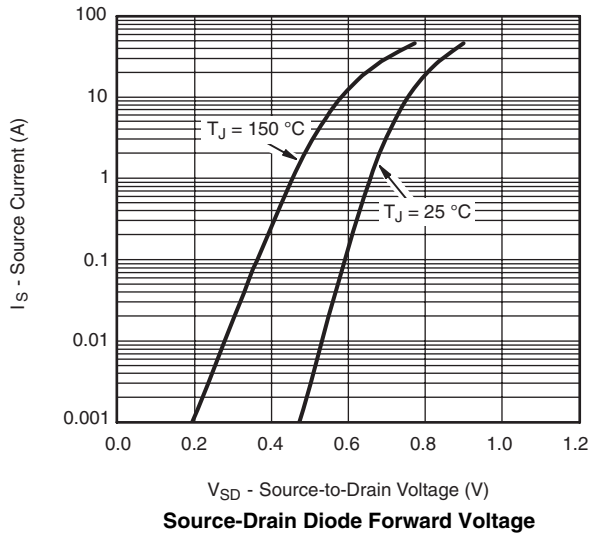
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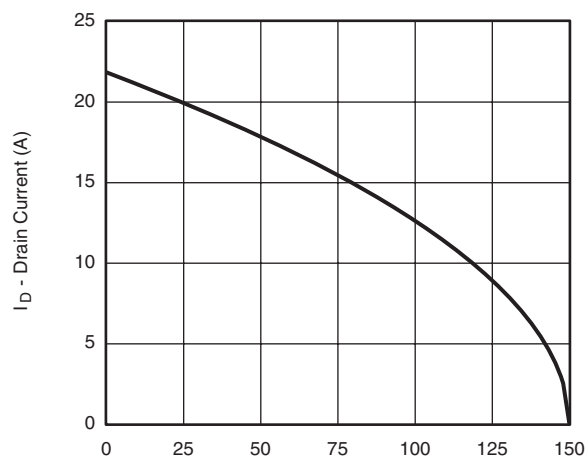
- Based on  $T_C = 25^\circ\text{C}$ .
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- Maximum under steady state conditions is  $120^\circ\text{C/W}$ .

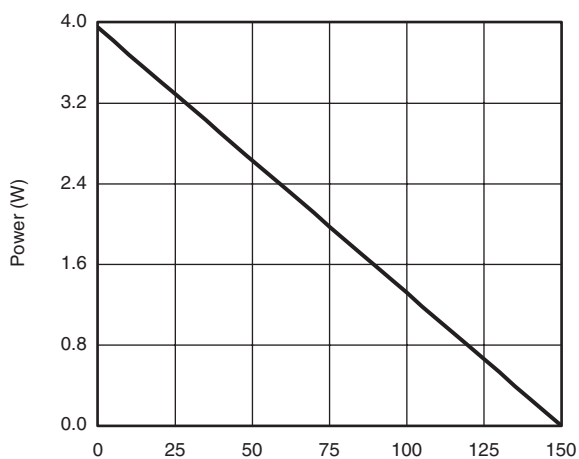
SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	20			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		20		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		- 5.8		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.0		2.4	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	20			A
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		0.0038		Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 8 A		0.0047		
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		50		S
Dynamic <sup>a</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 MHz		2110		pF
Output Capacitance	C <sub>oss</sub>			926		
Reverse Transfer Capacitance	C <sub>rss</sub>			235		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		30	45	nC
		V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A		14.5	22	
Gate-Source Charge	Q <sub>gs</sub>			4.5		
Gate-Drain Charge	Q <sub>gd</sub>			3.9		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	1.4	2.8	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 1 Ω I <sub>D</sub> ≅ 10 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		8	16	ns
Rise Time	t <sub>r</sub>			15	30	
Turn-Off Delay Time	t <sub>d(off)</sub>			24	45	
Fall Time	t <sub>f</sub>			9	18	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 1 Ω I <sub>D</sub> ≅ 10 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω		18	35	
Rise Time	t <sub>r</sub>			24	45	
Turn-Off Delay Time	t <sub>d(off)</sub>			26	50	
Fall Time	t <sub>f</sub>			13	26	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			2.7	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				50	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 3 A		0.70	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	N-Channel I <sub>F</sub> = 10 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		20	40	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			10	20	nC
Reverse Recovery Fall Time	t <sub>a</sub>			11		nS
Reverse Recovery Rise Time	t <sub>b</sub>			9		

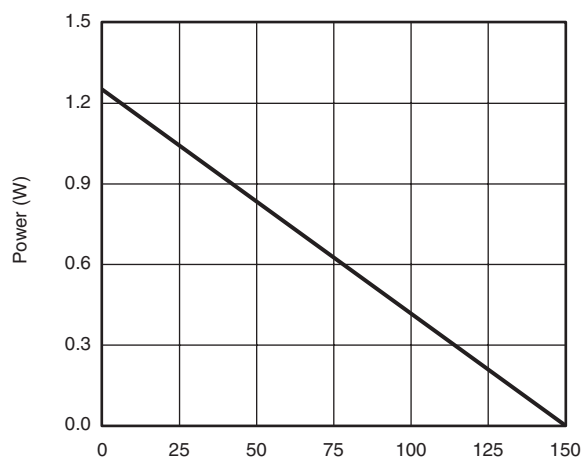
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


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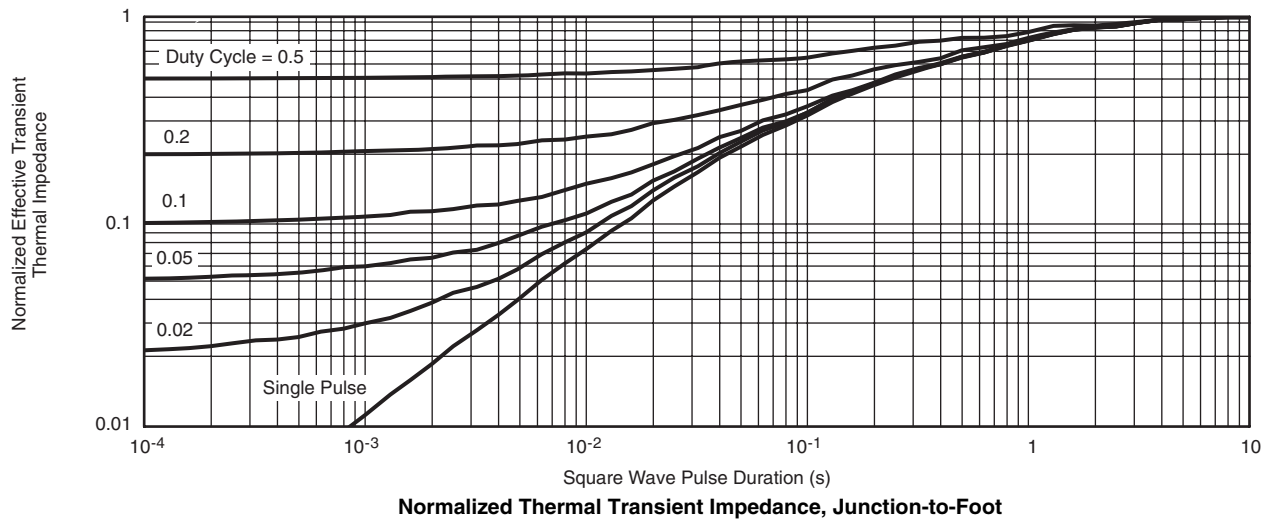
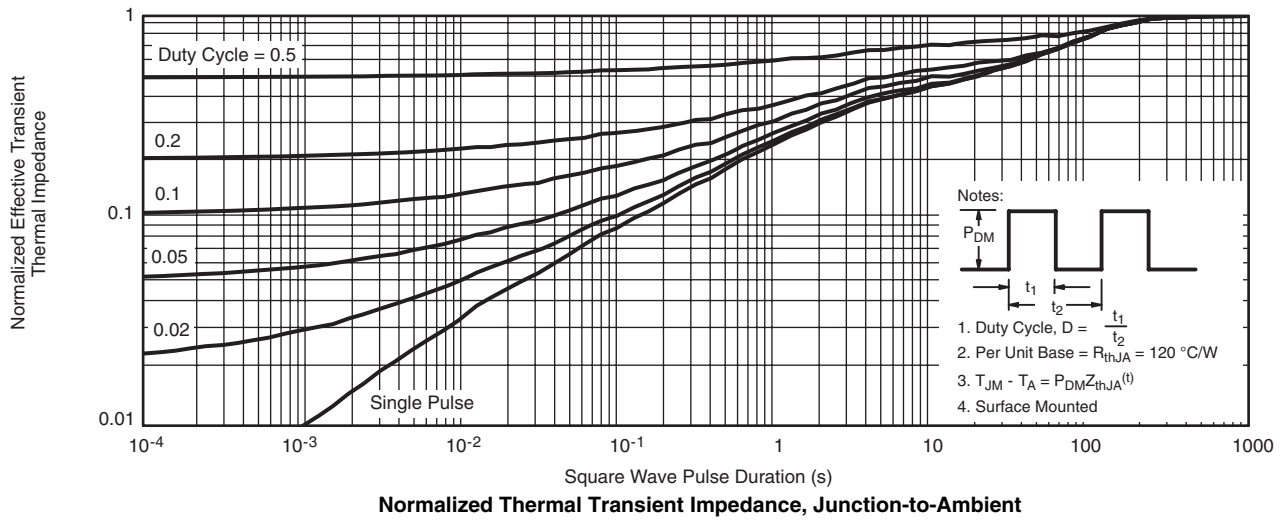
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

 $T_C$  - Case Temperature (°C)

**Current Derating\***

 $T_C$  - Case Temperature (°C)

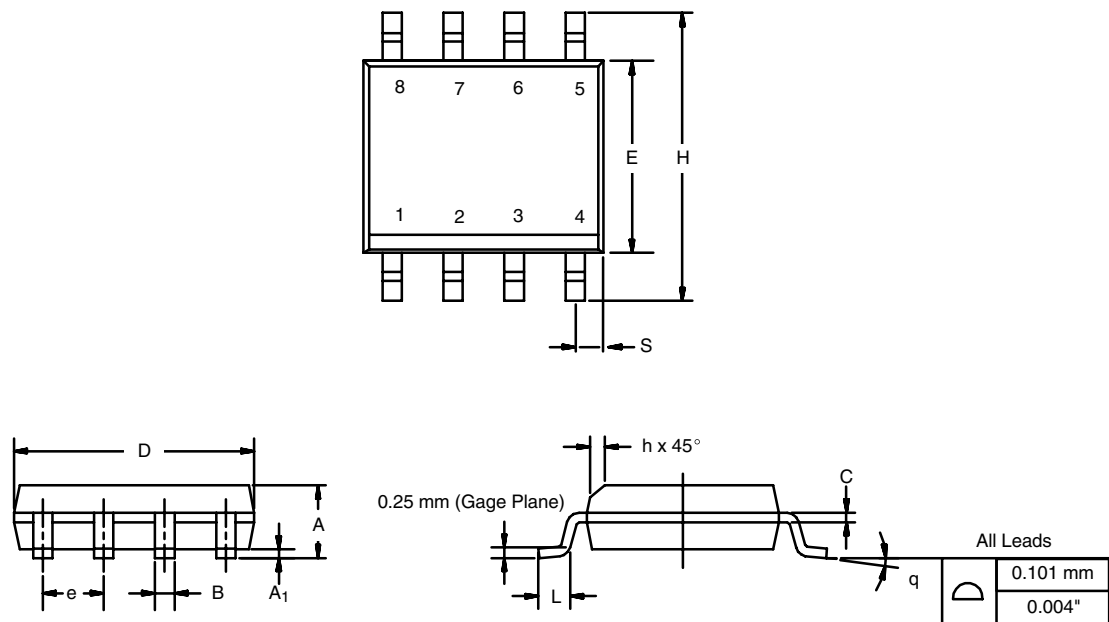
**Power Derating, Junction-to-Foot**

 $T_A$  - Ambient Temperature (°C)

**Power Derating, Junction-to-Ambient**

\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

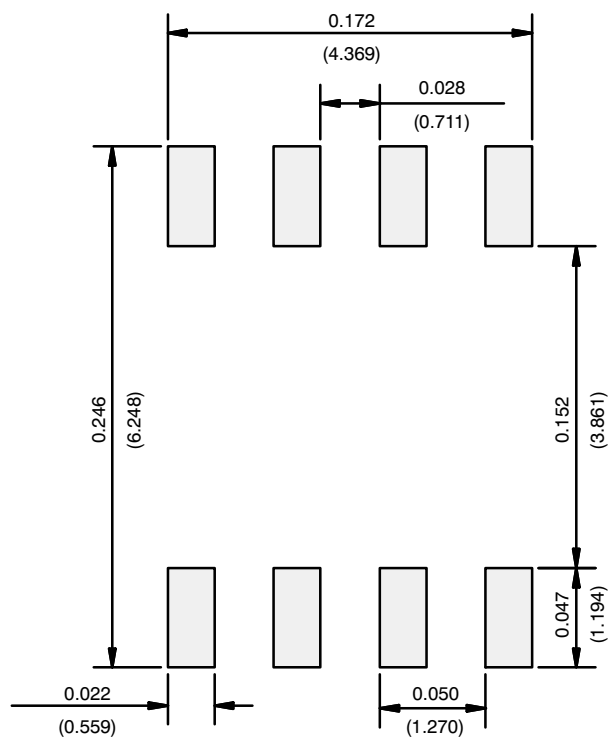
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


**SOIC (NARROW): 8-LEAD**  
JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads  
Dimensions in Inches/(mm)



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