

## MT6968-VB Datasheet

# **Dual N-Channel MOSFET**

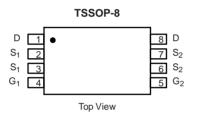
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
20	$0.014 \text{ at V}_{GS} = 4.5 \text{ V}$	7.6		
	0.018 at V <sub>GS</sub> = 2.5 V	6.5		

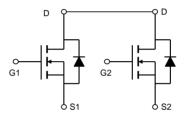
#### **FEATURES**

- Halogen-free Option Available
- Trench Power MOSFETs









ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 12			
Continuous Dusis Compant /T 450 90\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	7.6	6.2	•	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		6.5	4.5		
Pulsed Drain Current		I <sub>DM</sub>	30		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.5	1.0		
Maniana Dana Bissinstia 3	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.5	1.0	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.96	0.64		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Mariana kanting ta Andria (8	t ≤ 10 s	R <sub>thJA</sub>	72	83	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		100	120	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	55	70	

#### Notes:

a. Surface Mounted on FR4 board,  $t \le 10$  s.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply.



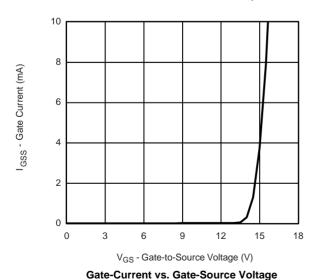
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Typ. <sup>a</sup>	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	0.6		1.6	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 200	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	IDSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			25	μA	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	30			Α	
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$		0.014		0	
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 5.5 A		0.018		Ω	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 6.5 \text{ A}$		30		S	
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$I_S = 1.5 A, V_{GS} = 0 V$		0.71	1.2	V	
Dynamic <sup>a</sup>							
Total Gate Charge	$Q_g$			12	18		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 6.5 \text{ A}$		2.2		nC	
Gate-Drain Charge	$Q_{gd}$			3.6			
Turn-On Delay Time	t <sub>d(on)</sub>			245	365		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		330	495	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 1 A, $V_{GEN}$ = 4.5 V, $R_G$ = 6 $\Omega$		860	1300		
Fall Time	t <sub>f</sub>			510	765		

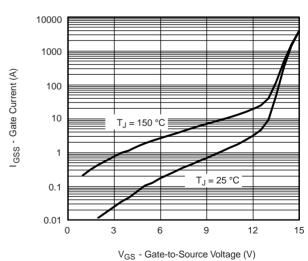
#### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

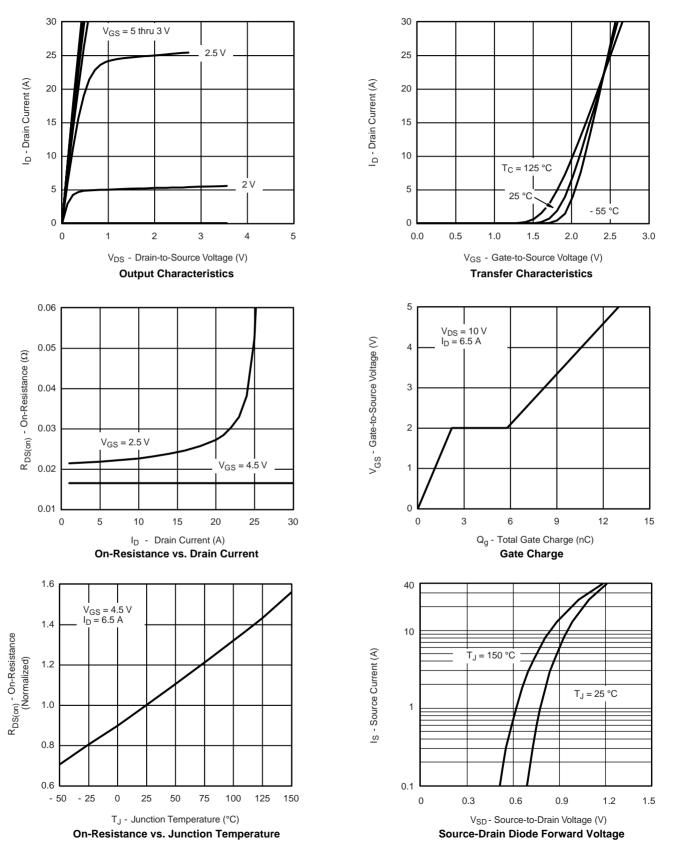




Gate Current vs. Gate-Source Voltage



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

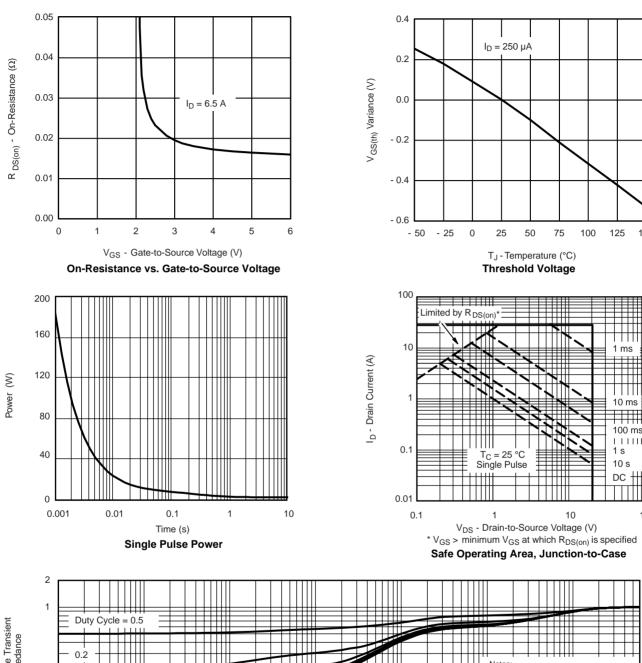


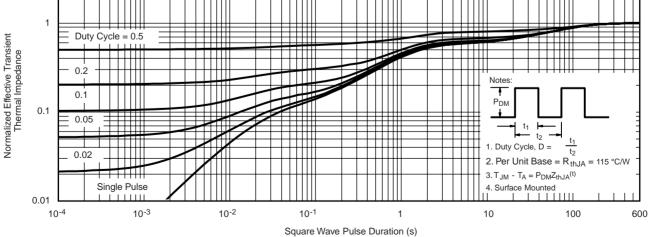


150

100

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

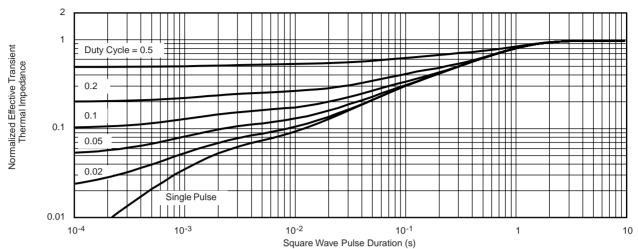




Normalized Thermal Transient Impedance, Junction-to-Ambient



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



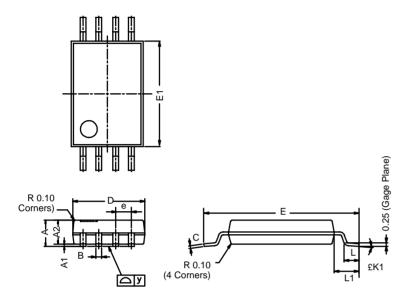
Normalized Thermal Transient Impedance, Junction-to-Foot



TSSOP: 8-LEAD

6

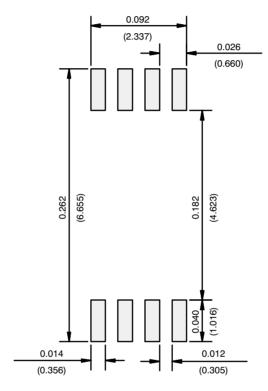
JEDEC Part Number: MO-153



	MILLIMETERS				
Dim	Min	Nom	Max		
Α	-	_	1.20		
A <sub>1</sub>	0.05	0.10	0.15		
A <sub>2</sub>	0.80	1.00	1.05		
В	0.19	0.28	0.30		
С	_	0.127	-		
D	2.90	3.00	3.10		
E	6.20	6.40	6.60		
E <sub>1</sub>	4.30	4.40	4.50		
е	_	0.65	-		
L	0.45	0.60	0.75		
L <sub>1</sub>	0.90	1.00	1.10		
Y	_	_	0.10		
£ <b>K1</b>	0°	3°	6°		
ECN: S-03946—Rev. G, 09-Jul-01 DWG: 5844					



#### **RECOMMENDED MINIMUM PADS FOR TSSOP-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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