

# IRF7756TRPBF-VB Datasheet

## **Dual P-Channel 30-V (D-S) MOSFET**

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
- 30	0.036 at V <sub>GS</sub> = - 10 V	- 5.2	
- 30	0.055 at V <sub>GS</sub> = - 4.5 V	- 4.2	

### **FEATURES**

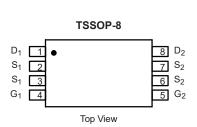
- · Halogen-free
- Trench Power MOSFETs

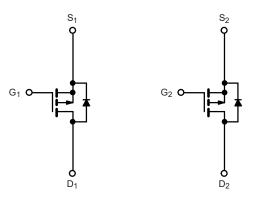


RoHS COMPLIANT

### **APPLICATIONS**

- · Load Switch
- · Battery Switch





P-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 30		V
Gate-Source Voltage		$V_{GS}$	± 20		V
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 5.2	- 4.1	
	T <sub>A</sub> = 70 °C		- 4.2	- 3.6	
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.0	- 0.70	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	Б	1.14	0.83	W
	T <sub>A</sub> = 70 °C	$P_{D}$	0.73	0.53	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55	to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mariana landina ta Andriada	t ≤ 10 s	R <sub>thJA</sub>	86	110	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		124	150	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	52	65	

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

服务热线:400-655-8788

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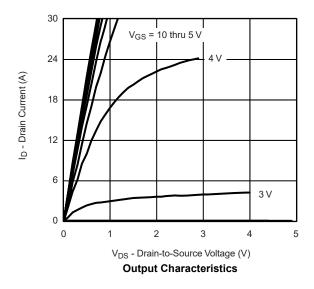
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
7 0 1 1/1 1 1 1 1		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 15			Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 4.7 A		0.036		0
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.8 A		0.055		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 4.7 A		14		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.0 A, V <sub>GS</sub> = 0 V		- 0.74	- 1.1	V
Dynamic <sup>b</sup>				<b>'</b>	•	
Total Gate Charge	Qg			13	20	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4.7 \text{ A}$		3		nC
Gate-Drain Charge	$Q_{gd}$			5.8		
Gate Resistance	$R_{g}$	f = 1.0 MHz		4.6		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			13	20	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 15 $\Omega$		14	22	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 10 V, $R_G$ = 6 $\Omega$		52	80	ns
Fall Time	t <sub>f</sub>			26	40	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.0 A, dl/dt = 100 A/μs		40	60	

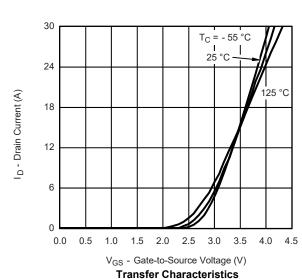
### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

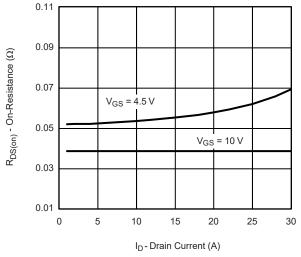
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

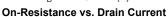


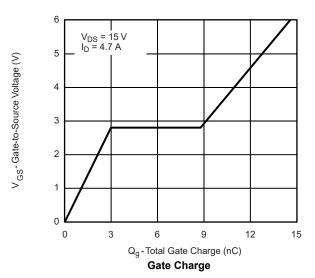




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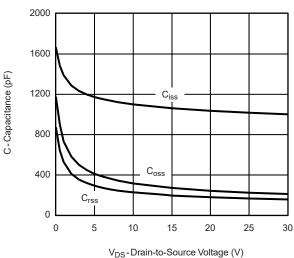






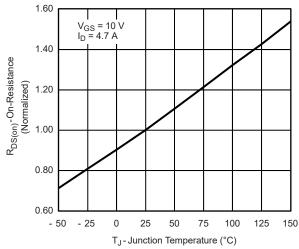
T<sub>J</sub> = 150 °C

 $\label{eq:VSD-Source-to-Drain Voltage} V_{SD}\text{-}\,\text{Source-to-Drain Voltage} \ (\text{V})$  Source-Drain Diode Forward Voltage

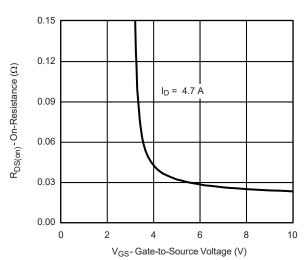


Compositor on





## On-Resistance vs. Junction Temperature



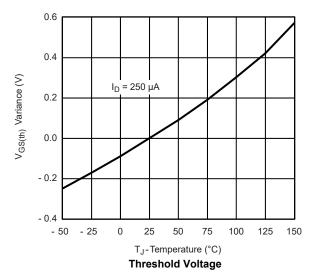
On-Resistance vs. Gate-to-Source Voltage

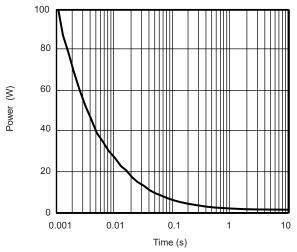
30

10

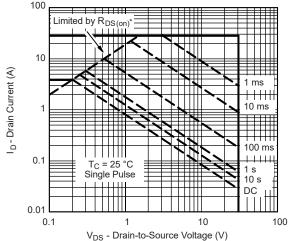


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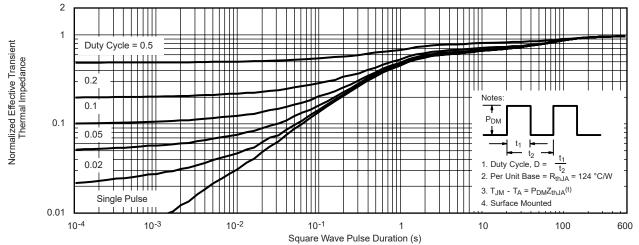




Single Pulse Power, Junction-to-Ambient



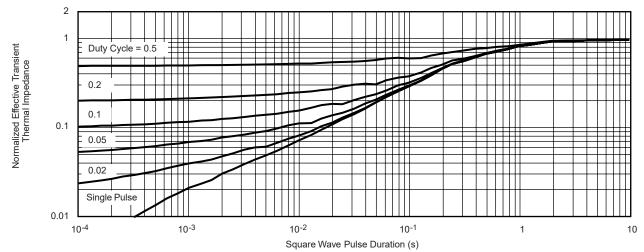
 $^*$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified **Safe Operating Area, Junction-to-Case** 



Normalized Thermal Transient Impedance, Junction-to-Ambient



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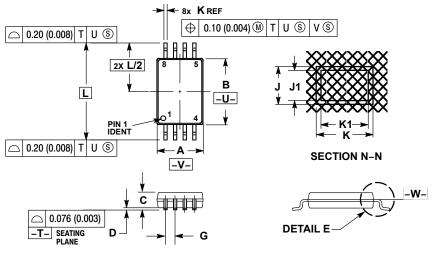


Normalized Thermal Transient Impedance, Junction-to-Foot



TSSOP-8





	MILLIN	IETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	2.90	3.10	0.114	0.122
В	4.30	4.50	0.169	0.177
С		1.10		0.043
D	0.05	0.15	0.002	0.006
F	0.50	0.70	0.020	0.028
G	0.65 BSC		0.026 BSC	
7	0.09	0.20	0.004	0.008
J1	0.09	0.16	0.004	0.006
K	0.19	0.30	0.007	0.012
K1	0.19	0.25	0.007	0.010
L	6.40 BSC		0.252	BSC
M	0°	8°	0	8 °

N 🚤	0.25 (0.010)	
N	M +	

**DETAIL E** 



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