

FTS1003-VB Datasheet

Dual P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
- 30	0.036 at V _{GS} = - 10 V	- 5.2			
	0.055 at V _{GS} = - 4.5 V	- 4.2			

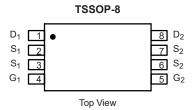
FEATURES

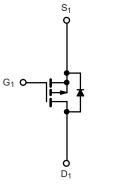
- Halogen-free
- Trench Power MOSFETs

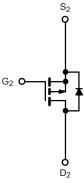
APPLICATIONS

Load SwitchBattery Switch









P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25$ °C, unles	ss otherwise r	noted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 20			
	T _A = 25 °C	– I _D	- 5.2	- 4.1	٩	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 4.2	- 3.6		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.0	- 0.70		
Maximum Power Dissipation ^a	T _A = 25 °C	Р	1.14	0.83	w	
	T _A = 70 °C	- P _D	0.73	0.53		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum humatism to Analysism to	t ≤ 10 s	- R _{thJA}	86	110	
Maximum Junction-to-Ambient ^a	Steady State		124	150	°C/W
Maximum Junction-to-Foot	Steady State	R _{thJF}	52	65	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Test Conditions Min. 1		Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1			
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	μA		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge$ - 5 V, V_{GS} = - 10 V	- 15			А		
Drain-Source On-State Resistance ^a	D	V _{GS} = - 10 V, I _D = - 4.7 A		0.036		0		
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 3.8 A		0.055		Ω		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 4.7 A		14		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.0 A, V _{GS} = 0 V		- 0.74	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			13	20			
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 4.7 A		3		nC		
Gate-Drain Charge	Q _{gd}			5.8				
Gate Resistance	Rg	f = 1.0 MHz		4.6		Ω		
Turn-On Delay Time	t _{d(on)}			13	20			
Rise Time	t _r	V_{DD} = - 15 V, R _L = 15 Ω		14	22			
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 1 A, ${\rm V}_{\rm GEN}$ = - 10 V, ${\rm R}_{\rm G}$ = 6 Ω		52	80	ns		
Fall Time	t _f			26	40			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.0 A, dl/dt = 100 A/μs		40	60			

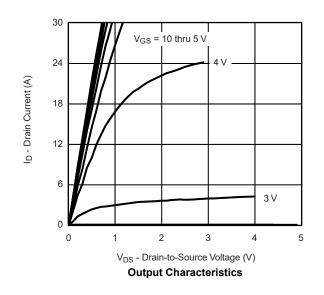
Notes:

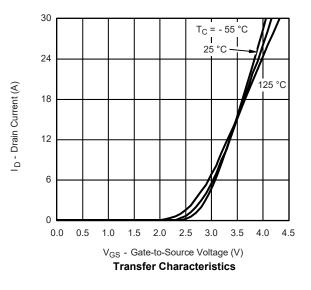
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

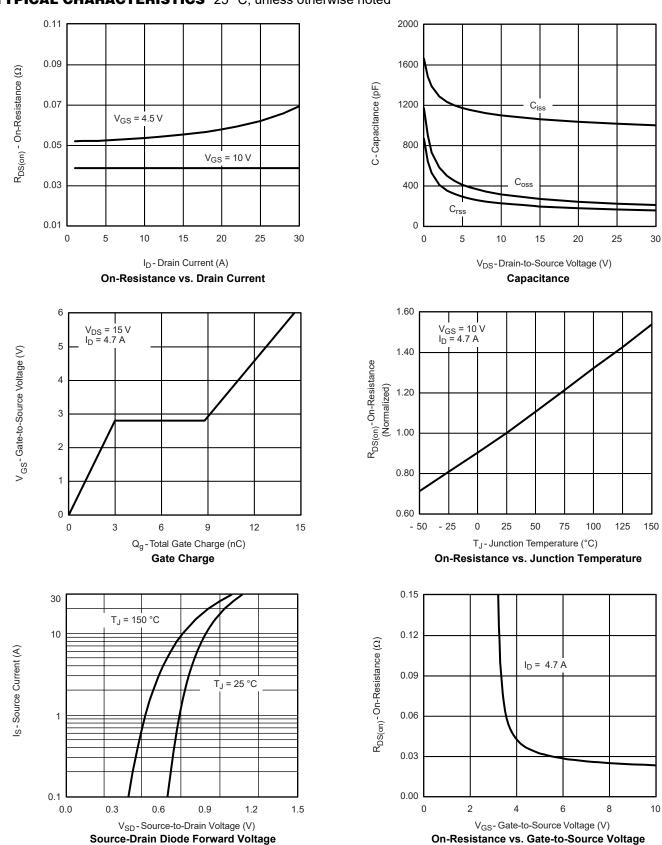
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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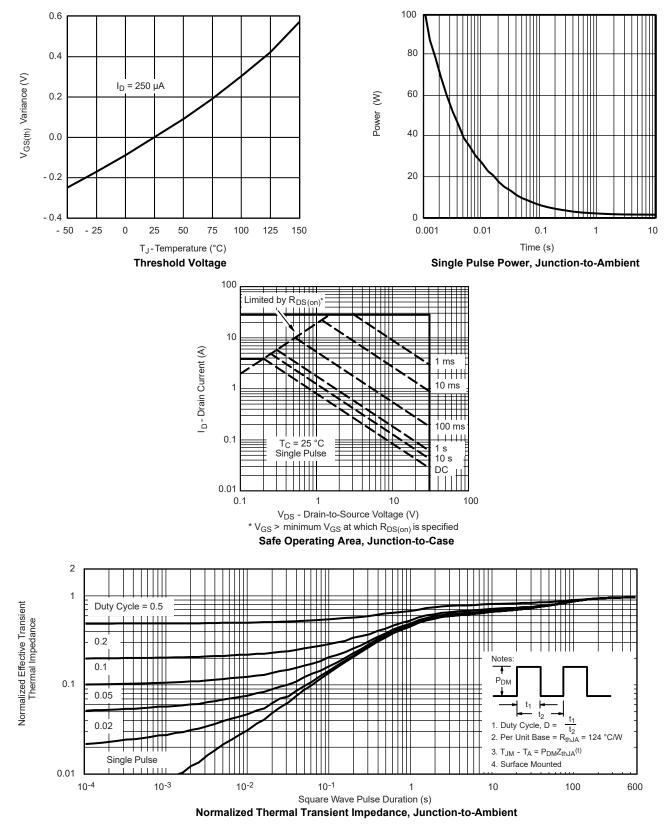




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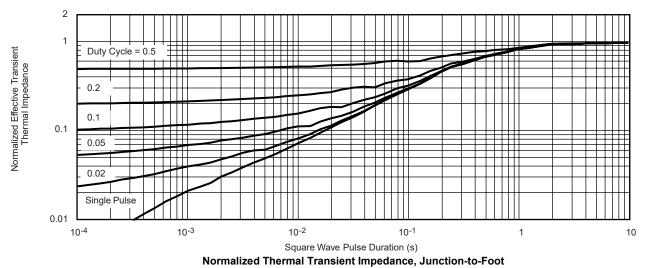


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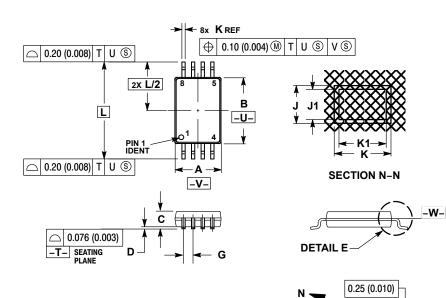


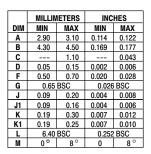
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