

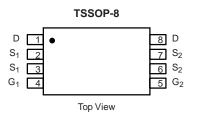
F7752-VB Datasheet Dual N-Channel 30-V (D-S) MOSFET

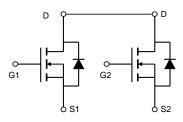
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
30	0.012 at V_{GS} = 10 V	8.6			
	0.019 at V _{GS} = 4.5 V	7.5			

FEATURES

- Halogen-free Option Available
- Trench Power MOSFETs







ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T. 450 °C)	T _A = 25 °C	– I _D	8.6	7.2	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		7.5	5.5	
Pulsed Drain Current		I _{DM}	30		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	1.5 1.0		
	T _A = 25 °C	- P _D	1.6	1.2	W
Maximum Power Dissipation ^a	T _A = 70 °C		0.98	0.67	vv
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Maximum has the te Aashingta	t ≤ 10 s	R _{thJA}	72	83	
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	55	70	

Notes:

a. Surface Mounted on FR4 board, $t \leq$ 10 s.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static			•	•	•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.5		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 200	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 \text{ °C}$			25	μA	
On-State Drain Current ^b	I _{D(on)}	$V_{DS}{\leq}5$ V, $V_{GS}{=}4.5$ V	30			А	
Drain-Source On-State Resistance ^b	Б	$V_{GS} = 10 \text{ V}$, $I_{D} = 8.5 \text{ A}$		0.012		Ω	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$		0.019			
Forward Transconductance ^b	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$		30		S	
Diode Forward Voltage ^b	V _{SD}	$I_{S} = 1.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.71	1.2	V	
Dynamic ^a			•				
Total Gate Charge	Qg			13			
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 6.5 A		2.2		nC	
Gate-Drain Charge	Q _{gd}			3.6			
Turn-On Delay Time	t _{d(on)}			245	365	1	
Rise Time	t _r	V_{DD} = 15V , R_L = 10 Ω		330	495		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 1 A, V_{GEN} = 4.5 V, R_G = 6 Ω		860	1300	ns	
Fall Time	t _f			510	765]	

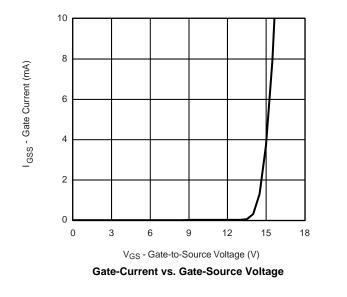
Notes:

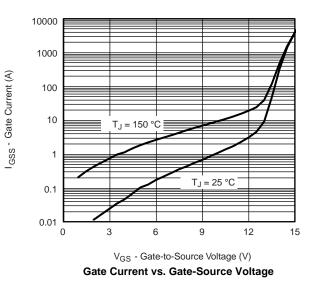
a. For design aid only; not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





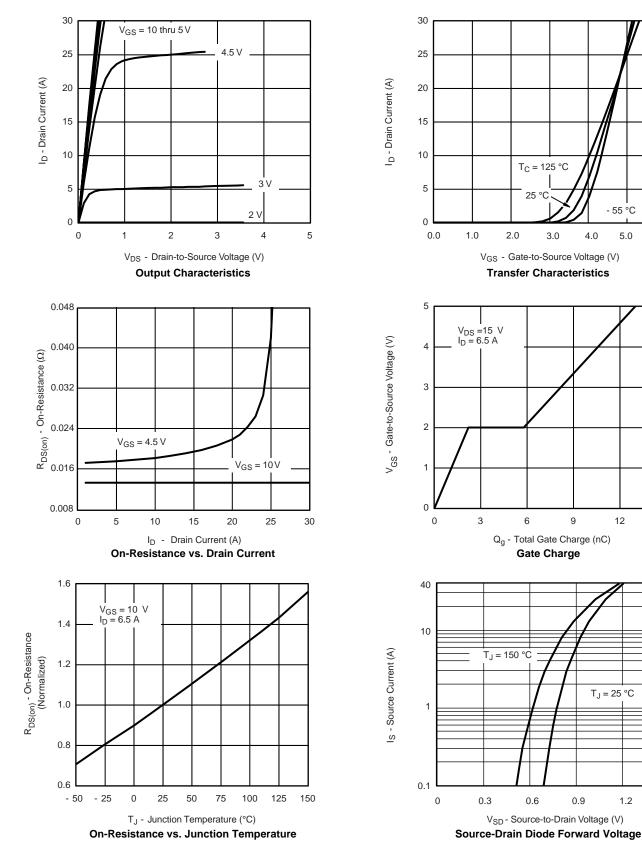
Bsemi

www.VBsemi.com



6.0

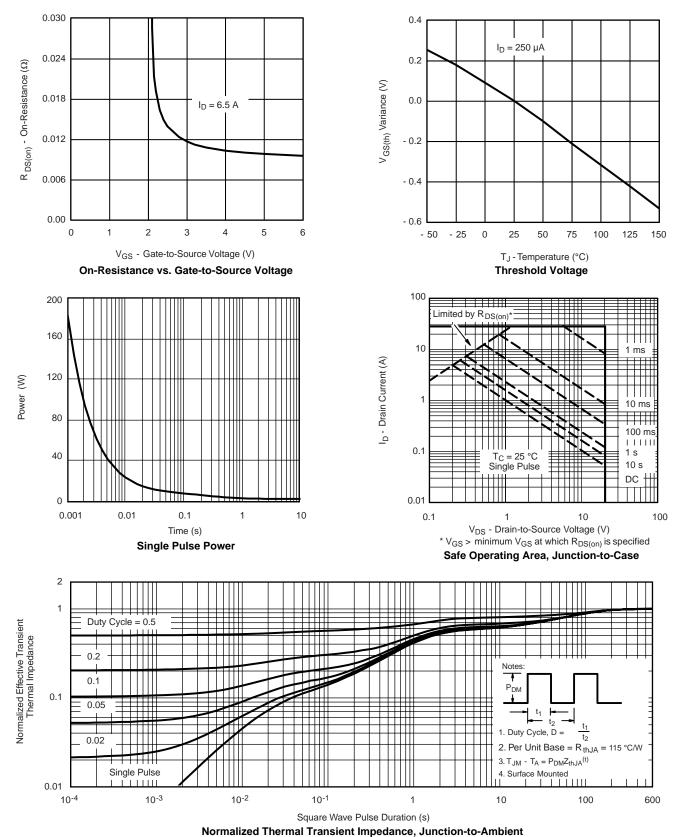
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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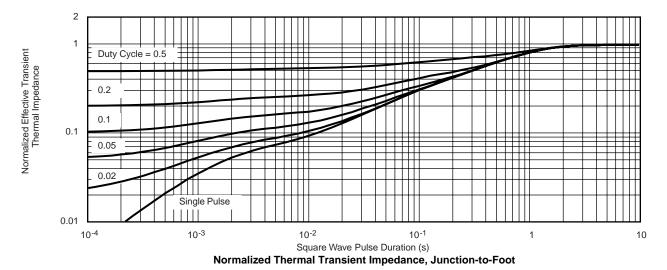




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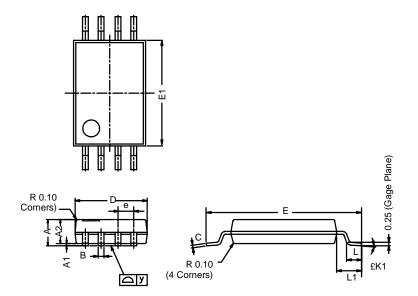


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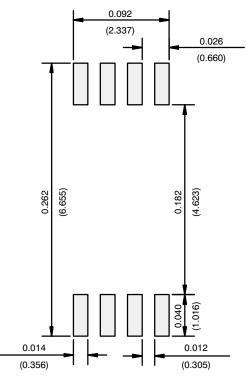
TSSOP: 8-LEAD JEDEC Part Number: MO-153



	MILLIMETERS				
Dim	Min	Min Nom I			
Α	-	-	1.20		
A ₁	0.05	0.10	0.15		
A ₂	0.80	1.00	1.05		
В	0.19	0.28	0.30		
С	-	0.127	-		
D	2.90	3.00	3.10		
Е	6.20	6.40	6.60		
E ₁	4.30	4.40	4.50		
е	-	0.65	-		
L	0.45	0.60	0.75		
L ₁	0.90	1.00	1.10		
Y	-	-	0.10		
£ K1	0°	3°	6°		
ECN: S-03946—Rev. G, 09-Jul-01 DWG: 5844					



RECOMMENDED MINIMUM PADS FOR TSSOP-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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