

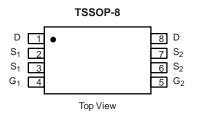
966E-VB Datasheet Dual N-Channel 25-V (D-S) MOSFET

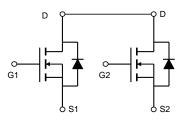
PRODUCT	SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	
25	0.022 at V_{GS} = 4.5 V	6.6	
25	0.032 at V _{GS} = 2.5 V	5.5	

FEATURES

- Halogen-free Option Available
- Trench Power MOSFETs







ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	s otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	25		V	
Gate-Source Voltage		V _{GS}	± 12		v	
Continuous Drain Current (T. 150 %C)a	T _A = 25 °C	- I _D	6.6	5.2	•	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		5.5	3.5		
Pulsed Drain Current		I _{DM}	30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	1.5	1.0	l	
	T _A = 25 °C	P _D	1.5	1.0	w	
Maximum Power Dissipation ^a	T _A = 70 °C		0.96	0.64		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Maximum has the Archiveta	t ≤ 10 s	P	72	83	
Maximum Junction-to-Ambient ^a	Steady State	R _{thJA}	100	120	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	55	70	

Notes:

a. Surface Mounted on FR4 board, $t \leq 10 \mbox{ s.}$

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS $T_J = 25$	°C, unless	otherwise noted					
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static	•			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.5		1.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 200	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 \text{ °C}$			25	μA	
On-State Drain Current ^b	I _{D(on)}	$V_{DS}{\leq}5$ V, $V_{GS}{=}4.5$ V	30			А	
	Б	$V_{GS} = 4.5 \text{ V}, I_{D} = 6.5 \text{ A}$		0.022		0	
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 5.5 \text{ A}$		0.032		Ω	
Forward Transconductance ^b	9 _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$		30		S	
Diode Forward Voltage ^b	V _{SD}	I _S = 1.5 A, V _{GS} = 0 V		0.71	1.2	V	
Dynamic ^a							
Total Gate Charge	Qg			12	18		
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 6.5 A		2.2		nC	
Gate-Drain Charge	Q _{gd}			3.6			
Turn-On Delay Time	t _{d(on)}			245	365		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		330	495	~~	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 1 A, V_{GEN} = 4.5 V, R_G = 6 Ω		860	1300	ns	
Fall Time	t _f			510	765		

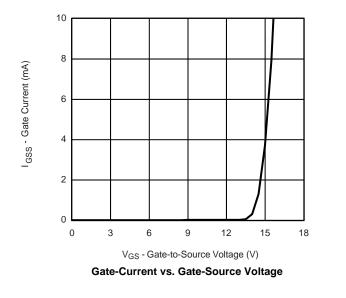
Notes:

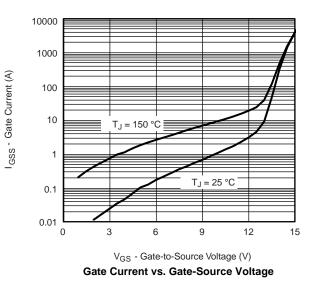
a. For design aid only; not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

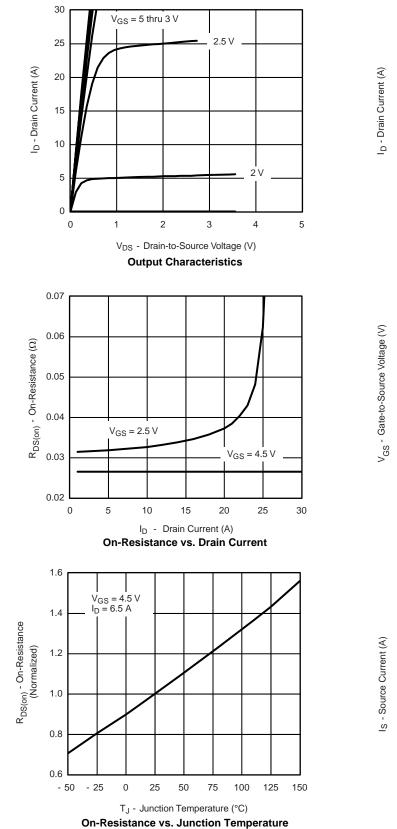




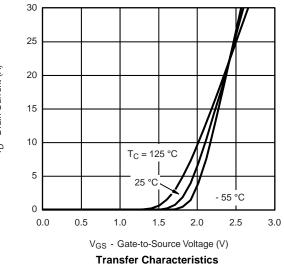
semi

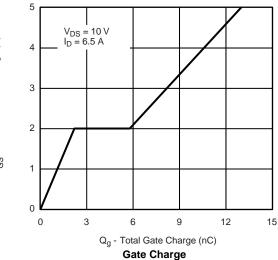
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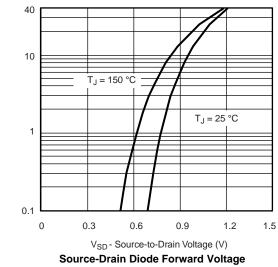




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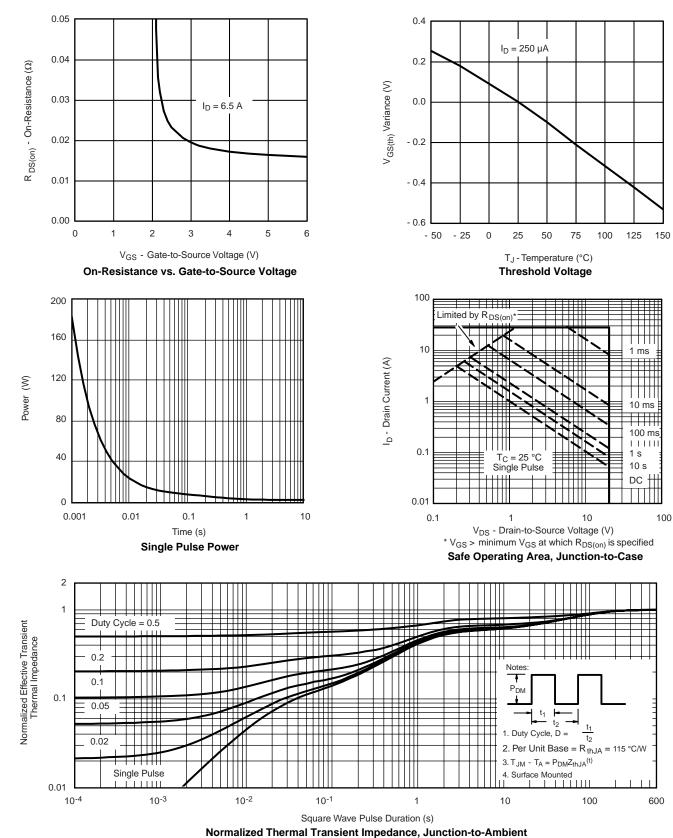




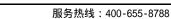


服务热线:400-655-8788



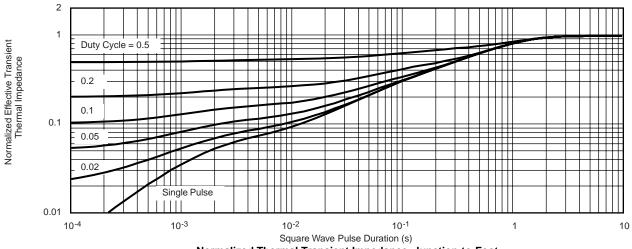


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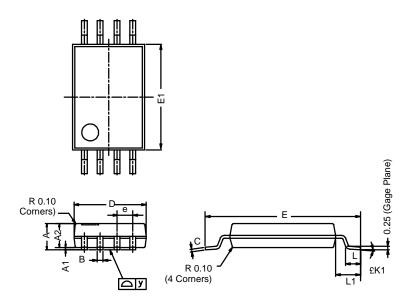
Normalized Thermal Transient Impedance, Junction-to-Foot

966E-VB



TSSOP: 8-LEAD

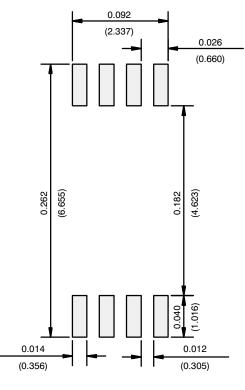
JEDEC Part Number: MO-153



Min - 0.05 0.80 0.19	Nom - 0.10 1.00 0.28 0.127	Max 1.20 0.15 1.05 0.30
0.80	1.00 0.28	0.15
0.80	1.00 0.28	1.05
	0.28	
0.19		0.30
-	0 1 2 7	
	0.127	-
2.90	3.00	3.10
6.20	6.40	6.60
4.30	4.40	4.50
-	0.65	-
0.45	0.60	0.75
0.90	1.00	1.10
-	-	0.10
0°	3°	6°
	6.20 4.30 - 0.45 0.90 - 0°	6.20 6.40 4.30 4.40 - 0.65 0.45 0.60 0.90 1.00 - -



RECOMMENDED MINIMUM PADS FOR TSSOP-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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