

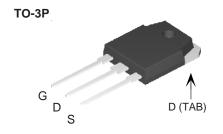
FS70SMJ-03-VB Datasheet N-Channel 60 V (D-S) 175 °C MOSFET

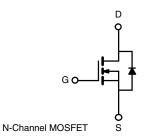
PRODUCT SUMMARY			
V _{DS} (V)	60		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.005		
I _D (A)	150		
Configuration	Single		
Package	TO-247		

FEATURES

- Trench power MOSFET
- Package with low thermal resistance
- \bullet 100 % R_g and UIS tested







ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	60	V	
Gate-Source Voltage		V _{GS} ± 20		7 v	
Continuous Drain Current	T _C = 25 °C	1	150		
	T _C = 125 °C	I _D	88		
Continuous Source Current (Diode Conduction) ^a		I _S	120	Α	
Pulsed Drain Current ^b		I _{DM}	480		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	65		
Single Pulse Avalanche Energy	L = 0.111111	E _{AS}	211	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	175	W	
	T _C = 125 °C		56	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	40	°C/W	
ınction-to-Case (Drain)		R_{thJC}	0.88	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR4 material).
- d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT
Static				l	_		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		60	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0	3.5	ľ
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V	V _{DS} = 60 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	250	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 30 A	-	0.005	-	
	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	0.0104	-	Ω
		V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	0.0129	-	
Forward Transconductance b	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		-	94	-	S
Dynamic ^b	<u> </u>						
Input Capacitance	C _{iss}			-	5196	6495	
Output Capacitance	Coss	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	708	885	pF
Reverse Transfer Capacitance	C _{rss}	1		-	336	420	
Total Gate Charge ^c	Qg		V _{DS} = 30 V, I _D = 75 A	-	96.5	145	nC
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	24.6	-	
Gate-Drain Charge ^c	Q _{gd}			-	27.2	-	
Gate Resistance	R _g	f = 1 MHz		0.3	1	1.7	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	16	24	
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V}, \text{ R}_L = 0.4 \Omega$ $I_D \cong 75 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		-	14	21	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	34	51	
Fall Time ^c	t _f			-	9	14	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	480	Α
Forward Voltage	V_{SD}	$I_F = 75 \text{ A}, V_{GS} = 0$		-	0.9	1.5	V

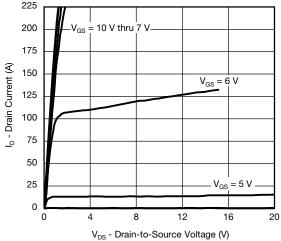
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

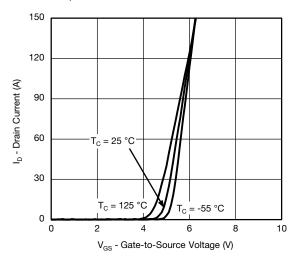


TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

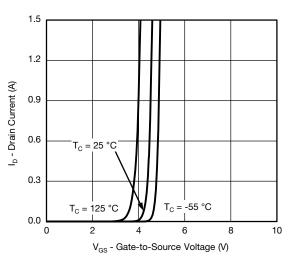


Output Characteristics

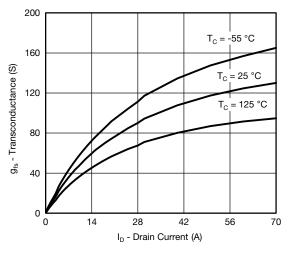




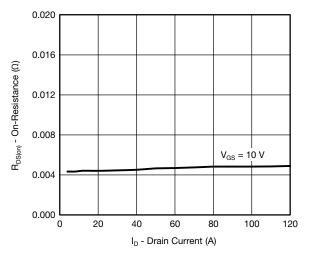
Transfer Characteristics



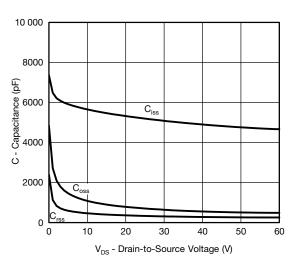
Transfer Characteristics



Transconductance



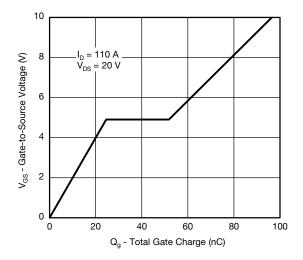
On-Resistance vs. Drain Current



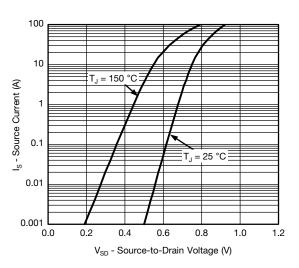
Capacitance



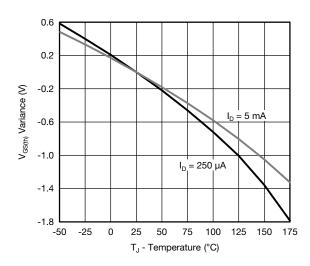
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



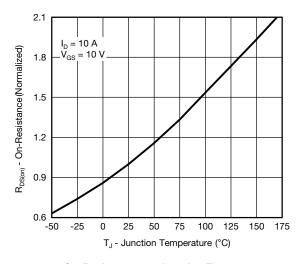
Gate Charge



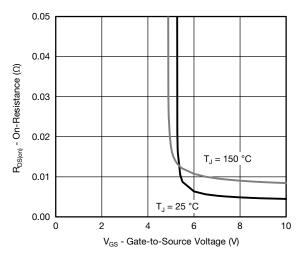
Source Drain Diode Forward Voltage



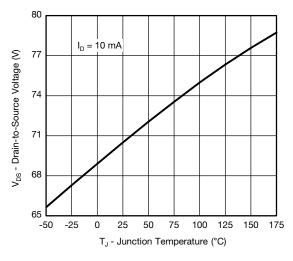
Threshold Voltage



On-Resistance vs. Junction Temperature



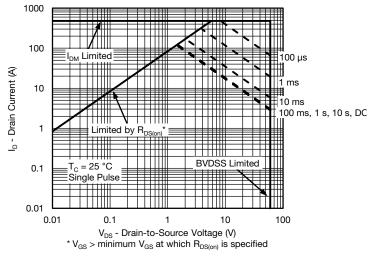
On-Resistance vs. Gate-to-Source Voltage



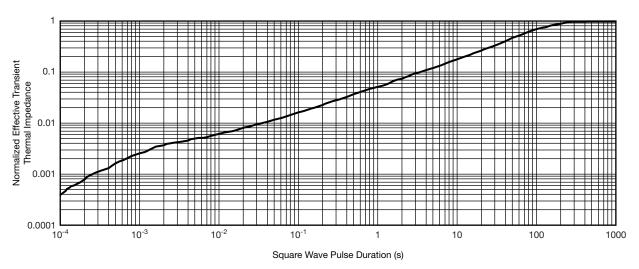
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



Safe Operating Area



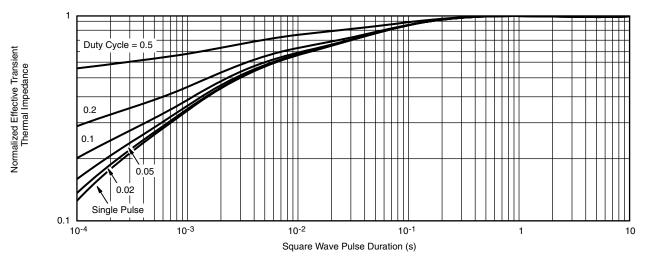
Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



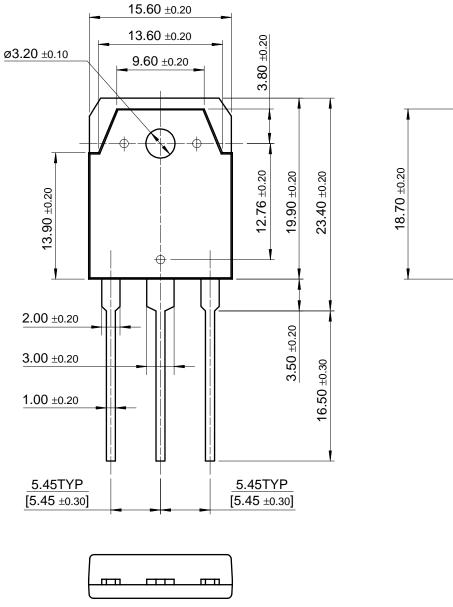
Normalized Thermal Transient Impedance, Junction-to-Case

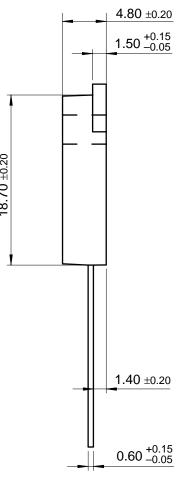
Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-3P







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