

NCE30H32WD-VB Datasheet

N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY

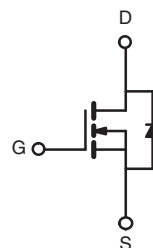
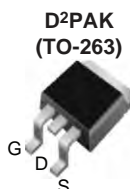
| | |
|---|--------|
| V_{DS} (V) | 30 |
| $R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V | 0.0014 |
| $R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V | 0.0016 |
| I_D (A) | 260 |
| Configuration | Single |

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- Package with Low Thermal Resistance
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNIT |
|---|----------------|----------------|------------------|------|
| Drain-Source Voltage | | V_{DS} | 30 | V |
| Gate-Source Voltage | | V_{GS} | ± 20 | |
| Continuous Drain Current | $T_C = 25$ °C | I_D | 260 | A |
| | $T_C = 125$ °C | | 120 ^a | |
| Continuous Source Current (Diode Conduction) ^a | | I_S | 120 | |
| Pulsed Drain Current ^b | | I_{DM} | 680 | |
| Single Pulse Avalanche Current | L = 0.1 mH | I_{AS} | 82 | mJ |
| Single Pulse Avalanche Energy | | E_{AS} | 336 | |
| Maximum Power Dissipation ^b | $T_C = 25$ °C | P_D | 375 | W |
| | $T_C = 125$ °C | | 125 | |
| Operating Junction and Storage Temperature Range | | T_J, T_{stg} | - 55 to + 175 | °C |

THERMAL RESISTANCE RATINGS

| PARAMETER | | SYMBOL | LIMIT | UNIT |
|--------------------------|------------------------|------------|-------|------|
| Junction-to-Ambient | PCB Mount ^c | R_{thJA} | 40 | °C/W |
| Junction-to-Case (Drain) | | R_{thJC} | 0.4 | |

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

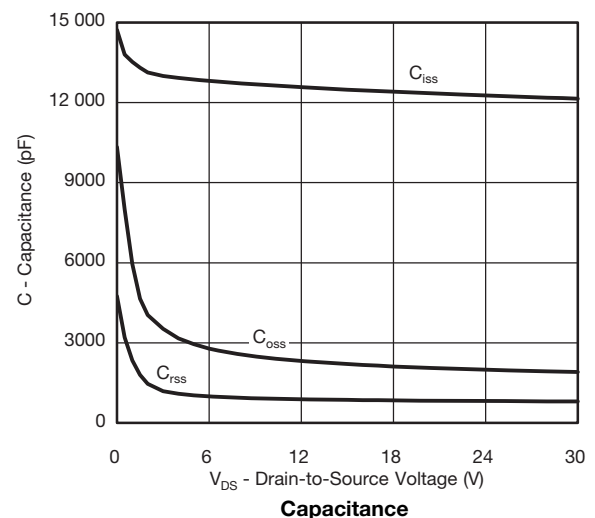
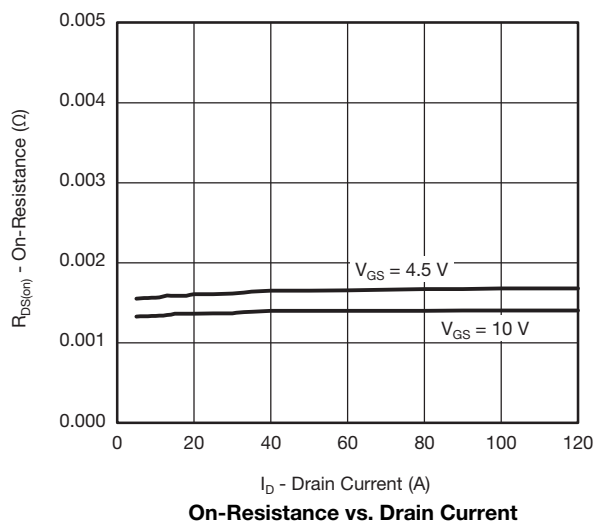
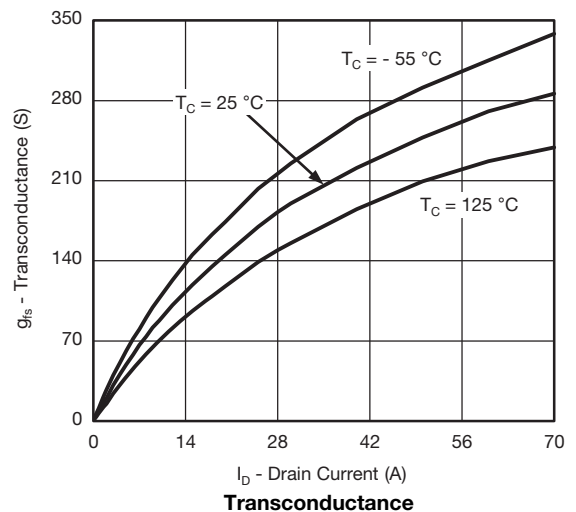
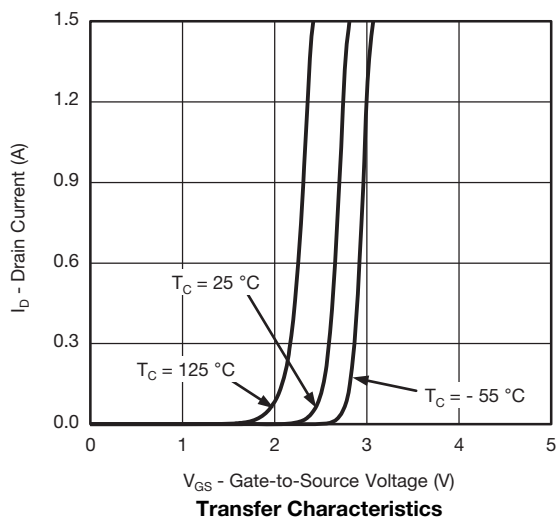
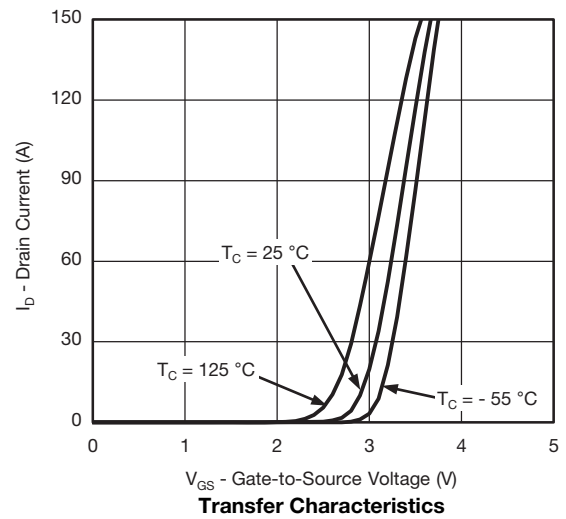
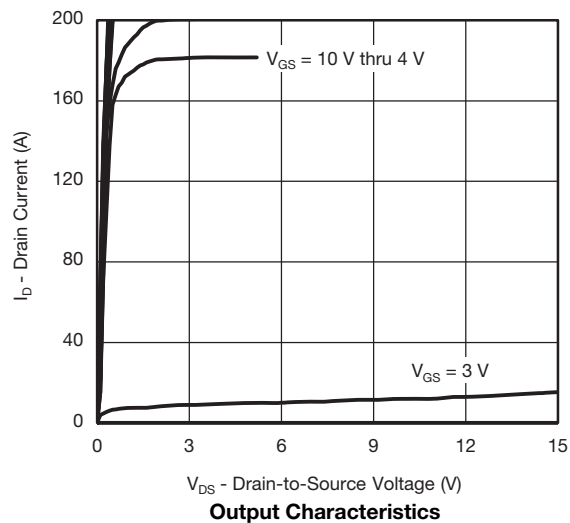
| SPECIFICATIONS (T _C = 25 °C, unless otherwise noted) | | | | | | | |
|---|---------------------|--|---|------|--------|--------|------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNIT |
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | V _{GS} = 0 V, I _D = 250 μA | | 30 | - | - | V |
| Gate-Source Threshold Voltage | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 250 μA | | 1.5 | 2.0 | 2.5 | |
| Gate-Source Leakage | I _{GSS} | V _{DS} = 0 V, V _{GS} = ± 20 V | | - | - | ± 100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{GS} = 0 V | V _{DS} = 30 V | - | - | 1 | μA |
| | | V _{GS} = 0 V | V _{DS} = 30 V, T _J = 125 °C | - | - | 50 | |
| | | V _{GS} = 0 V | V _{DS} = 30 V, T _J = 175 °C | - | - | 250 | |
| On-State Drain Current ^a | I _{D(on)} | V _{GS} = 10 V | V _{DS} ≥ 5 V | 120 | - | - | A |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = 10 V | I _D = 30 A | - | 0.0014 | - | Ω |
| | | V _{GS} = 10 V | I _D = 30 A, T _J = 125 °C | - | 0.0023 | - | |
| | | V _{GS} = 10 V | I _D = 30 A, T _J = 175 °C | - | 0.0028 | - | |
| | | V _{GS} = 4.5 V | I _D = 20 A | - | 0.0016 | - | |
| Forward Transconductance ^b | g _{fs} | V _{DS} = 15 V, I _D = 30 A | | - | 190 | - | S |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | V _{GS} = 0 V | V _{DS} = 15 V, f = 1 MHz | - | 12 484 | 15 605 | pF |
| Output Capacitance | C _{oss} | | | - | 2204 | 2755 | |
| Reverse Transfer Capacitance | C _{rss} | | | - | 860 | 1075 | |
| Total Gate Charge ^c | Q _g | V _{GS} = 10 V | V _{DS} = 10 V, I _D = 120 A | - | 179 | 270 | nC |
| Gate-Source Charge ^c | Q _{gs} | | | - | 34 | - | |
| Gate-Drain Charge ^c | Q _{gd} | | | - | 21 | - | |
| Gate Resistance | R _g | f = 1 MHz | | 0.59 | 1.19 | 1.79 | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | V _{DD} = 15 V, R _L = 0.3 Ω I _D ≅ 50 A, V _{GEN} = 10 V, R _g = 1 Ω | | - | 18 | 27 | ns |
| Rise Time ^c | t _r | | | - | 11 | 17 | |
| Turn-Off Delay Time ^c | t _{d(off)} | | | - | 64 | 96 | |
| Fall Time ^c | t _f | | | - | 11 | 17 | |
| Source-Drain Diode Ratings and Characteristics ^b | | | | | | | |
| Pulsed Current ^a | I _{SM} | | | - | - | 480 | A |
| Forward Voltage | V _{SD} | I _F = 60 A, V _{GS} = 0 V | | - | 0.81 | 1.5 | V |

Notes

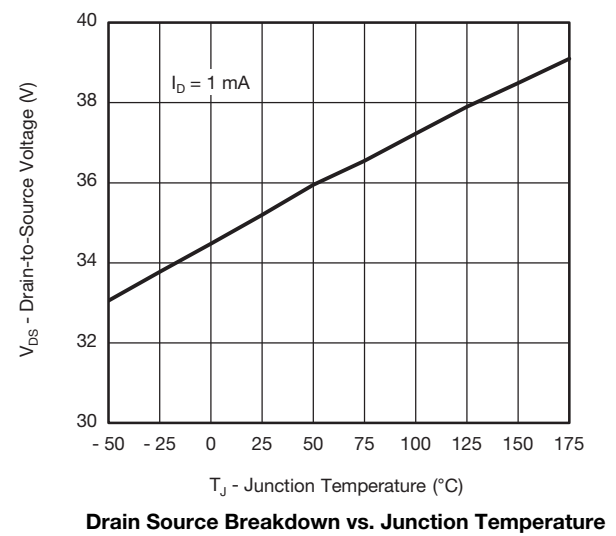
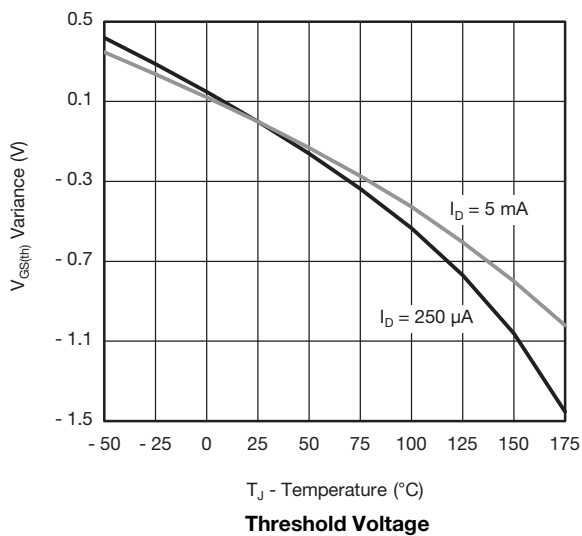
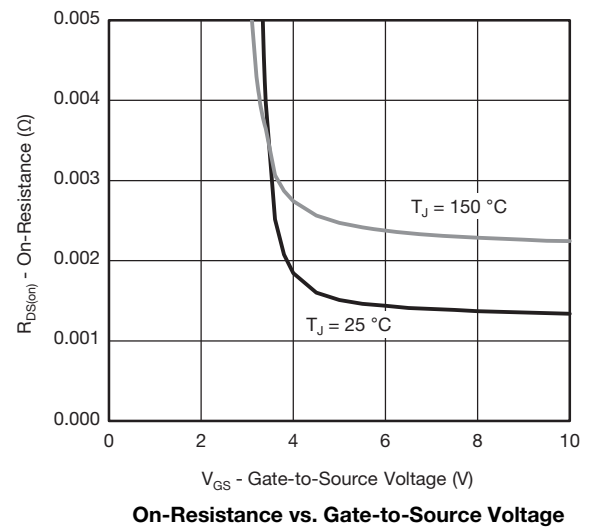
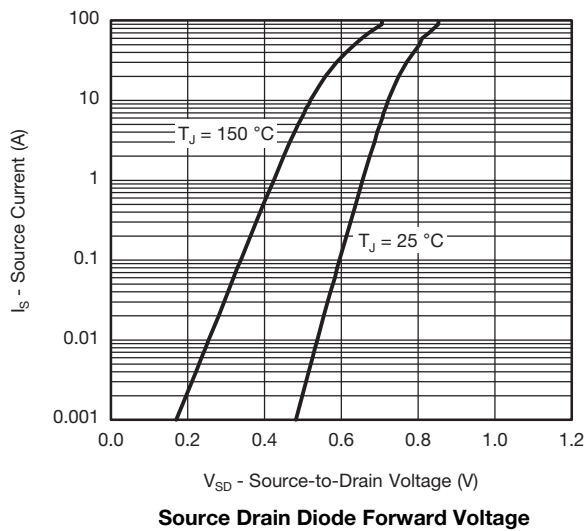
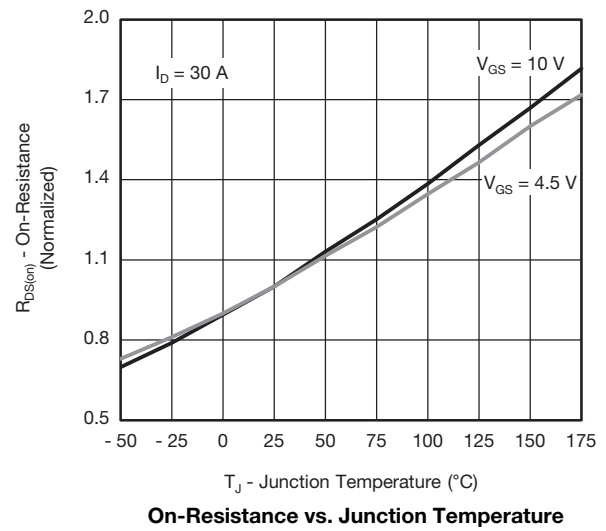
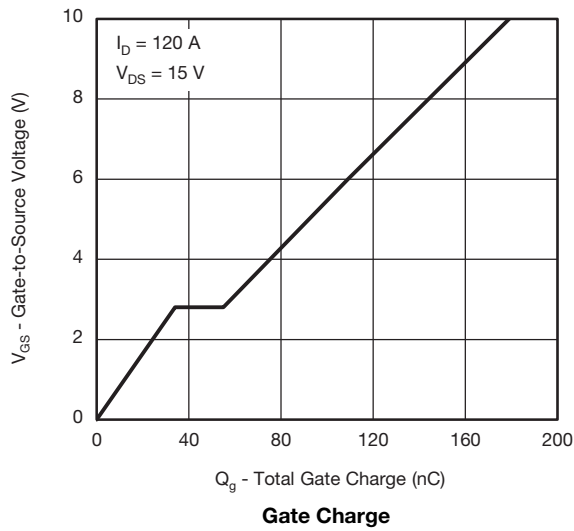
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

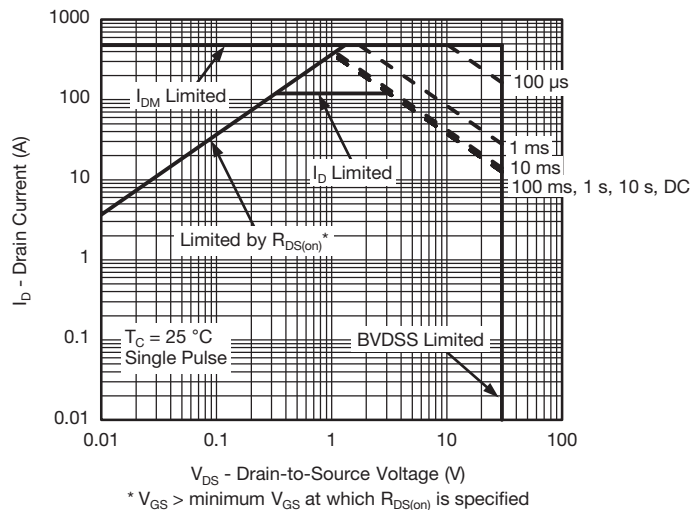
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



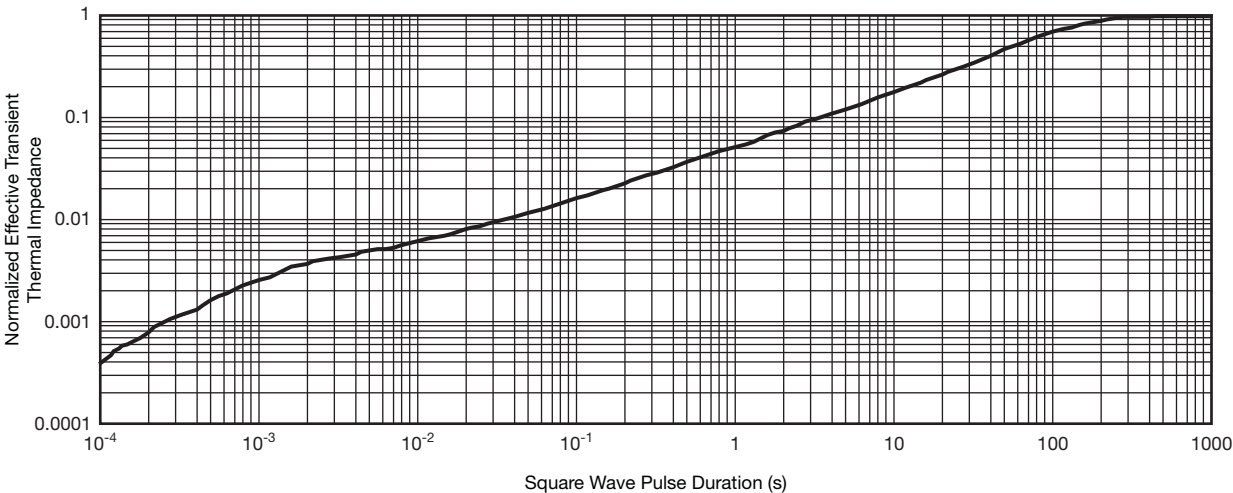
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



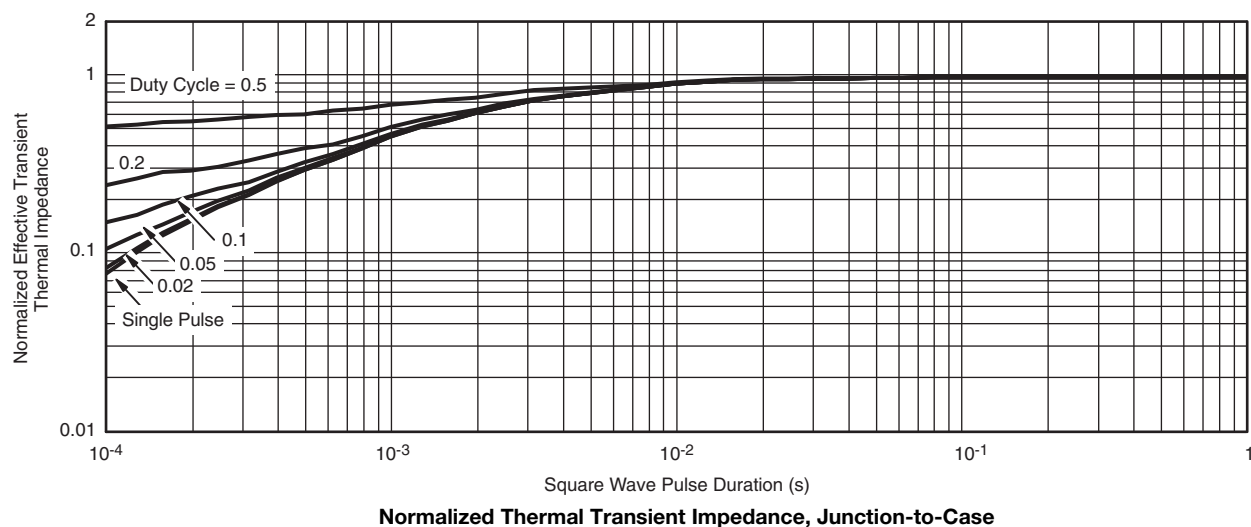
THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Case ($25\text{ }^{\circ}\text{C}$)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

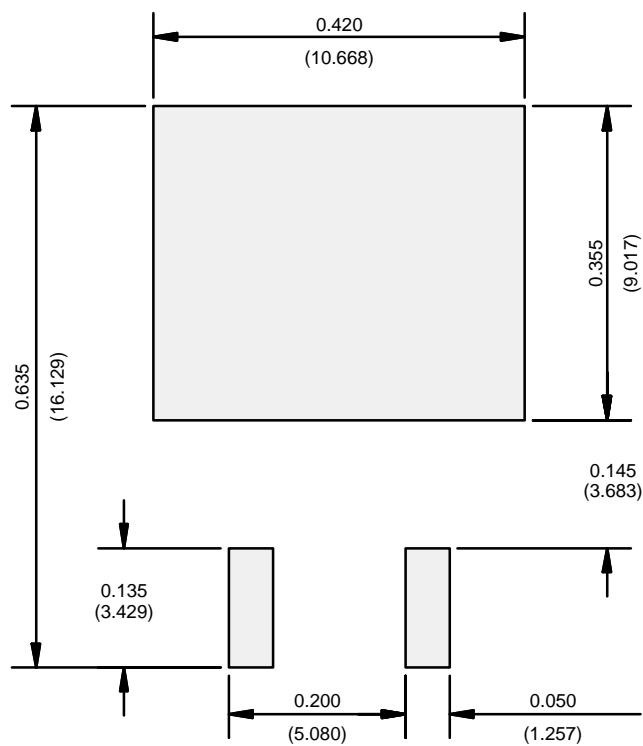
Technical drawing of a lead tip assembly, showing multiple views and annotations:

- Main View (Top Left):** Shows the lead tip assembly with dimensions $L1$, D , H , E , $L4$, A , c , $c2$, $A1$, B , C , $2 \times b2$, $2 \times b$, $2 \times e$, and $2 \times c$. It includes feature control frames for surface texture (Ra), circular runout (ϕ), and position (\square).
- Detail A (Top Right):** A circular detail view of the lead tip, showing dimensions A , $c2$, A , and c . It includes a feature control frame for surface texture (Ra).
- Section B - B and C - C (Bottom Center):** A cross-sectional view showing the lead tip, plating, and base metal. Dimensions include $b1$, $b3$, $c1$, $(b, b2)$, and (c) . It includes a feature control frame for surface texture (Ra).
- View A - A (Bottom Right):** A side view of the lead tip assembly, showing dimensions E , $D1$, $E1$, and $A1$. It includes a feature control frame for surface texture (Ra).
- Lead Tip (Bottom Left):** A small detail view of the lead tip, showing dimensions $L1$, D , H , E , $L4$, A , c , $c2$, $A1$, B , C , $2 \times b2$, $2 \times b$, $2 \times e$, and $2 \times c$.
- Annotations:**
 - (Datum A):** A feature control frame indicating datum A.
 - Seating plane:** A feature control frame indicating the seating plane.
 - Gauge plane:** A feature control frame indicating the gauge plane.
 - Detail "A":** A feature control frame indicating detail A.
 - Section B - B and C - C:** A feature control frame indicating the section view.
 - View A - A:** A feature control frame indicating the view A - A.
 - Scale: none**
 - Scale: 8:1**
 - Scale: 1:1**
 - Scale: 2:1**
 - Scale: 4:1**
 - Scale: 8:1**
 - Scale: 16:1**
 - Scale: 32:1**
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 - Scale: 128:1**
 - Scale: 256:1**
 - Scale: 512:1**
 - Scale: 1024:1**
 - Scale: 2048:1**
 - Scale: 4096:1**
 - Scale: 8192:1**
 - Scale: 16384:1**
 - Scale: 32768:1**
 - Scale: 65536:1**
 - Scale: 131072:1**
 - Scale: 262144:1**
 - Scale: 524288:1**
 - Scale: 1048576:1**
 - Scale: 2097152:1**
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 - Scale: 40564819207303340847894502572032:1**
 - Scale: 8**

| | MILLIMETERS | | INCHES | |
|------|-------------|-------|-----------|-------|
| DIM. | MIN. | MAX. | MIN. | MAX. |
| D1 | 6.86 | - | 0.270 | - |
| E | 9.65 | 10.67 | 0.380 | 0.420 |
| E1 | 6.22 | - | 0.245 | - |
| e | 2.54 BSC | | 0.100 BSC | |
| H | 14.61 | 15.88 | 0.575 | 0.625 |
| L | 1.78 | 2.79 | 0.070 | 0.110 |
| L1 | - | 1.65 | - | 0.066 |
| L2 | - | 1.78 | - | 0.070 |
| L3 | 0.25 BSC | | 0.010 BSC | |
| L4 | 4.78 | 5.28 | 0.188 | 0.208 |

ECN: S-82110-Rev. A, 15-Sep-08
DWG: 5970

1. Dimensioning and tolerancing per ASME Y14.5M-1994.
2. Dimensions are shown in millimeters (inches).
3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
5. Dimension b1 and c1 apply to base metal only.
6. Datum A and B to be determined at datum plane H.
7. Outline conforms to JEDEC outline to TO-263AB.

RECOMMENDED MINIMUM PADS FOR D PAK: 3-Lead

Recommended Minimum Pads
Dimensions in Inches/(mm)

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