

# IRFS4229PBF-VB Datasheet N-Channel 250 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)		
250	0.040 at V <sub>GS</sub> = 10 V	60	95		
	0.045 at V <sub>GS</sub> = 6 V	55	95		

#### **FEATURES**

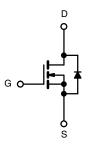
- Trench Power MOSFETS
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- Compliant to RoHS Directive 2002/95/EC



## **APPLICATIONS**

Industrial





N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_C = 25  ^{\circ}C$ , unless oth	nerwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	250	V	
Gate-Source Voltage	V <sub>GS</sub>	± 30		
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I-	60	
Continuous Diam Current (1) = 175 C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	35	A
Pulsed Drain Current	I <sub>DM</sub>	200	_ ^	
Avalanche Current	I <sub>AR</sub>	35		
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	61	mJ
Maniana Bana Bisahariani	T <sub>C</sub> = 25 °C	В	300 <sup>b</sup>	14/
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	P <sub>D</sub>	3.75	- W
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W		
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.5	C/VV		

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	250			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 30 \text{ V}$			± 250	nA	
		V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V			1	50 μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
		V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	70			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.040		Ω	
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}, T_J = 125 ^{\circ}\text{C}$		0.091			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.123			
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 25 A		0.045			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		70		S	
Dynamic <sup>b</sup>	<del>'</del>			•	· · · · · · · · · · · · · · · · · · ·		
Input Capacitance	C <sub>iss</sub>			5000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		300			
Reverse Transfer Capacitance	C <sub>rss</sub>			170			
Total Gate Charge <sup>c</sup>	Qg			95	140		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 125 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 45 \text{ A}$		28		nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			34			
Gate Resistance	$R_{g}$	f = 1 MHz		1.6		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			22	35		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_L = 2.78 \Omega$		220	330		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 45 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		40	60	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>			145	220	1	
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C) <sup>b</sup>					
Continuous Current	Is				45	^	
Pulsed Current	I <sub>SM</sub>				70	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 45 A, V <sub>GS</sub> = 0 V		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			150	225	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 45 A, di/dt = 100 A/μs		12	18	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			0.9	2	μС	

#### Notes

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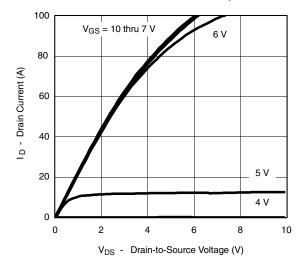
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

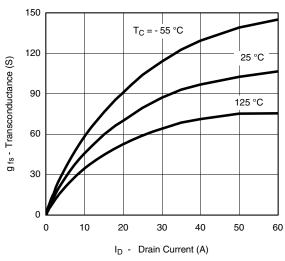
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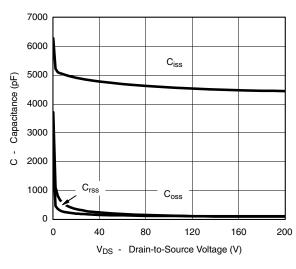
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



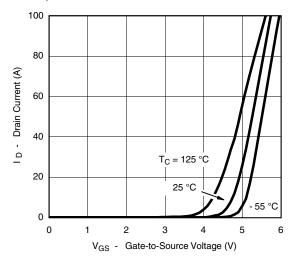




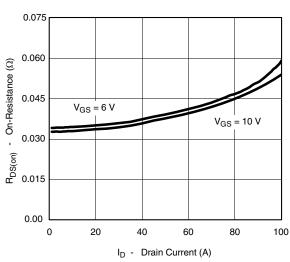
#### Transconductance



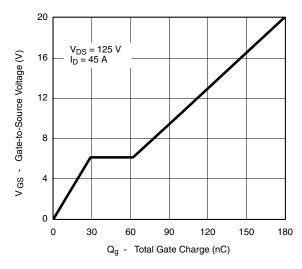
Capacitance



#### Transfer Characteristics



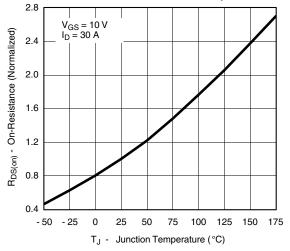
### On-Resistance vs. Drain Current



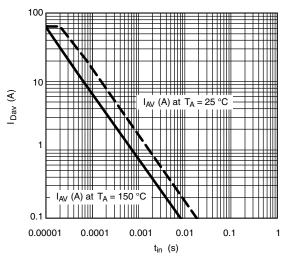
**Gate Charge** 



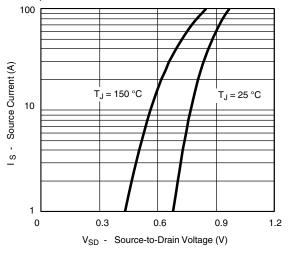
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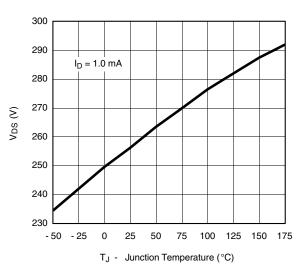
On-Resistance vs. Junction Temperature



**Avalanche Current vs. Time** 



Source-Drain Diode Forward Voltage

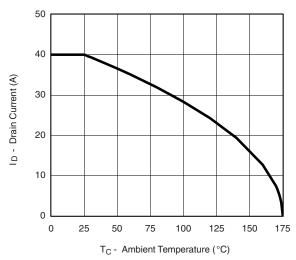


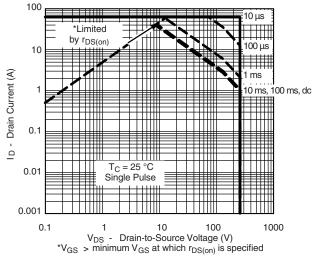
Drain Source Breakdown vs. Junction Temperature

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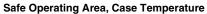


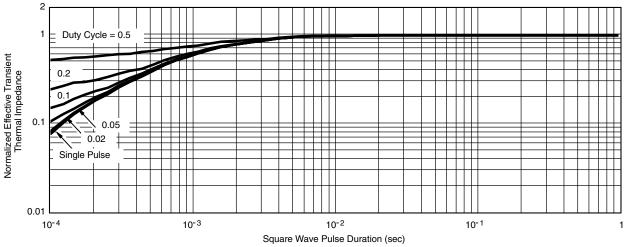
#### **THERMAL RATINGS**





Maximum Avalanche and Drain Current vs. Case Temperature



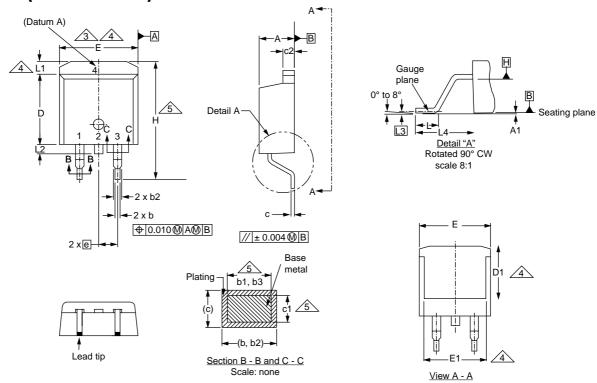


Normalized Thermal Transient Impedance, Junction-to-Case

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## **TO-263AB (HIGH VOLTAGE)**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.06	4.83	0.160	0.190
A1	0.00	0.25	0.000	0.010
b	0.51	0.99	0.020	0.039
b1	0.51	0.89	0.020	0.035
b2	1.14	1.78	0.045	0.070
b3	1.14	1.73	0.045	0.068
С	0.38	0.74	0.015	0.029
c1	0.38	0.58	0.015	0.023
c2	1.14	1.65	0.045	0.065
D	8.38	9.65	0.330	0.380

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
D1	6.86	-	0.270	-
Е	9.65	10.67	0.380	0.420
E1	6.22	-	0.245	-
е	2.54 BSC		0.100 BSC	
Н	14.61	15.88	0.575	0.625
L	1.78	2.79	0.070	0.110
L1	-	1.65	-	0.066
L2	-	1.78	-	0.070
L3	0.25 BSC		0.010	BSC
L4	4.78	5.28	0.188	0.208

ECN: S-82110-Rev. A, 15-Sep-08

DWG: 5970

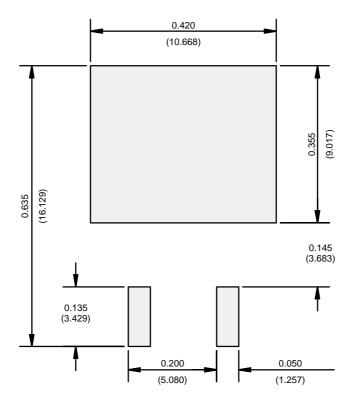
#### Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
- 4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
- 5. Dimension b1 and c1 apply to base metal only.
- 6. Datum A and B to be determined at datum plane H.
- 7. Outline conforms to JEDEC outline to TO-263AB.

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# RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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