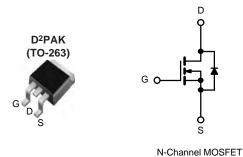


## HUF76137S3ST-VB Datasheet N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY								
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ)					
30	0.0024 at V <sub>GS</sub> = 10 V	98	82 nC					
	0.0027 at V <sub>GS</sub> = 4.5 V	98	02 110					



#### **FEATURES**

- Trench Power MOSFET .
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC •

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	N	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		98 <sup>a, e</sup>		
Continuous Drain Current (T 175 °C)	T <sub>C</sub> = 70 °C	, <u> </u>	98 <sup>e</sup>		
Continuous Drain Current ( $T_J = 175 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	28.8 <sup>b, c</sup>	A	
	T <sub>A</sub> = 70 °C		27 <sup>b, c</sup>	A	
Pulsed Drain Current	I <sub>DM</sub>	300			
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	36		
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	64.8	V	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	la la	90 <sup>a, e</sup>	٨	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>		
Maximum David Dissis ation	T <sub>C</sub> = 70 °C	Б	175	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.75 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>		
Operating Junction and Storage Temperature R	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Тур.	Max.	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	32	40	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	0/11		

#### Notes:

a. Based on  $T_C = 25 \text{ °C}$ . b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



<b>SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ , unless otherwise noted)									
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit			
Static						-			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30			V			
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		35		m\//°C			
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 230 \ \mu A$		- 7.5		mV/°(			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.5		2.5	V			
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA			
Zana Cata Maltana Drain Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10				
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	90			Α			
	P	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 28.8 A		0.0024		0			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 27 \text{ A}$		0.0027		Ω			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 28.8 A		160		S			
Dynamic <sup>b</sup>			1	1					
Input Capacitance	C <sub>iss</sub>			12065					
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 0 V, f = 1 MHz		1725		pF			
Reverse Transfer Capacitance	C <sub>rss</sub>			970					
Takal Oaks Oksawa	0	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 28.8 A		171	257	nC			
Total Gate Charge	Qg	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 28.8 A		81.5	123				
Gate-Source Charge	Q <sub>gs</sub>			34					
Gate-Drain Charge	Q <sub>gd</sub>			29					
Gate Resistance	Rg	f = 1 MHz		1.4	2.1	Ω			
Turn-On Delay Time	t <sub>d(on)</sub>			18	27				
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 15 V, R <sub>I</sub> = 0.625 Ω		11	17	- ns			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 24 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		70	105				
Fall Time	t <sub>f</sub>			10	15				
Turn-On Delay Time	t <sub>d(on)</sub>			55	83				
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_{L}$ = 0.67 $\Omega$		180	270				
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 22.5 \text{ A}, \text{ V}_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		55	83				
Fall Time	t <sub>f</sub>			12	18	-			
Drain-Source Body Diode Characteristic	cs		1			L			
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			90				
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				90	A			
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.8	1.2	V			
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns			
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		70.2	105	nC			
Reverse Recovery Fall Time	$I_{\rm E} = 20$ A.			27					
Reverse Recovery Rise Time	t <sub>b</sub>			25		ns			

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

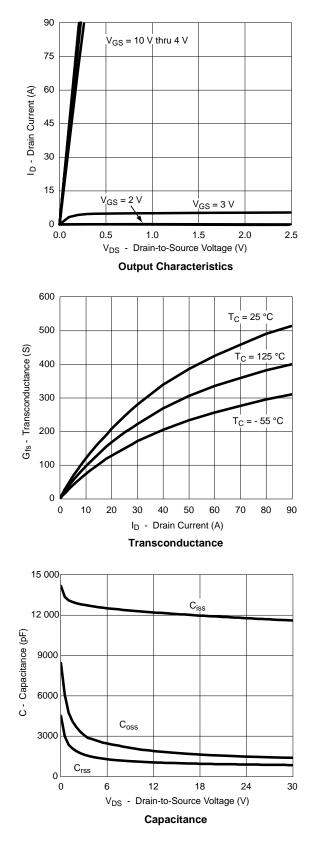
b. Guaranteed by design, not subject to production testing.

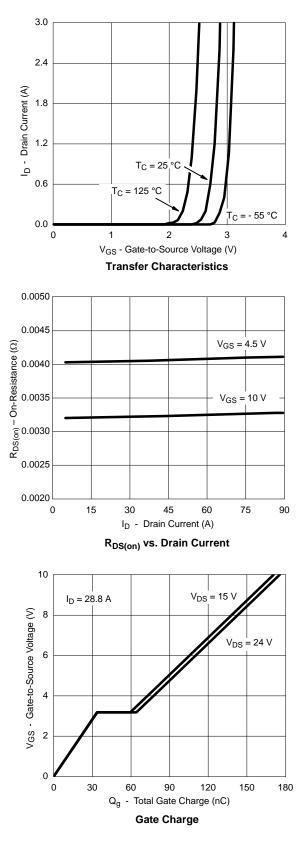
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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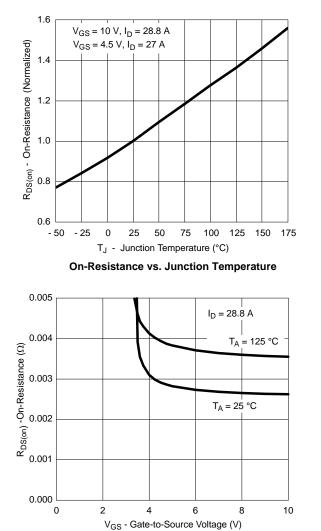


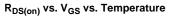


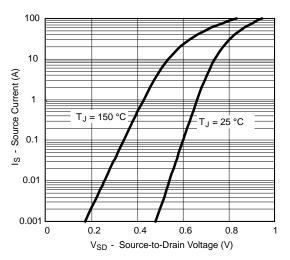




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

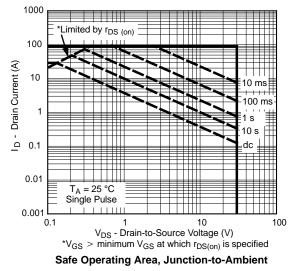




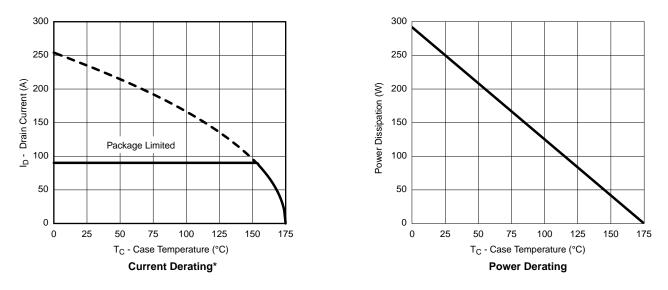












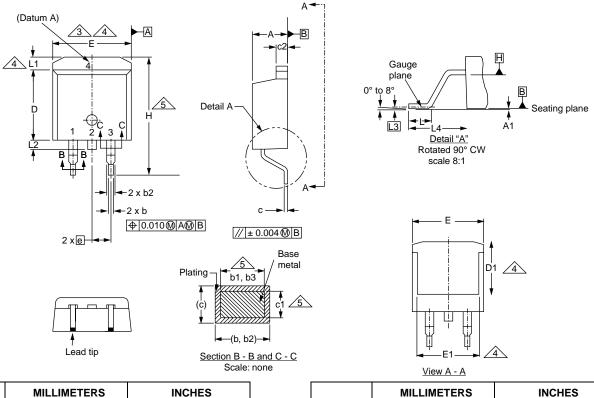
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

\*The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





#### **TO-263AB (HIGH VOLTAGE)**



	MILLIN	METERS	INC	HES	S MILLIMETERS		IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.		DIM.	MIN.	MAX.	MIN.	MAX.
А	4.06	4.83	0.160	0.190		D1	6.86	-	0.270	-
A1	0.00	0.25	0.000	0.010		Е	9.65	10.67	0.380	0.420
b	0.51	0.99	0.020	0.039		E1	6.22	-	0.245	-
b1	0.51	0.89	0.020	0.035		е	2.54 BSC		0.100 BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625
b3	1.14	1.73	0.045	0.068		L	1.78	2.79	0.070	0.110
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066
c1	0.38	0.58	0.015	0.023		L2	-	1.78	-	0.070
c2	1.14	1.65	0.045	0.065		L3	0.25 BSC		0.010 BSC	
D	8.38	9.65	0.330	0.380		L4	4.78	5.28	0.188	0.208
ECN: S-82 DWG: 597	110-Rev. A, 0	15-Sep-08	•	•	·					

Notes

2. Dimensions are shown in millimeters (inches).

3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.

4. Thermal PAD contour optional within dimension E, L1, D1 and E1.

5. Dimension b1 and c1 apply to base metal only.

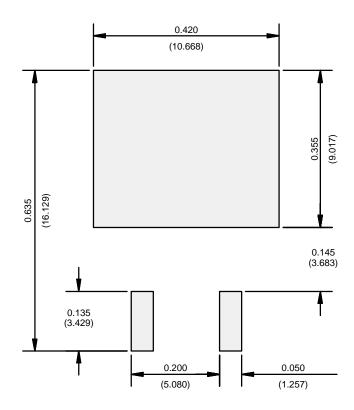
6. Datum A and B to be determined at datum plane H.

7. Outline conforms to JEDEC outline to TO-263AB.

<sup>1.</sup> Dimensioning and tolerancing per ASME Y14.5M-1994.



### **RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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