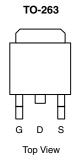


# FS4620-VB Datasheet N-Channel 200 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)	
200	0.048 at V <sub>GS</sub> = 10 V	40	
	0.060 at V <sub>GS</sub> = 6.5 V	35	

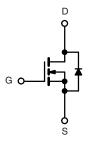


## **FEATURES**

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- · PWM Optimized for Fast Switching
- Compliant to RoHS Directive 2002/95/EC

### **APPLICATIONS**

- Isolated DC/DC Converters
  - Primary-Side Switch



N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	200	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I-	40	
Continuous Diain Current (1) = 175 C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	25	
Pulsed Drain Current		I <sub>DM</sub>	80	
Avalanche Current		I <sub>AR</sub>	20	
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	16.2	mJ
	T <sub>C</sub> = 25 °C	D.	200 <sup>b</sup>	w
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	P <sub>D</sub>	4.5	] vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount (TO-263) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	1	C/VV

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V			1	μΑ	
	I <sub>DSS</sub>	V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
		V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 15 \text{ V}, V_{GS} = 10 \text{ V}$	60			Α	
Drain-Source On-State Resistance <sup>a</sup>	(* /	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.048		Ω	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.150			
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C		0.180			
Drain-Source on State Resistance		$V_{GS}$ = 6.5 V, $I_D$ = 15 A		0.060			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	15			S	
Dynamic <sup>b</sup>	•			•			
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		2820		pF	
Output Capacitance	C <sub>oss</sub>			300			
Reverse Transfer Capacitance	C <sub>rss</sub>			120			
Total Gate Charge <sup>c</sup>	$Q_g$			35		nC	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		11			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			14			
Gate Resistance	$R_{G}$			2		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_L = 5 \Omega$ $I_D \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		35	55	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			40	60		
Fall Time <sup>c</sup>	t <sub>f</sub>			30	45		
Source-Drain Diode Ratings and Cha	racteristics (	T <sub>C</sub> = 25 °C) <sup>b</sup>					
Continuous Current	I <sub>S</sub>				40	۸	
Pulsed Current	I <sub>SM</sub>				60	A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			115	170	ns	
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, dl/dt = 100 A/μs		7.5	12	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			0.43	1.02	μС	

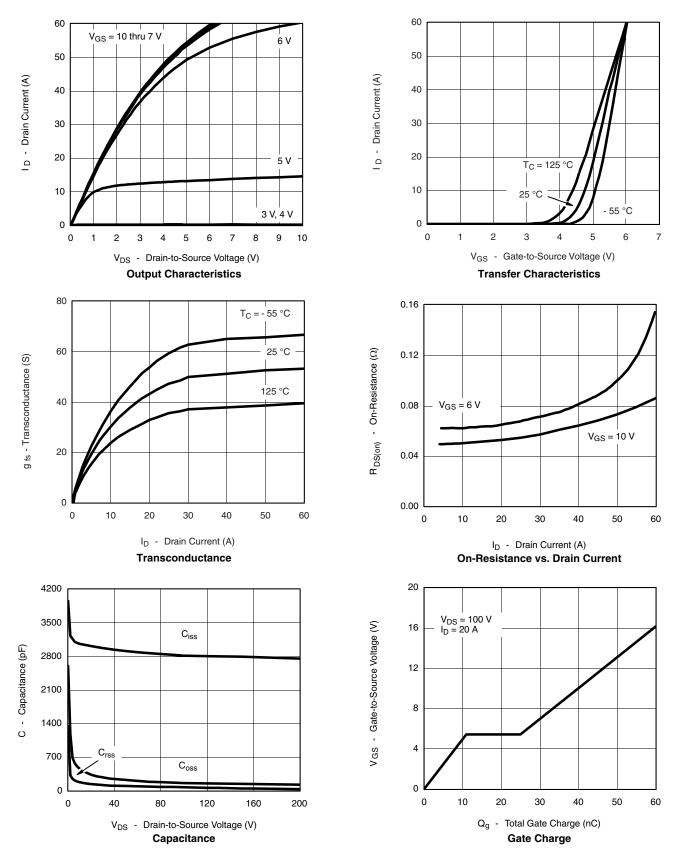
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



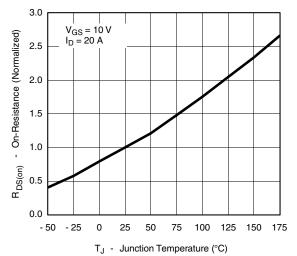
# TYPICAL CHARACTERISTICS (25 °C unless noted)



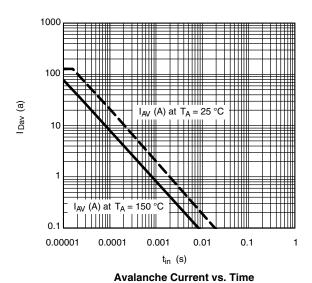
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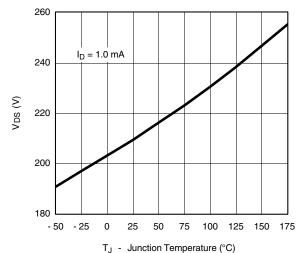
## TYPICAL CHARACTERISTICS (25 °C unless noted)



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

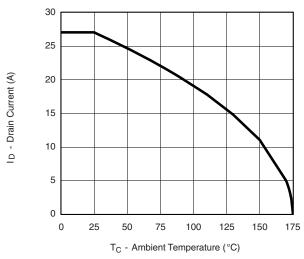


Drain Source Breakdown vs. Junction Temperature

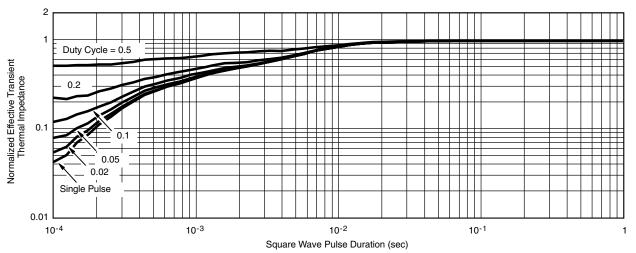
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### **THERMAL RATINGS**



Maximum Avalanche and Drain Current vs. Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case

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