

# FS4615-VB Datasheet N-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
150	0.035 at V <sub>GS</sub> = 10 V	45		
	0.042 at V <sub>GS</sub> = 7.5 V	42		

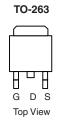
#### **FEATURES**

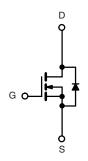
- Trench Power MOSFETs
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

· Primary Side Switch





N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_C = 25  ^{\circ}C$ , unless oth	erwise noted		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	150	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C		45	
Continuous Drain Current (1j = 175 C)	T <sub>C</sub> = 125 °C	l <sub>D</sub>	31	٦ ,
Pulsed Drain Current		I <sub>DM</sub>	140	A
Avalanche Current		I <sub>AR</sub>	50	
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	80	mJ
M	T <sub>C</sub> = 25 °C	В	160 <sup>b</sup>	14/
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	$ P_D$ $-$	3.7	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>sta</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount TO-263°)	R <sub>thJA</sub>	40	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.9	

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

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1



<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	150			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	4		6	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	80			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		0.035			
		$V_{GS} = 7.5 \text{ V}, I_D = 10 \text{ A}$		0.042		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C		0.060			
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 175 °C		0.080			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	10			S	
Dynamic <sup>b</sup>	•			•			
Input Capacitance	C <sub>iss</sub>			2200		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		290			
Reverse Transfer Capacitance	C <sub>rss</sub>			190			
Gate Resistance	$R_{g}$			2		Ω	
Total Gate Charge <sup>c</sup>	$Q_g$			38	60		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		13		nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			13			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 75 \text{ V}, R_{L} = 1.80 \Omega$		130	200		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 40 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>			90	140		
Source-Drain Diode Ratings and Characteristics T <sub>C</sub> = 25 °C <sup>b</sup>							
Continuous Current	I <sub>S</sub>				40	_	
Pulsed Current	I <sub>SM</sub>				80	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 40 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			100	150	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 40 A, dl/dt = 100 A/μs		5	8	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			0.25	0.6	μC	

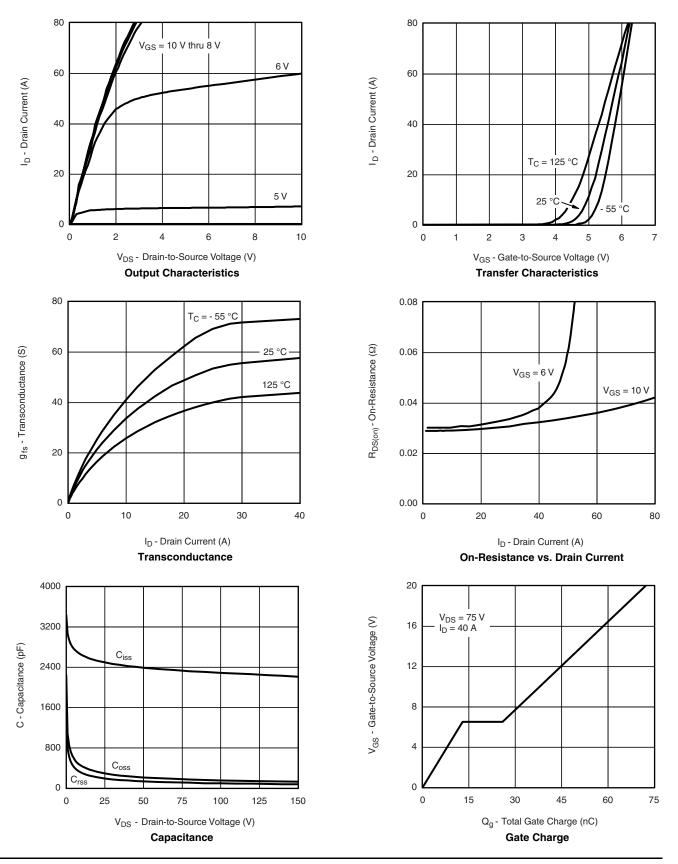
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

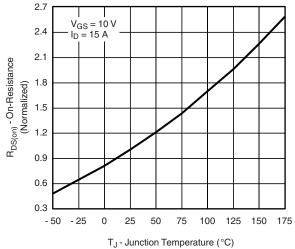


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



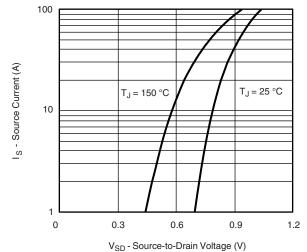


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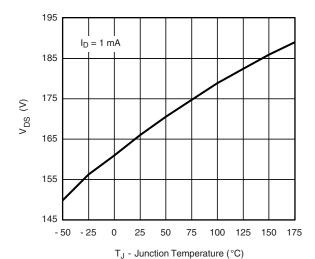




On-Resistance vs. Junction Temperature



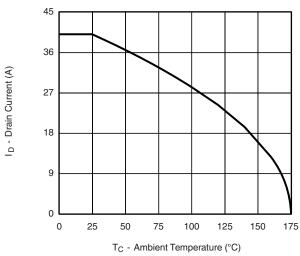
Source-Drain Diode Forward Voltage

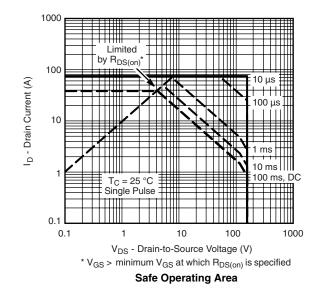


Drain Source Breakdown vs. Junction Temperature

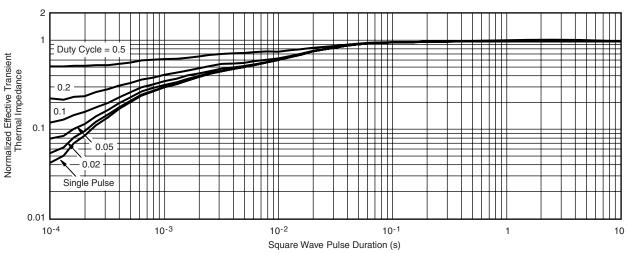


#### THERMAL RATINGS





Maximum Avalanche and Drain Current vs. Case Temperature



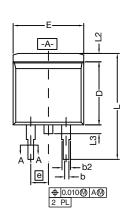
Normalized Thermal Transient Impedance, Junction-to-Case

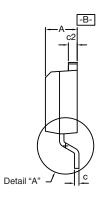
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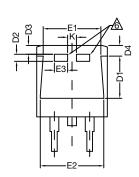
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# TO-263 (D<sup>2</sup>PAK): 3-LEAD

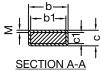








DETAIL A (ROTATED 90°)



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- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

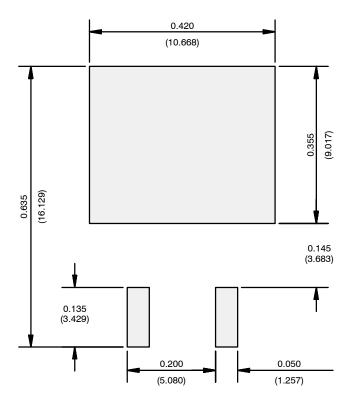
6 This feature is for thick lead.

		INCHES		MILLIN	METERS
DIM.		MIN.	MAX.	MIN.	MAX.
Α		0.160	0.190	4.064	4.826
	b	0.020	0.039	0.508	0.990
	b1	0.020	0.035	0.508	0.889
	b2	0.045	0.055	1.143	1.397
c*	Thin lead	0.013	0.018	0.330	0.457
C	Thick lead	0.023	0.028	0.584	0.711
c1	Thin lead	0.013	0.017	0.330	0.431
CI	Thick lead	0.023	0.027	0.584	0.685
	c2	0.045	0.055	1.143	1.397
	D	0.340	0.380	8.636	9.652
	D1	0.220	0.240	5.588	6.096
	D2	0.038	0.042	0.965	1.067
	D3	0.045	0.055	1.143	1.397
	D4	0.044	0.052	1.118	1.321
	E	0.380	0.410	9.652	10.414
	E1	0.245	-	6.223	-
	E2	0.355	0.375	9.017	9.525
	E3	0.072	0.078	1.829	1.981
	е		0.100 BSC		BSC
	K	0.045	0.055	1.143	1.397
	L	0.575	0.625	14.605	15.875
L1		0.090	0.110	2.286	2.794
L2		0.040	0.055	1.016	1.397
L3		0.050	0.070	1.270	1.778
L4		0.010 BSC		0.254 BSC	
M		-	0.002		0.050
ECN: T13-0707-Rev. K. 30-Sep-13					

DWG: 5843



### RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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