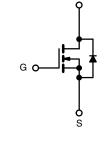


## B270L-VB Datasheet N-Channel 80 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)	
80	0.0050 at $V_{GS}$ = 10 V	215	94	
	0.0095 at V <sub>GS</sub> = 7.5 V	205	94	





D

N-Channel MOSFET

#### FEATURES

- Trench power MOSFET
- Maximum 175 °C junction temperature
- Very low  $Q_{gd}$  reduces power loss from passing through  $V_{plateau}$
- 100 %  $R_g$  and UIS tested



HALOGEN

FREE

#### **APPLICATIONS**

- Power supply
  - Secondary synchronous rectification
- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_c = 25 \degree C$ , unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	80	v	
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
	T <sub>C</sub> = 25 °C		215		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>C</sub> = 70 °C	- I <sub>D</sub>	120 <sup>d</sup>		
Pulsed Drain Current (t = 100 µs)	I <sub>DM</sub>	600	A		
Avalanche Current	I <sub>AS</sub>	70			
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	245	mJ	
Martin an Daria a Disata ating a	T <sub>C</sub> = 25 °C	D	375 <sup>b</sup>	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	– P <sub>D</sub>	125 <sup>b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W		
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.4			

#### Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. Package limited.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu$ A	80	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	2	-	4	v
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V	-	-	± 250	nA
		$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 80 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C	-	-	150	
		$V_{DS}$ = 80 V, $V_{GS}$ = 0 V, $T_J$ = 175 $^\circ C$	-	-	5	mA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 10 \text{ V},  V_{GS} = 10 \text{ V}$	120	-	-	А
Drain-Source On-State Resistance a	P	$V_{GS}$ = 10 V, I <sub>D</sub> = 30 A	-	0.0050	-	0
Drain-Source On-State Resistance ~	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	0.0095	-	Ω
Forward Transconductance <sup>a</sup>	<b>g</b> <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	82	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 40 V, f = 1 MHz	-	7910	-	pF
Output Capacitance	C <sub>oss</sub>		-	3250	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	348	-	
Total Gate Charge <sup>c</sup>	Qg	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A	-	94	141	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>		-	31	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	10	-	
Gate Resistance	Rg	f = 1 MHz	0.28	1.4	2.8	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	24	40	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 40 V, $R_L$ = 4 $\Omega$	-	24	40	20
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 10$ Å, $V_{GEN} = 10$ V, $R_g = 1$ $\Omega$	-	34	60	ns
Fall Time <sup>c</sup>	t <sub>f</sub>		-	14	28	
Drain-Source Body Diode Ratings ar	nd Characteris	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)				
Pulsed Current (t = 100 µs)	I <sub>SM</sub>		-	-	250	А
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_F = 10 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.5	V
Reverse Recovery Time	t <sub>rr</sub>		-	126	190	ns
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	I <sub>F</sub> = 34 A, di/dt = 100 A/μs	-	5	10	А
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.315	0.475	μC

Notes

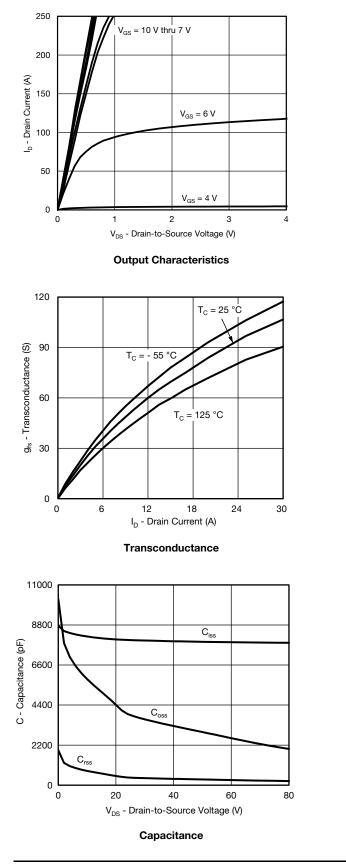
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

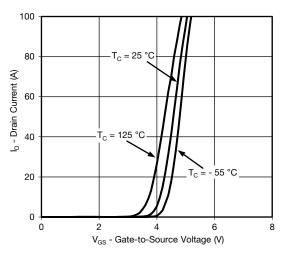
semi

www.VBsemi.com

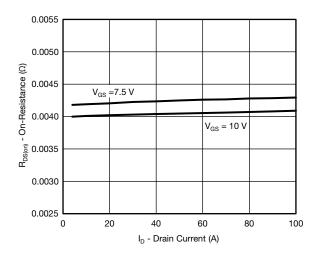


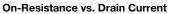
## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

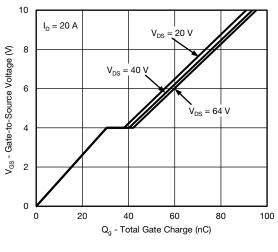




**Transfer Characteristics** 



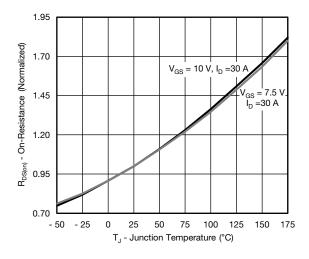




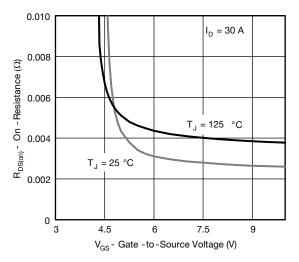




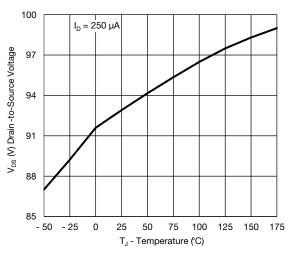
## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



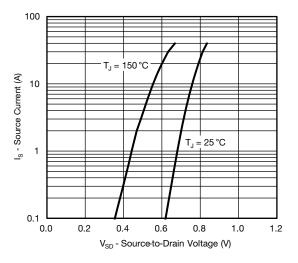
**On-Resistance vs. Junction Temperature** 



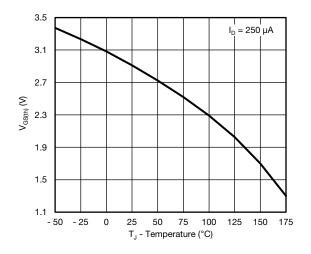
On-Resistance vs. Gate-to-Source Voltage



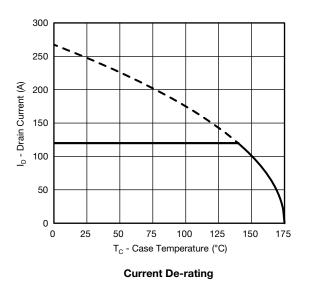
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage

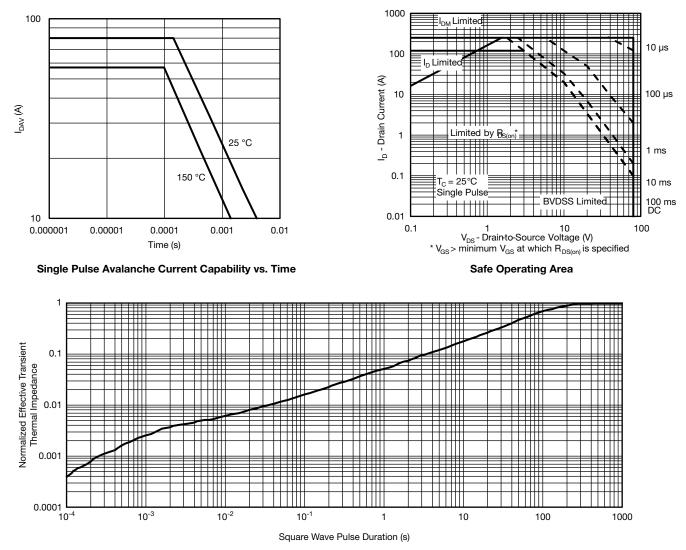








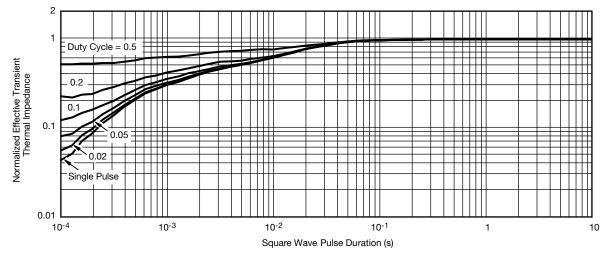
## **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

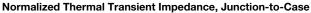


Normalized Thermal Transient Impedance, Junction-to-Ambient



#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)





#### Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

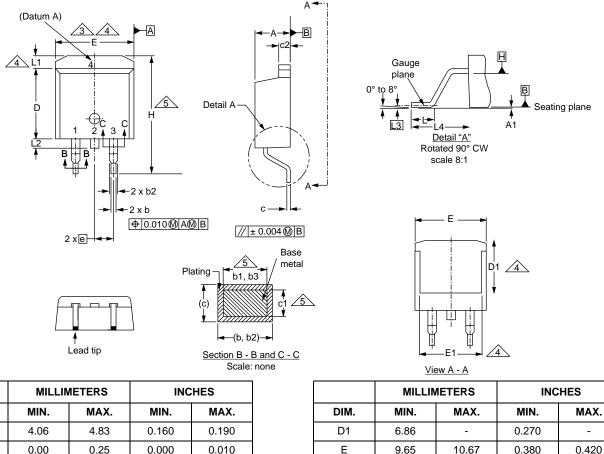
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

## **B270L-VB**



#### **TO-263AB**



E1

е

Н

L

L1

L2

L3

L4

6.22

14.61

1.78

-

-

4.78

-

15.88

2.79

1.65

1.78

5.28

2.54 BSC

0.25 BSC

0.245

0.575

0.070

-

-

0.188

\_

0.625

0.110

0.066

0.070

0.208

0.100 BSC

0.010 BSC

A	4.06	4.83	0.160	0.190
A1	0.00	0.25	0.000	0.010
b	0.51	0.99	0.020	0.039
b1	0.51	0.89	0.020	0.035
b2	1.14	1.78	0.045	0.070
b3	1.14	1.73	0.045	0.068
с	0.38	0.74	0.015	0.029
c1	0.38	0.58	0.015	0.023
c2	1.14	1.65	0.045	0.065
D	8.38	9.65	0.330	0.380
ECN: S-82110-Rev. A, 15-Sep-08				

DWG:	597	70
Notes		

DIM.

1. Dimensioning and tolerancing per ASME Y14.5M-1994.

2. Dimensions are shown in millimeters (inches).

3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.

4. Thermal PAD contour optional within dimension E, L1, D1 and E1.

5. Dimension b1 and c1 apply to base metal only.

6. Datum A and B to be determined at datum plane H.

7. Outline conforms to JEDEC outline to TO-263AB.



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