

AP6679GS-HF-VB Datasheet P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)	
- 30	0.008 at V_{GS} = - 10 V	- 75	56 nC	
	0.011 at V _{GS} = - 4.5 V	- 65	30 110	

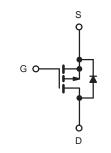
FEATURES

- Halogen-free
- Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- Load Switch
- Notebook Adaptor Switch





P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage		V _{GS}	± 20	V	
	T _C = 25 °C		- 75		
Continuous Drain Current ($T_1 = 150 \text{ °C}$)	T _C = 70 °C		- 65		
Continuous Drain Current $(T_j = 150^{\circ} C)$	T _A = 25 °C	I _D	-55 ^{a, b}		
	T _A = 70 °C		-45 ^{a, b}	^	
Pulsed Drain Current		I _{DM}	- 200	Α	
	T _C = 25 °C	1	- 4.1		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 2.2 ^{a, b}		
Avalanche Current	L = 0.1 mH	I _{AS}	- 75		
L = 0 Single-Pulse Avalanche Energy		E _{AS}	280	mJ	
	T _C = 25 °C		250		
Maximum Power Dissipation	T _C = 70 °C	Ъ	205	10/	
	T _A = 25 °C	P _D	3.7 ^{a, b}	W	
	T _A = 70 °C		2.7 ^{a, b}		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	38	46	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	20	25		

Notes:

b. t = 10 s.

c. Maximum under Steady State conditions is 85 $^{\circ}\text{C/W}.$

d. Based on T_C = 25 °C.

a. Surface mounted on 1" x 1" FR4 board.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	Cymbol			тур.	max.	0111	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$		00	- 34		mV/ °C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		5.3			
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1.0	0.0	- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 25 V$	1.0		± 100	nA	
Cale-Source Leanage	.635	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	- 30			Α	
	_	V _{GS} = - 10 V, I _D = - 10 A		0.008		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 8 A		0.011			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 10 A		28		S	
Dynamic ^b				11			
Input Capacitance	C _{iss}			4550		pF	
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		1455			
Reverse Transfer Capacitance	C _{rss}			570			
Total Gate Charge		V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 10 A		115			
	Q _g			56		_	
Gate-Source Charge	Q_{gs} $V_{DS} = -15 V, V_{GS} = -4.5 V, I_{D} = -10 A$		8		nC		
Gate-Drain Charge	Q _{gd}			22		ĺ	
Gate Resistance	R _g	f = 1 MHz	0.5	2.2	4.4	Ω	
Turn-On Delay Time	t _{d(on)}			13	25		
Rise Time	ťr	V_{DD} = - 15 V, R_L = 1.5 Ω $I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		12	24		
Turn-Off DelayTime	t _{d(off)}			40	70		
Fall Time	t _f	, i i i i i i i i i i i i i i i i i i i		9	18		
Turn-On Delay Time t _{d(on)}				48	80	ns	
Rise Time	t _r	V_{DD} = - 15 V, R _L = 1.5 Ω		92	160	-	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 10 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		34	60		
Fall Time	t _f			19	35		
Drain-Source Body Diode Characteris	stics						
Continous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 4.1	A	
Pulse Diode Forward Current	I _{SM}				- 60		
Body Diode Voltage	V _{SD}	I _S = - 3 A, V _{GS} = 0 V		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			27	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	l _F = - 10 A, dl/dt = 100 A/μs, T _J = 25 °C		16	27	nC	
Reverse Recovery Fall Time	t _a			12			
Reverse Recovery Rise Time	t _b			15		ns	

Notes:

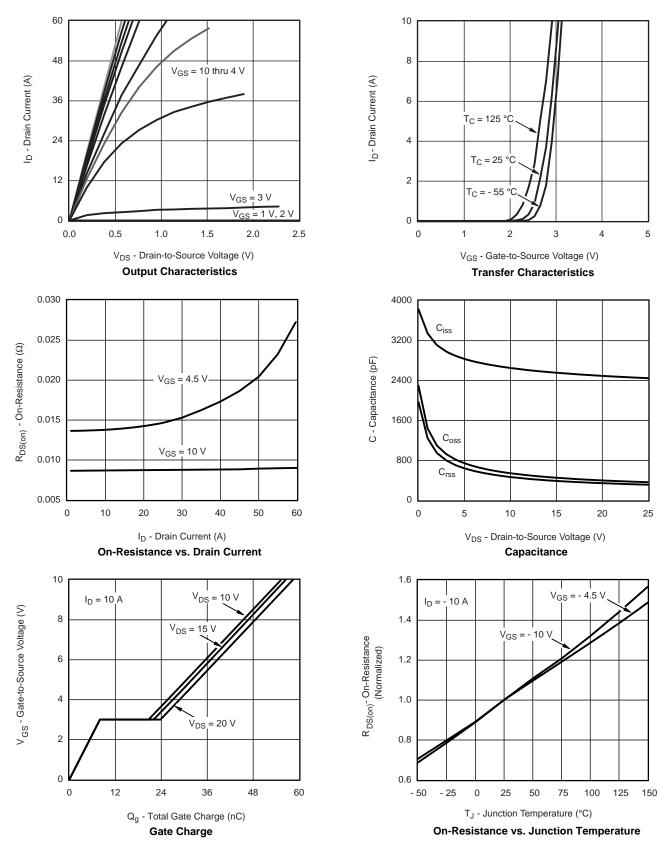
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

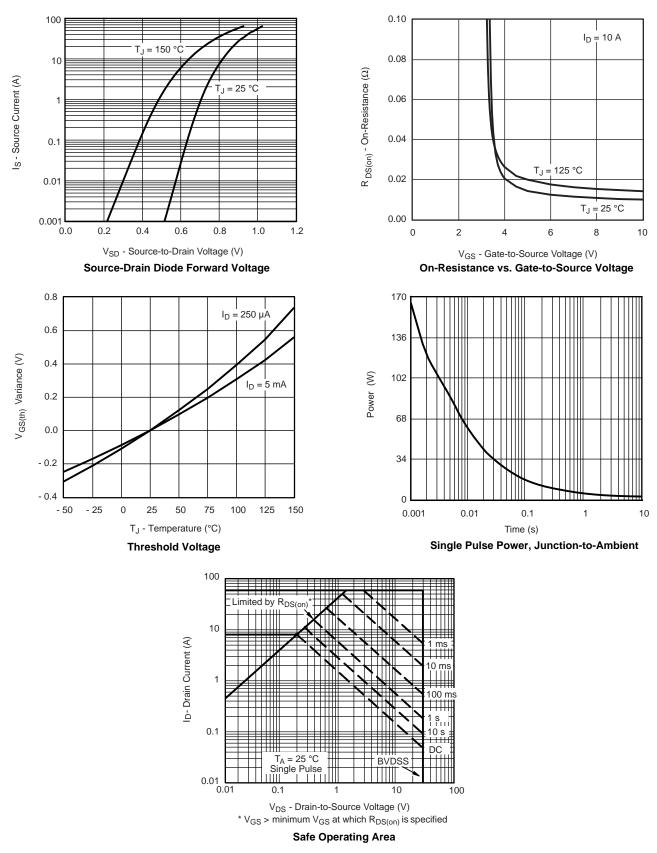






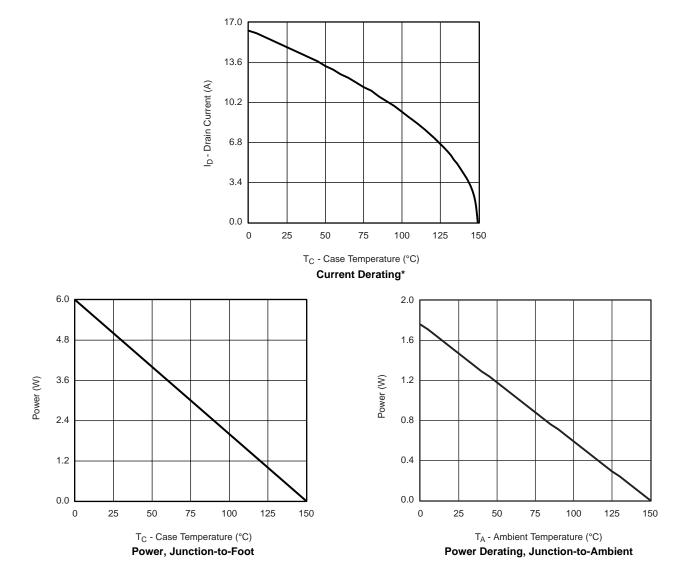








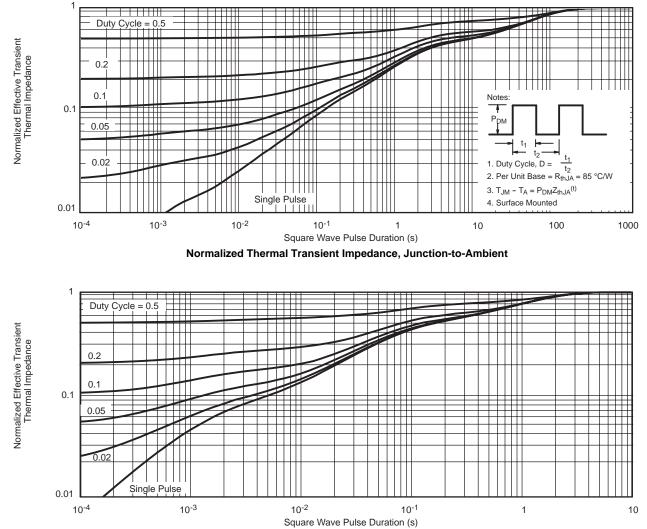
MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



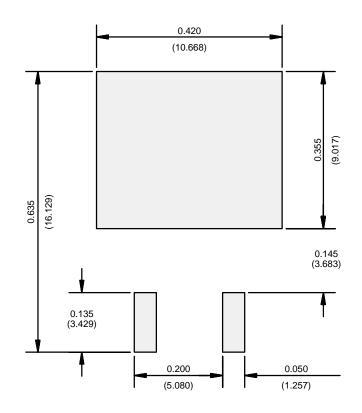
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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