

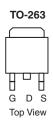
# 3N10L16-VB Datasheet N-Channel 100-V (D-S) MOSFET

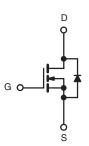
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
100	0.010 at V <sub>GS</sub> = 10 V	100			
	0.023 at V <sub>GS</sub> = 4.5 V	85			

#### **FEATURES**

- Trench Power MOSFET
- 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage			100			
Gate-Source Voltage			± 20	V		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I <sub>D</sub>	100	A		
	T <sub>C</sub> = 125 °C		75 <sup>a</sup>			
Pulsed Drain Current	$I_{DM}$	300	A			
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	75			
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.1 IIII1	E <sub>AS</sub>	280	mJ		
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C (TO-220AB and TO-263)	P <sub>D</sub>	250 <sup>c</sup>	W		
	T <sub>A</sub> = 25 °C (TO-263) <sup>d</sup>	ט י	3.75	VV		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Limit	Unit			
Junction-to-Ambient	PCB Mount (TO-263) <sup>d</sup>	R <sub>thJA</sub>	40				
Junction-to-Ambient	Free Air (TO-220AB)	' 'thJA	62.5	°C/W			
Junction-to-Case		R <sub>thJC</sub>	0.6				

#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$			4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	1	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.010		1	
	<sub>D</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		0.023		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.020			
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.030			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			6550		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		665			
Reverse Transfer Capacitance	C <sub>rss</sub>			265			
Total Gate Charge <sup>c</sup>	$Q_g$			105	160		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 85 \text{ A}$		17		nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$	1		23		1	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			12	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 0.6 \Omega$		90	135	ns	
Turn-Off DelayTime <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 85 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		55	85		
Fall Time <sup>c</sup>	t <sub>f</sub>	1		130	195		
Source-Drain Diode Ratings and Char	racteristics T <sub>C</sub>	= 25 °C <sup>b</sup>					
Continuous Current	Is				85	^	
Pulsed Current	I <sub>SM</sub>				240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			85	140	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, dI/dt = 100 A/μs		4.5	7	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	_		0.17	0.35	μС	

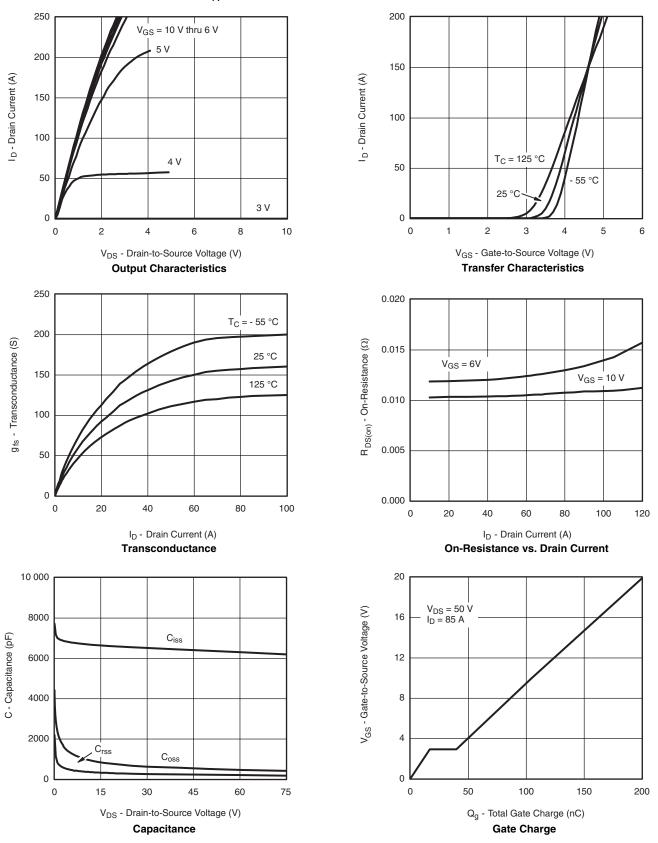
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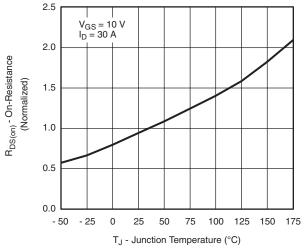


**TYPICAL CHARACTERISTICS**  $T_A = 25 \, ^{\circ}C$ , unless otherwise noted

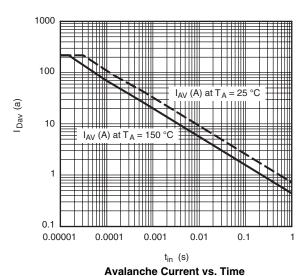




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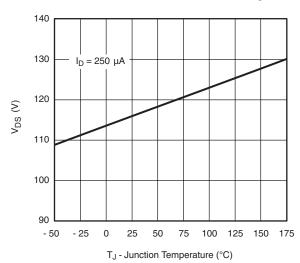


On-Resistance vs. Junction Temperature



T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C T<sub>J</sub>

Source-Drain Diode Forward Voltage

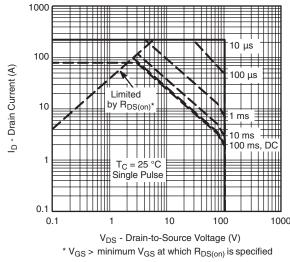


T<sub>J</sub> - Drain-Source Breakdown vs. Junction-Temperature



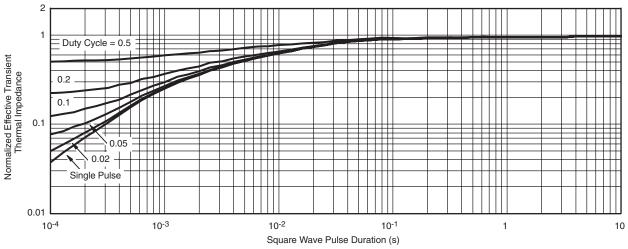
#### **THERMAL RATINGS**





**Maximum Avalanche and Drain Current** vs. Case Temperature

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788

5



**MILLIMETERS** 

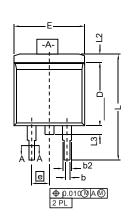
MAX.

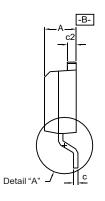
4.826

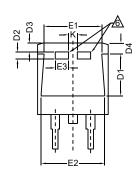
MIN.

4.064

# TO-263 (D<sup>2</sup>PAK): 3-LEAD

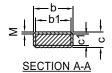








**DETAIL A (ROTATED 90°)** 



b		0.020	0.039	0.508	0.990
b1		0.020	0.035	0.508	0.889
b2		0.045	0.055	1.143	1.397
c*	Thin lead	0.013	0.018	0.330	0.457
c1	Thick lead	0.023	0.028	0.584	0.711
	Thin lead	0.013	0.017	0.330	0.431
Ci	Thick lead	0.023	0.027	0.584	0.685
	c2	0.045	0.055	1.143	1.397
	D	0.340	0.380	8.636	9.652
D1		0.220	0.240	5.588	6.096
D2		0.038	0.042	0.965	1.067
D3		0.045	0.055	1.143	1.397
D4		0.044	0.052	1.118	1.321
E		0.380	0.410	9.652	10.414
	E1	0.245	-	6.223	-
	E2	0.355	0.375	9.017	9.525
E3		0.072	0.078	1.829	1.981
е		0.100 BSC		2.54 BSC	
K		0.045	0.055	1.143	1.397
	L	0.575	0.625	14.605	15.875
L1		0.090	0.110	2.286	2.794

**INCHES** 

MAX.

0.190

MIN.

0.160

ECN: T13-0707-Rev. K, 30-Sep-13

0.040

0.050

0.010 BSC

0.055

0.070

0.002

1.016

1.270

0.254 BSC

1.397

1.778

0.050

DWG: 5843

L2

L3

L4

Μ

DIM.

Α

#### Notos

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by  $\;$  max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB.
  Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

  6. This feature is for thick lead.



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