

## 30P10GS-VB Datasheet

### P-Channel 100 V (D-S) MOSFET

#### PRODUCT SUMMARY

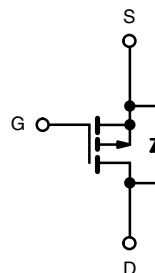
| $V_{DS}$ (V) | $R_{DS(on)}$ ( $\Omega$ )   | $I_D$ (A) | $Q_g$ (Typ.) |
|--------------|-----------------------------|-----------|--------------|
| - 100        | 0.040 at $V_{GS} = - 10$ V  | - 37      | 54 nC        |
|              | 0.050 at $V_{GS} = - 4.5$ V | - 32      |              |

#### FEATURES

- Trench Power MOSFET



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



P-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

| Parameter                                                           |                          | Symbol         | Limit                  | Unit             |
|---------------------------------------------------------------------|--------------------------|----------------|------------------------|------------------|
| Drain-Source Voltage                                                |                          | $V_{DS}$       | - 100                  | V                |
| Gate-Source Voltage                                                 |                          | $V_{GS}$       | $\pm 20$               |                  |
| Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>b</sup> | $T_C = 25^\circ\text{C}$ | $I_D$          | - 37                   | A                |
|                                                                     | $T_C = 70^\circ\text{C}$ |                | - 29.5                 |                  |
|                                                                     | $T_A = 25^\circ\text{C}$ |                | - 10 <sup>b, c</sup>   |                  |
|                                                                     | $T_A = 70^\circ\text{C}$ |                | - 8.2 <sup>b, c</sup>  |                  |
| Pulsed Drain Current                                                |                          | $I_{DM}$       | - 150                  |                  |
| Continuous Source Current (Diode Conduction)                        | $T_C = 25^\circ\text{C}$ | $I_S$          | - 50 <sup>a</sup>      |                  |
|                                                                     | $T_A = 25^\circ\text{C}$ |                | - 6.75 <sup>b, c</sup> |                  |
| Avalanche Current                                                   | L = 0.1 mH               | $I_{AS}$       | - 35                   | mJ               |
| Single Pulse Avalanche Energy                                       |                          | $E_{AS}$       | 61                     |                  |
| Maximum Power Dissipation                                           | $T_C = 25^\circ\text{C}$ | $P_D$          | 113.6                  | W                |
|                                                                     | $T_C = 70^\circ\text{C}$ |                | 72.7                   |                  |
|                                                                     | $T_A = 25^\circ\text{C}$ |                | 6.9 <sup>b, c</sup>    |                  |
|                                                                     | $T_A = 70^\circ\text{C}$ |                | 4.4 <sup>b, c</sup>    |                  |
| Operating Junction and Storage Temperature Range                    |                          | $T_J, T_{stg}$ | - 55 to 150            | $^\circ\text{C}$ |

#### THERMAL RESISTANCE RATINGS

| Parameter                |                                 | Symbol     | Limit | Unit               |
|--------------------------|---------------------------------|------------|-------|--------------------|
| Junction-to-Ambient      | PCB Mount (TO-263) <sup>c</sup> | $R_{thJA}$ | 40    | $^\circ\text{C/W}$ |
| Junction-to-Case (Drain) |                                 | $R_{thJC}$ | 2.1   |                    |

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

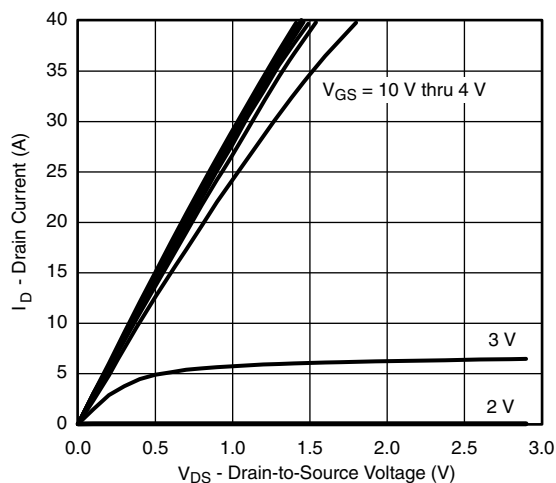
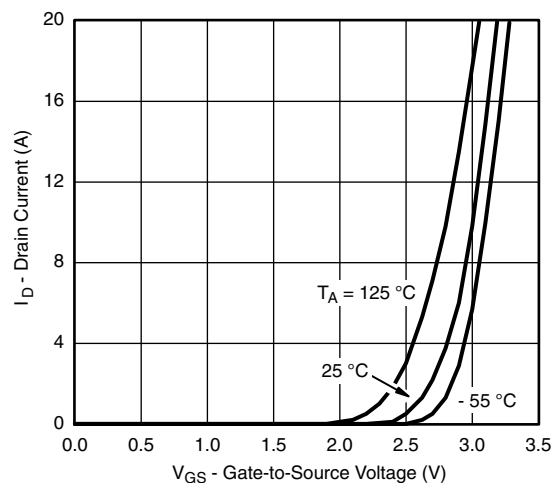
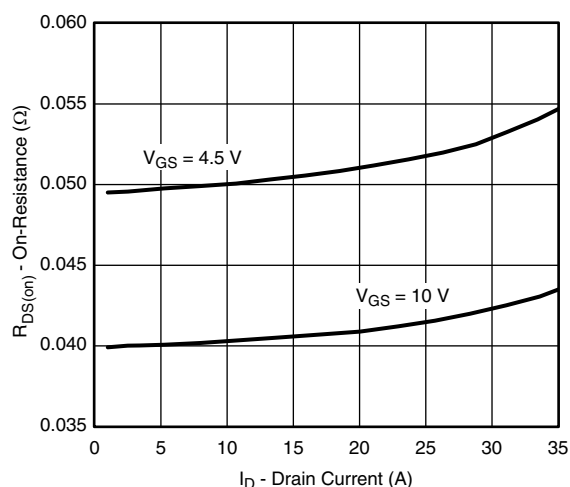
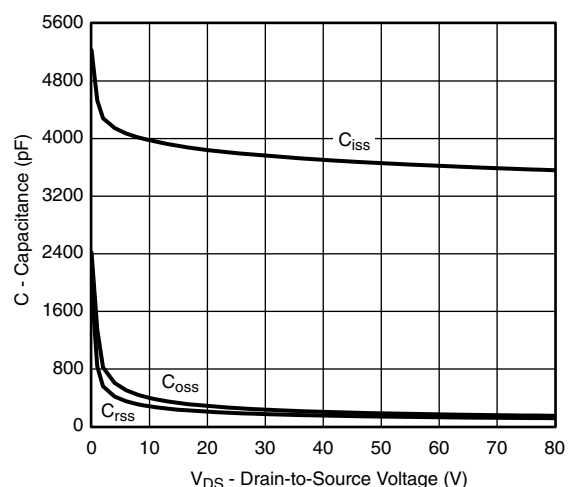
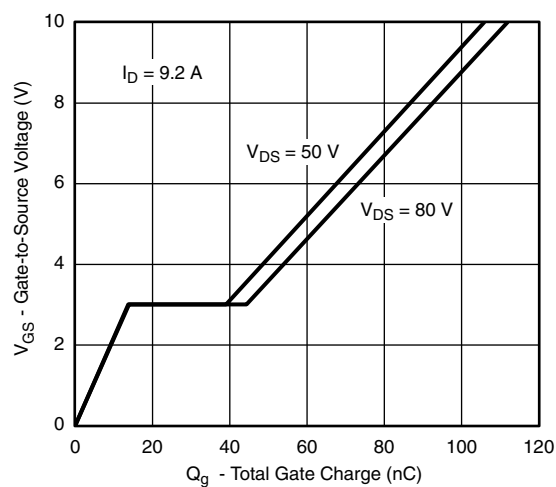
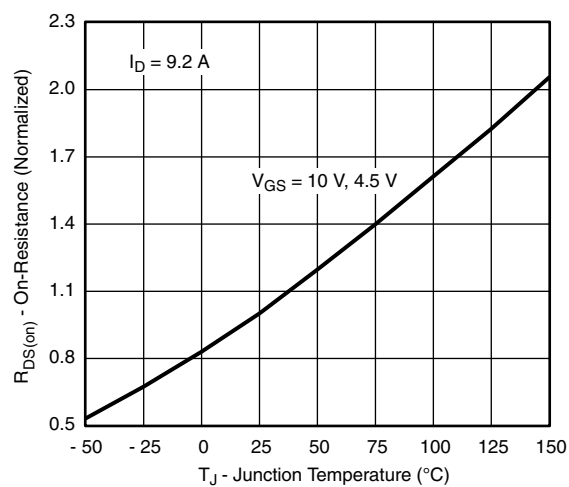
| SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted) |                                      |                                                                                                                                |       |       |       |       |
|-----------------------------------------------------------------|--------------------------------------|--------------------------------------------------------------------------------------------------------------------------------|-------|-------|-------|-------|
| Parameter                                                       | Symbol                               | Test Conditions                                                                                                                | Min.  | Typ.  | Max.  | Unit  |
| Static                                                          |                                      |                                                                                                                                |       |       |       |       |
| Drain-Source Breakdown Voltage                                  | V <sub>DS</sub>                      | V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA                                                                               | - 100 |       |       | V     |
| V <sub>DS</sub> Temperature Coefficient                         | ΔV <sub>DS</sub> /T <sub>J</sub>     | I <sub>D</sub> = - 250 μA                                                                                                      |       | - 109 |       | mV/°C |
| V <sub>GS(th)</sub> Temperature Coefficient                     | ΔV <sub>GS(th)</sub> /T <sub>J</sub> |                                                                                                                                |       | 5.9   |       |       |
| Gate-Source Threshold Voltage                                   | V <sub>GS(th)</sub>                  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA                                                                  | - 1   |       | - 3   | V     |
| Gate-Source Leakage                                             | I <sub>GSS</sub>                     | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V                                                                                |       |       | ± 100 | nA    |
| Zero Gate Voltage Drain Current                                 | I <sub>DSS</sub>                     | V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V                                                                               |       |       | - 1   | μA    |
|                                                                 |                                      | V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C                                                       |       |       | - 10  |       |
| On-State Drain Current <sup>a</sup>                             | I <sub>D(on)</sub>                   | V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = - 10 V                                                                                | - 40  |       |       | A     |
| Drain-Source On-State Resistance <sup>a</sup>                   | R <sub>DS(on)</sub>                  | V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 9.2 A                                                                             |       | 0.040 |       | Ω     |
|                                                                 |                                      | V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7.7 A                                                                            |       | 0.050 |       |       |
| Forward Transconductance <sup>a</sup>                           | g <sub>fs</sub>                      | V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 9.2 A                                                                             |       | 38    |       | S     |
| Dynamic <sup>b</sup>                                            |                                      |                                                                                                                                |       |       |       |       |
| Input Capacitance                                               | C <sub>iss</sub>                     | V <sub>DS</sub> = - 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz                                                                     |       | 3800  |       | pF    |
| Output Capacitance                                              | C <sub>oss</sub>                     |                                                                                                                                |       | 185   |       |       |
| Reverse Transfer Capacitance                                    | C <sub>rss</sub>                     |                                                                                                                                |       | 135   |       |       |
| Total Gate Charge                                               | Q <sub>g</sub>                       | V <sub>DS</sub> = - 50 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 9.2 A                                                   |       | 106   | 160   | nC    |
|                                                                 |                                      | V <sub>DS</sub> = - 50 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 9.2 A                                                  |       | 54    | 81    |       |
| Gate-Source Charge                                              | Q <sub>gs</sub>                      |                                                                                                                                |       | 14    |       |       |
| Gate-Drain Charge                                               | Q <sub>gd</sub>                      |                                                                                                                                |       | 26    |       |       |
| Gate Resistance                                                 | R <sub>g</sub>                       | f = 1 MHz                                                                                                                      |       | 4     |       | Ω     |
| Turn-On Delay Time                                              | t <sub>d(on)</sub>                   | V <sub>DD</sub> = - 50 V, R <sub>L</sub> = 6.5 Ω<br>I <sub>D</sub> ≅ - 7.7 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω  |       | 15    | 25    | ns    |
| Rise Time                                                       | t <sub>r</sub>                       |                                                                                                                                |       | 20    | 30    |       |
| Turn-Off Delay Time                                             | t <sub>d(off)</sub>                  |                                                                                                                                |       | 110   | 165   |       |
| Fall Time                                                       | t <sub>f</sub>                       |                                                                                                                                |       | 100   | 150   |       |
| Turn-On Delay Time                                              | t <sub>d(on)</sub>                   | V <sub>DD</sub> = - 50 V, R <sub>L</sub> = 6.5 Ω<br>I <sub>D</sub> ≅ - 7.7 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 1 Ω |       | 42    | 65    | ns    |
| Rise Time                                                       | t <sub>r</sub>                       |                                                                                                                                |       | 160   | 240   |       |
| Turn-Off Delay Time                                             | t <sub>d(off)</sub>                  |                                                                                                                                |       | 100   | 150   |       |
| Fall Time                                                       | t <sub>f</sub>                       |                                                                                                                                |       | 100   | 150   |       |
| Drain-Source Body Diode Characteristics                         |                                      |                                                                                                                                |       |       |       |       |
| Continuous Source-Drain Diode Current                           | I <sub>S</sub>                       | T <sub>C</sub> = 25 °C                                                                                                         |       |       | - 50  | A     |
| Pulse Diode Forward Current <sup>a</sup>                        | I <sub>SM</sub>                      |                                                                                                                                |       |       | - 40  |       |
| Body Diode Voltage                                              | V <sub>SD</sub>                      | I <sub>S</sub> = - 7.7 A                                                                                                       |       | - 0.8 | - 1.2 | V     |
| Body Diode Reverse Recovery Time                                | t <sub>rr</sub>                      | I <sub>F</sub> = - 7.7 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C                                                             |       | 60    | 90    | ns    |
| Body Diode Reverse Recovery Charge                              | Q <sub>rr</sub>                      |                                                                                                                                |       | 150   | 225   | nC    |
| Reverse Recovery Fall Time                                      | t <sub>a</sub>                       |                                                                                                                                |       | 46    |       | ns    |
| Reverse Recovery Rise Time                                      | t <sub>b</sub>                       |                                                                                                                                |       | 14    |       |       |

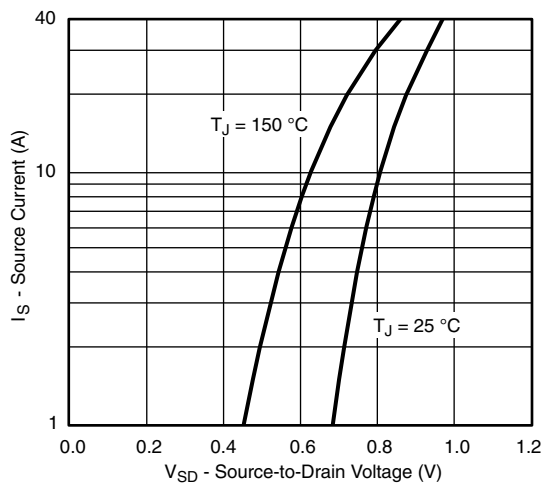
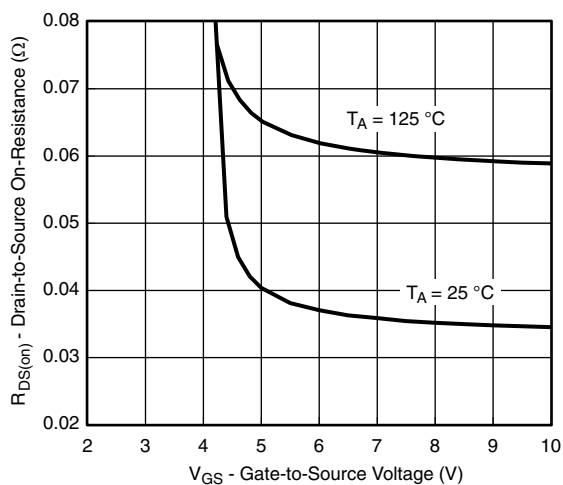
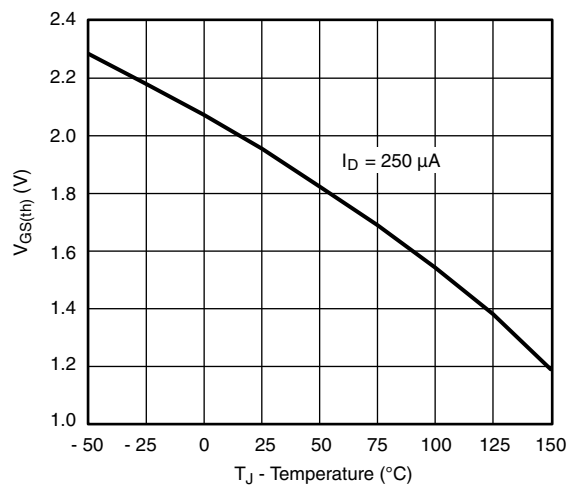
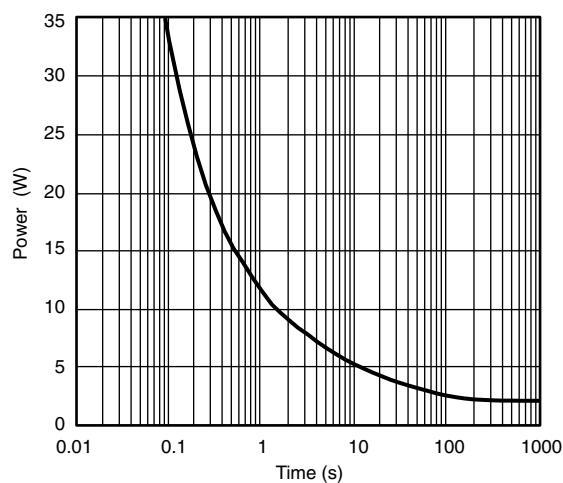
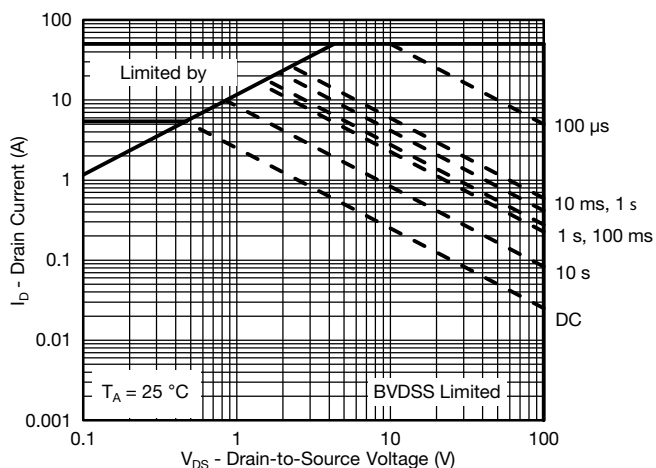
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

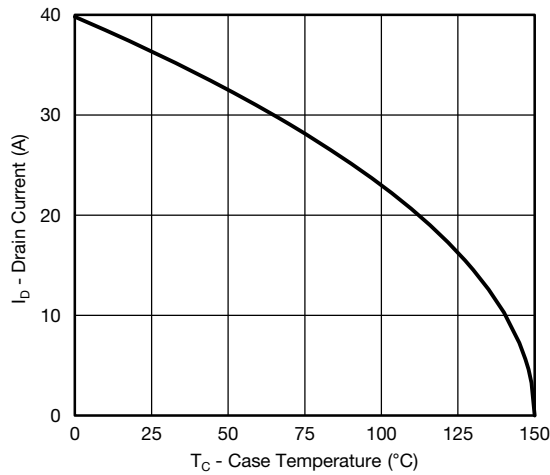
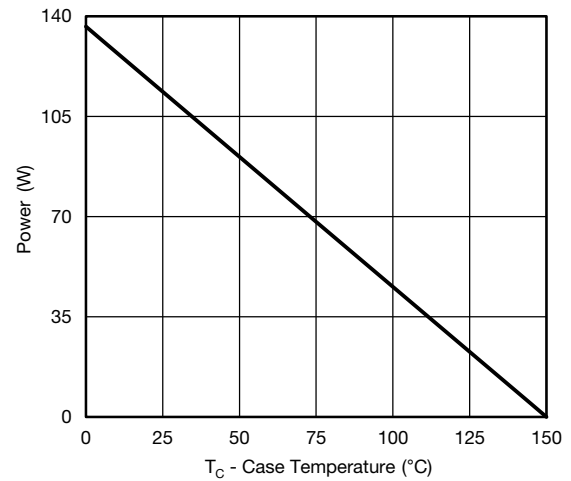
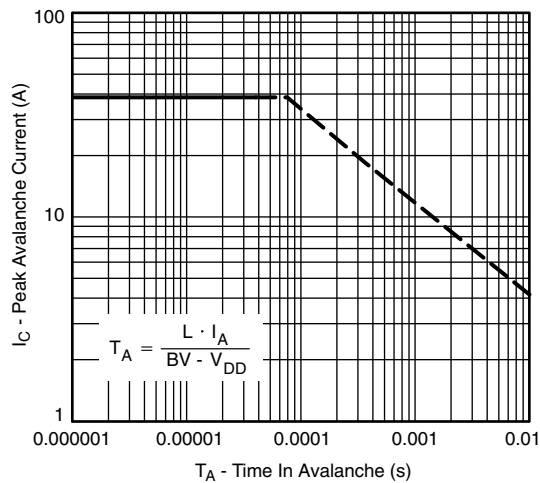
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current and Gate Voltage**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

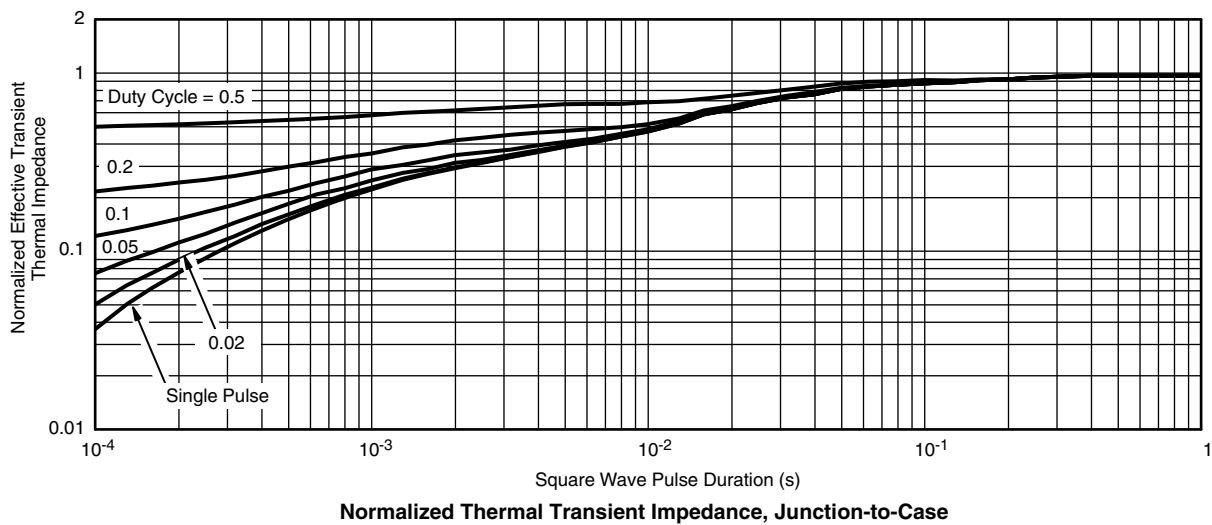
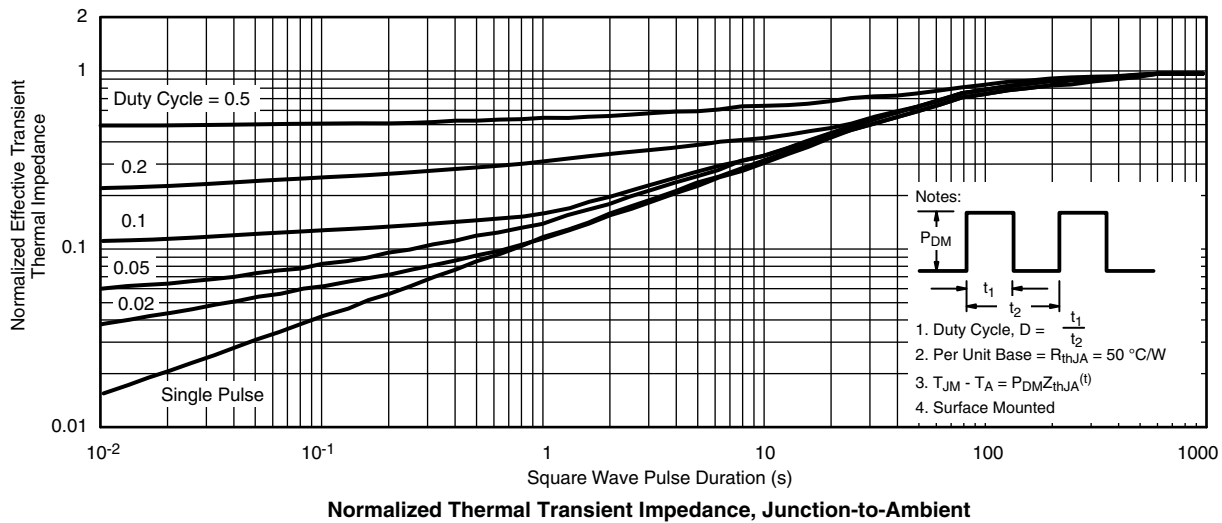
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Single Pulse Power, Junction-to-Ambient**


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Current Derating\***

**Single Pulse Power, Junction-to-Ambient**

**Single Pulse Avalanche Capability**

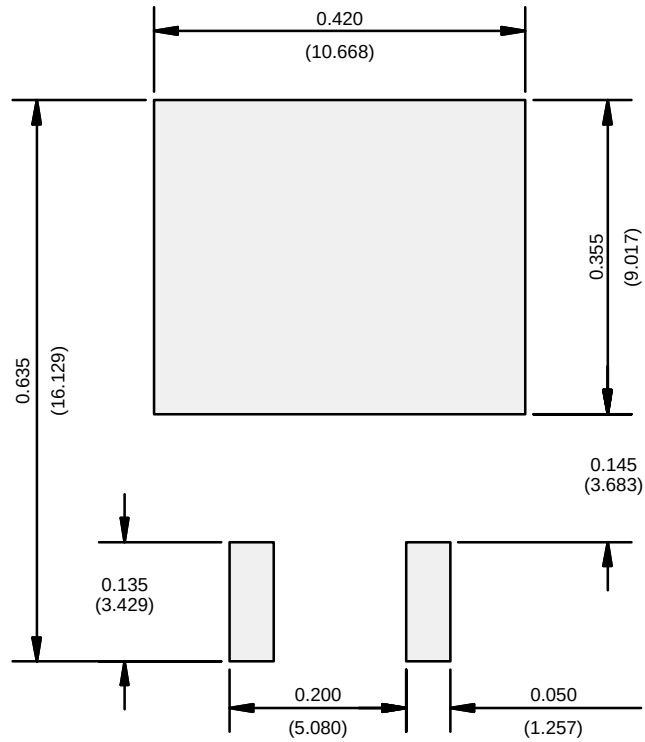
\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)


[illegible]

|      | MILLIMETERS |       | INCHES    |       |
|------|-------------|-------|-----------|-------|
| DIM. | MIN.        | MAX.  | MIN.      | MAX.  |
| D1   | 6.86        | -     | 0.270     | -     |
| E    | 9.65        | 10.67 | 0.380     | 0.420 |
| E1   | 6.22        | -     | 0.245     | -     |
| e    | 2.54 BSC    |       | 0.100 BSC |       |
| H    | 14.61       | 15.88 | 0.575     | 0.625 |
| L    | 1.78        | 2.79  | 0.070     | 0.110 |
| L1   | -           | 1.65  | -         | 0.066 |
| L2   | -           | 1.78  | -         | 0.070 |
| L3   | 0.25 BSC    |       | 0.010 BSC |       |
| L4   | 4.78        | 5.28  | 0.188     | 0.208 |

1. Dimensioning and tolerancing per ASME Y14.5M-1994.
2. Dimensions are shown in millimeters (inches).
3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
5. Dimension b1 and c1 apply to base metal only.
6. Datum A and B to be determined at datum plane H.
7. Outline conforms to JEDEC outline to TO-263AB.

**RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**

Recommended Minimum Pads  
Dimensions in Inches/(mm)



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