

IRF1405ZL-7PPBF-VB Datasheet N-Channel 60 V (D-S) 175 °C MOSFET

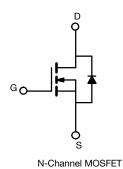
PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.00163			
I _D (A)	150			
Configuration	Single			
Package	TO-263-7L			

FEATURES

- Trench power MOSFET
- Package with low thermal resistance
- 100 % R_g and UIS tested







ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	60	V	
Gate-source voltage		V_{GS}	± 20		
Continuous drain current	T _C = 25 °C	I _D	150		
	T _C = 125 °C		120 ^a		
Continuous source current (diode conduction) a		I _S	120	Α	
Pulsed drain current ^b		I _{DM}	400		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	75		
Single pulse avalanche energy	L=0.11IIII	E _{AS}	281	mJ	
Maximum power dissipation ^b	T _C = 25 °C	P _D	375	W	
	T _C = 125 °C	r _D	125		
Operating junction and storage temperature range)	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount c	R_{thJA}	40	°C/W	
nction-to-case (drain)		R_{thJC}	0.4	C/VV	

Notes

- a. Package limited
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- c. When mounted on 1" square PCB (FR4 material)



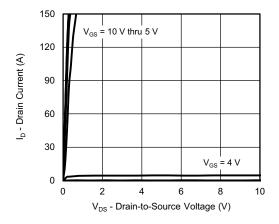
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		3.0	3.5	v	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 60 V	-	-	1		
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μA	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	250	μΑ	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 \text{ V}$	120	-	-	Α	
Drain-source on-state resistance ^a	, ,	V _{GS} = 10 V	I _D = 30 A	-	0.00163	-	Ω	
	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	0.00300	-		
		V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	0.00360	-		
Forward transconductance b	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		-	142	-	S	
Dynamic ^b								
Input capacitance	C _{iss}		V V _{DS} = 25 V, f = 1 MHz	-	9100	11 900	pF	
Output capacitance	C _{oss}	V _{GS} = 0 V		-	3550	4700		
Reverse transfer capacitance	C _{rss}]		-	160	220		
Total gate charge ^c	Qg			-	123	185	nC	
Gate-source charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 30 \text{ V}, I_{D} = 50 \text{ A}$	-	40	-		
Gate-drain charge ^c	Q_{gd}			-	19	-		
Gate resistance	R_g	f = 1 MHz		4	8.6	13	Ω	
Turn-on delay time ^c	t _{d(on)}	$V_{DD} = 30 \text{ V, } R_L = 0.6 \Omega$ $I_D \cong 50 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	48	75		
Rise time ^c	t _r			-	26	40	200	
Turn-off delay time ^c	t _{d(off)}			-	105	160	- ns	
Fall time ^c	t _f			-	25	40		
Source-Drain Diode Ratings and Chara	cteristics ^b							
Pulsed current ^a	I _{SM}			=	-	240	Α	
Forward voltage	V_{SD}	I _F = 50 A, V _{GS} = 0 V		-	0.84	1.5	V	
Body diode reverse recovery time	t _{rr}	I _F = 25 A, di/dt = 100 A/μs		-	100	200	ns	
Body diode reverse recovery charge	Q _{rr}			-	243	500	nC	
Reverse recovery fall time	t _a			-	48	-		
Reverse recovery rise time	t _b			-	53	-	ns	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-4.6	1	Α	

Notes

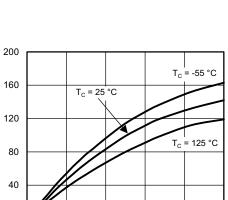
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing c. Independent of operating temperature



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Output Characteristics

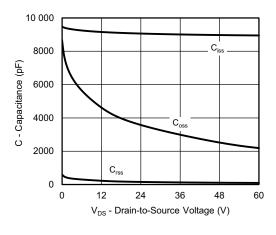


Transconductance

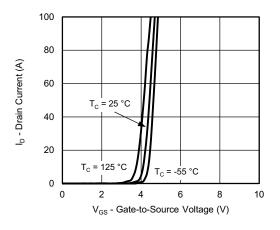
I_D - Drain Current (A)

24

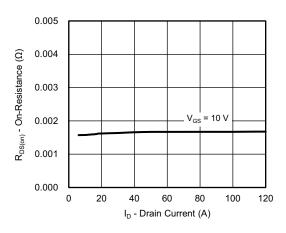
30



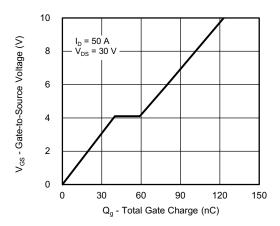
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current



Gate Charge

服务热线:400-655-8788

gfs - Transconductance (S)

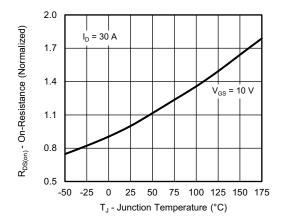
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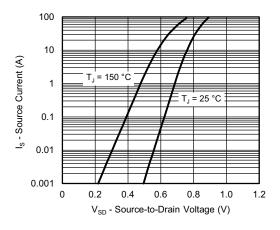
6



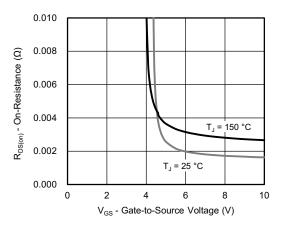
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



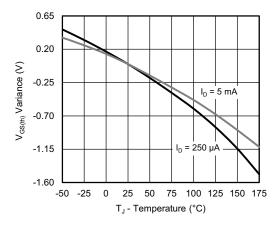
On-Resistance vs. Junction Temperature



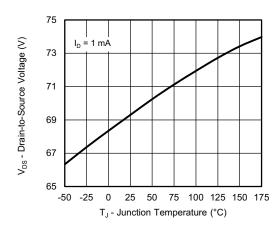
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



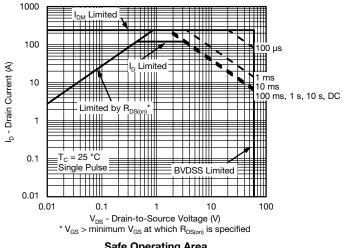
Threshold Voltage



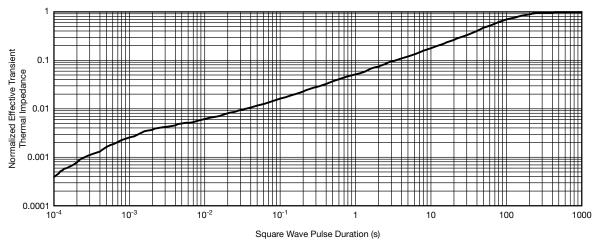
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



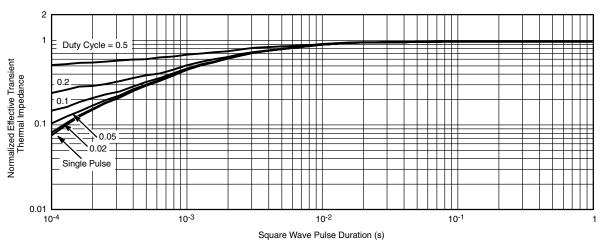
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions



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