

## CMB200N04B-7-VB Datasheet

### N-Channel 40 V (D-S) 175 °C MOSFET



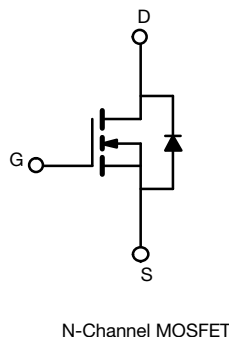
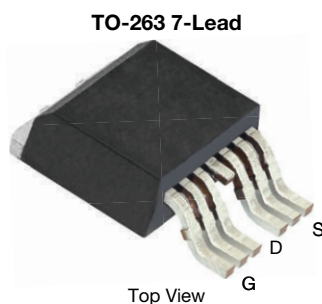
**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

#### PRODUCT SUMMARY

$V_{DS}$ (V)	40
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 10$ V	0.00084
$I_D$ (A)	350
Configuration	Single
Package	TO-263-7L

#### FEATURES

- Trench power MOSFET
- Package with low thermal resistance
- 100 %  $R_g$  and UIS tested



#### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	$V_{DS}$	40	V
Gate-source voltage	$V_{GS}$	$\pm 20$	
Continuous drain current	$I_D$	350	A
		200 <sup>a</sup>	
Continuous source current (diode conduction) <sup>a</sup>	$I_S$	200	
Pulsed drain current <sup>b</sup>	$I_{DM}$	1050	
Single pulse avalanche current	$I_{AS}$	100	
Single pulse avalanche energy	$E_{AS}$	500	mJ
Maximum power dissipation <sup>b</sup>	$P_D$	375	W
		125	
Operating junction and storage temperature range	$T_J, T_{stg}$	-55 to +175	$^\circ\text{C}$

#### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	$R_{thJA}$	40	$^\circ\text{C}/\text{W}$
Junction-to-case (drain)	$R_{thJC}$	0.4	

#### Notes

- Package limited
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$
- When mounted on 1" square PCB (FR4 material)

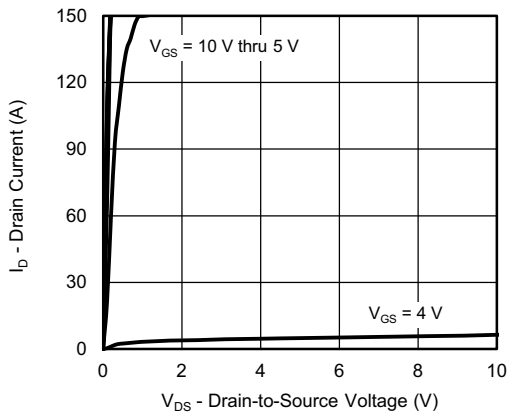
SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		40	-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		2.5	3.0	3.5	
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V	-	-	1	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 175 °C	-	-	300	μA
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≥ 5 V	100	-	-	A
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 35 A	-	0.00084	-	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 35 A, T <sub>J</sub> = 125 °C	-	-	-	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 35 A, T <sub>J</sub> = 175 °C	-	-	-	
Forward transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		-	196	-	S
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 25 V, f = 1 MHz	-	11 938	15 525	pF
Output capacitance	C <sub>oss</sub>			-	11 163	14 520	
Reverse transfer capacitance	C <sub>rss</sub>			-	282	370	
Total gate charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 100 A	-	158	250	nC
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>			-	44	-	
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>			-	22	-	
Gate resistance	R <sub>g</sub>	f = 1 MHz		2.70	5.44	8.20	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 0.2 Ω I <sub>D</sub> ≡ 100 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		-	16	25	ns
Rise time <sup>c</sup>	t <sub>r</sub>			-	10	17	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>			-	103	160	
Fall time <sup>c</sup>	t <sub>f</sub>			-	61	95	
Source-Drain Diode Ratings and Characteristics <sup>b</sup>							
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	260	A
Forward voltage	V <sub>SD</sub>	I <sub>F</sub> = 60 A, V <sub>GS</sub> = 0 V		-	0.81	1.5	V
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		-	165	350	ns
Body diode reverse recovery charge	Q <sub>rr</sub>			-	530	1100	nC
Reverse recovery fall time	t <sub>a</sub>			-	66	-	ns
Reverse recovery rise time	t <sub>b</sub>			-	99	-	
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-6.2	-	A

**Notes**

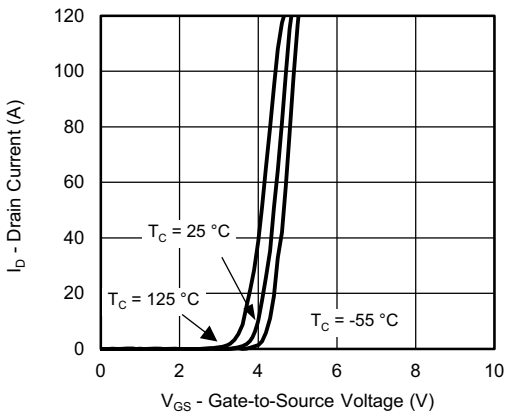
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$   
 b. Guaranteed by design, not subject to production testing  
 c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

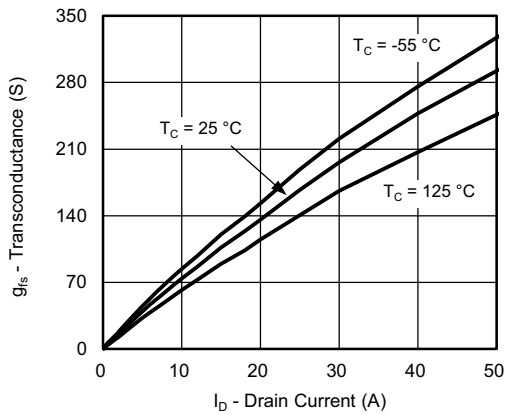
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



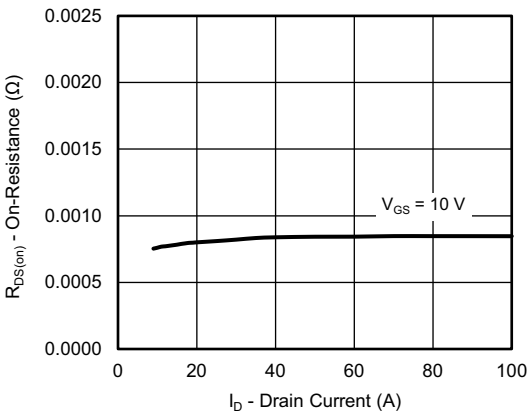
**Output Characteristics**



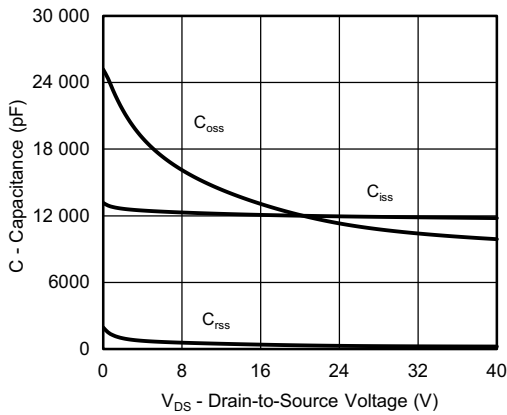
**Transfer Characteristics**



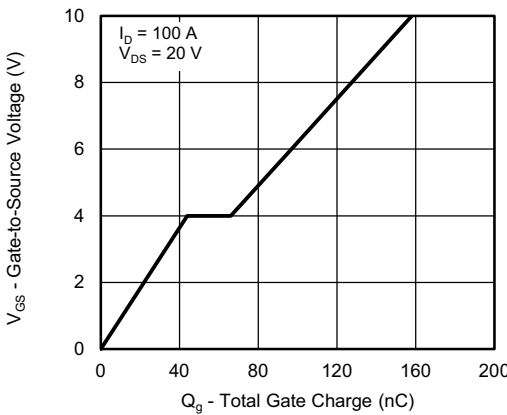
**Transconductance**



**On-Resistance vs. Drain Current**

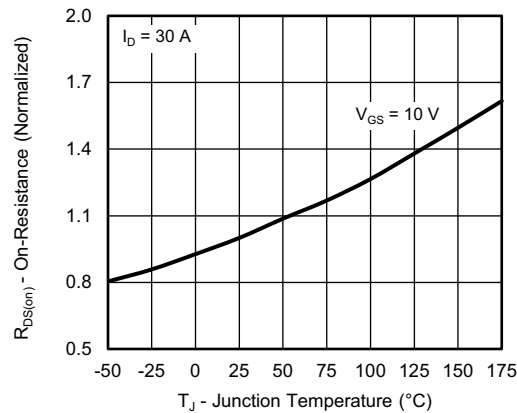


**Capacitance**

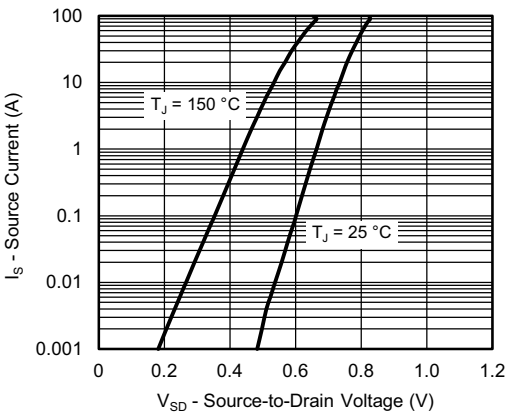


**Gate Charge**

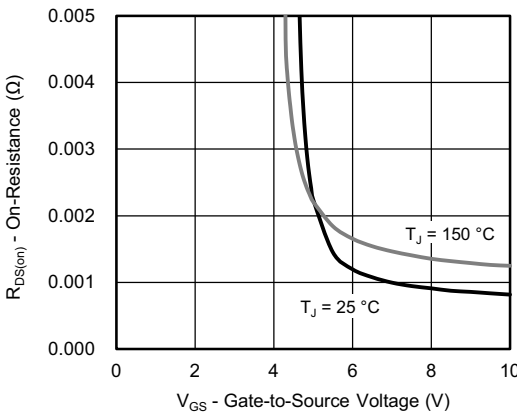
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



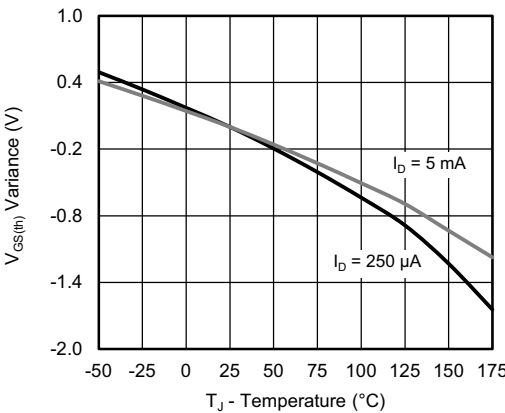
**On-Resistance vs. Junction Temperature**



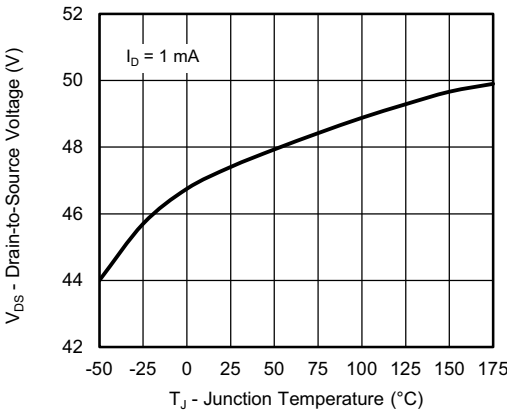
**Source Drain Diode Forward Voltage**



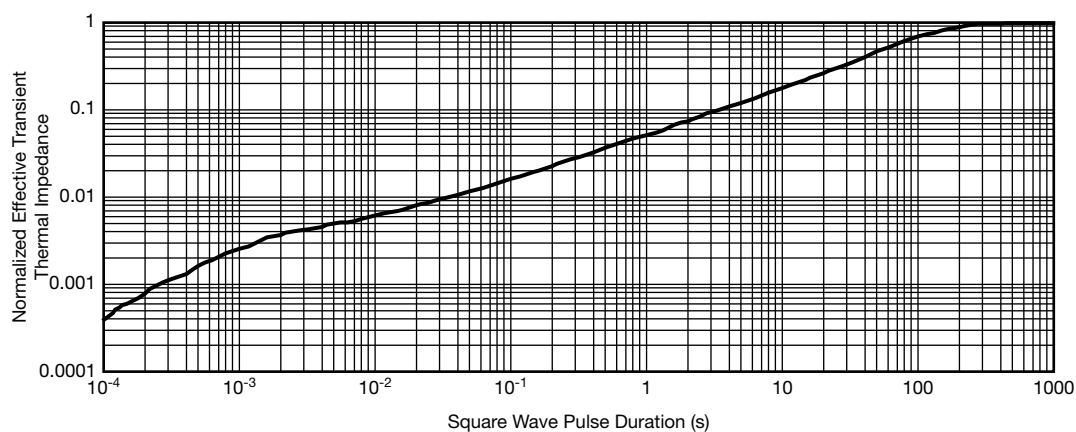
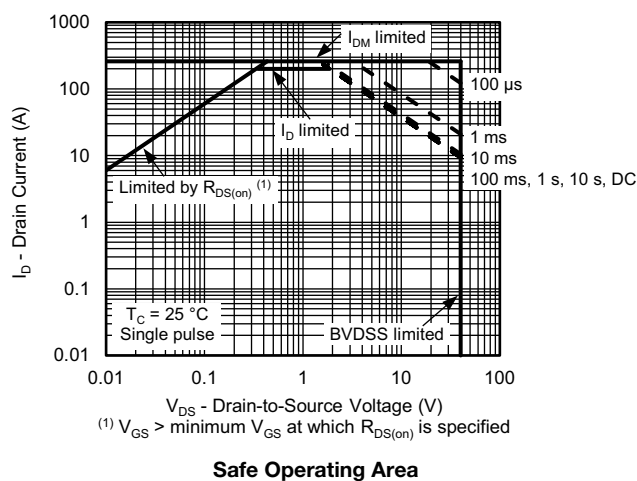
**On-Resistance vs. Gate-to-Source Voltage**

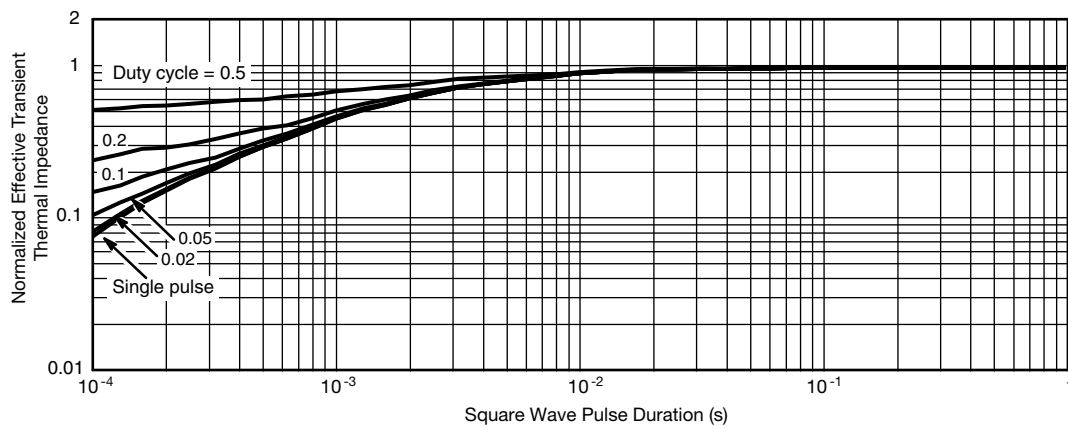


**Threshold Voltage**



**Drain Source Breakdown vs. Junction Temperature**

**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

**Normalized Thermal Transient Impedance, Junction-to-Ambient**

**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^{\circ}\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Case ( $25\text{ }^{\circ}\text{C}$ )are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size  $1\text{''} \times 1\text{''} \times 0.062\text{''}$ , double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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