

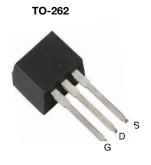
# NP82N04NDG-VB Datasheet N-Channel 40-V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V <sub>(BR)DSS</sub> (V)	r <sub>DS(on)</sub> (∧)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)	
40	0.005 at V <sub>GS</sub> = 10 V	100	95	

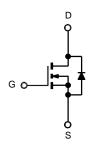
#### **FEATURES**

- · Trench Power MOSFET
- 175 °C Junction Temperature
- · High Threshold Voltage at High Temperature









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS TC=	= 25 °C, unless other	wise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	40	V	
Gate-Source Voltage		$V_{GS}$	20		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	110		
	T <sub>C</sub> = 125 °C		70		
Pulsed Drain Current		I <sub>DM</sub>	300	Α	
Avalanche Current		I <sub>AR</sub>	50		
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	125	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	В	150 <sup>b</sup>	144	
	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub>	3.75	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case		R <sub>thJC</sub>	1	]	

### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•			'	<u>'</u>		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250  \mu\text{A}$	40			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1.0	2.0	4.0		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μA	
		V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.005			
	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C		0.008			
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 175 ^{\circ}\text{C}$		0.0106			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 15 \text{ A}$	20	50		S	
Dynamic <sup>b</sup>			<u> </u>		'		
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		3200		pF	
Output Capacitance	C <sub>oss</sub>			600			
Reverse Transfer Capacitance	C <sub>rss</sub>			320			
Total Gate Charge <sup>c</sup>	Qg			95		nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$		37			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			21			
Gate Resistance	Rg	f = 1.0 MHz		1.7		٨	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			20	30	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 20 \text{ V}, R_L = 0.4 \land$ $I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \land$		95	145		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			50	75		
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20		
Source-Drain Diode Ratings and Cha	racteristics T	<sub>C</sub> = 25 °C <sup>b</sup>	I				
Continuous Current	Is				100	^	
Pulsed Current	I <sub>SM</sub>				300	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		0.90	1.50	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		40	60	ns	

#### Notes:

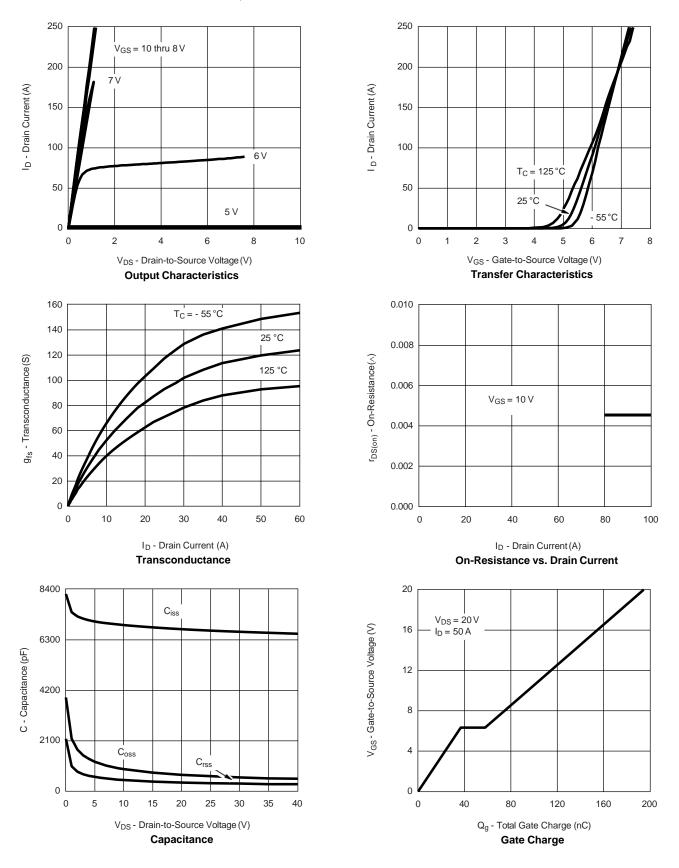
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- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

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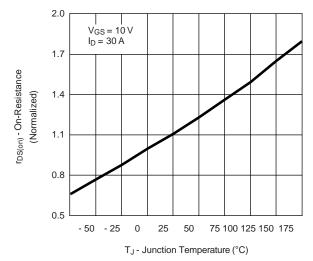
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



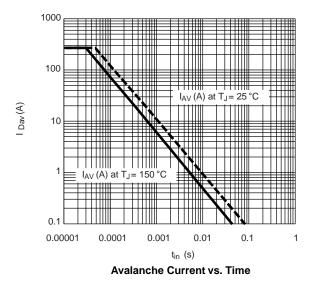
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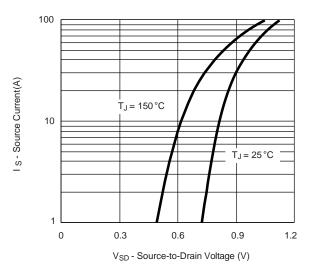


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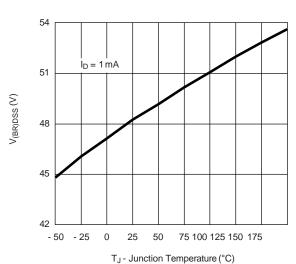


On-Resistance vs. Junction Temperature





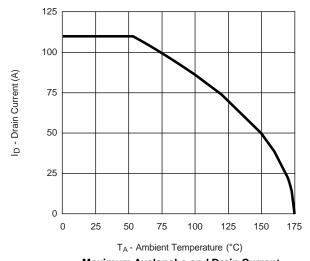
Source-Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



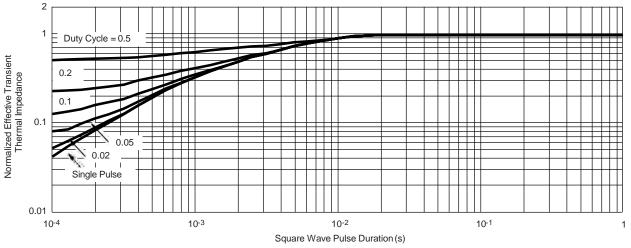
#### THERMAL RATINGS



10 µs 100 I D - Drain Current(A) 10 Limited 1 ms by r<sub>DS(on)</sub>\* 10 ms, 100 ms, DC 0.1 T<sub>C</sub> = 25 °C Single Pulse 0.01 0.001 0.1 10 V<sub>DS</sub> - Drain-to-Source Voltage (V) \*  $V_{GS}\,>\,$  minimum  $V_{GS}$  at which  $r_{DS(on)}$  is specified

Maximum Avalanche and Drain Current vs. Case Temperature





Normalized Thermal Transient Impedance, Junction-to-Case

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