

## NP40N055DLE-VB Datasheet N-Channel 60 V (D-S) MOSFET

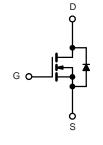
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>		
60	0.003 at V <sub>GS</sub> = 10 V	210		
	0.005 at V <sub>GS</sub> = 4.5 V	185		

#### FEATURES

- 175 °C Junction Temperature
- Trench Power MOSFET
- Material categorization:







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> =	= 25 °C, unless othe	rwise noted)		
Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
	T <sub>C</sub> = 25 °C	1-	210	
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 100 °C	I <sub>D</sub>	185 <sup>a</sup>	
Pulsed Drain Current		I <sub>DM</sub>	200	А
Continuous Source Current (Diode Conduction)		۱ <sub>S</sub>	180 <sup>a</sup>	
Avalanche Current		I <sub>AS</sub>	70	
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ
Movimum Dower Discipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	136	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		3 <sup>b</sup> , 8.3 <sup>b, c</sup>	V
Operating Junction and Storage Temperature Range	·	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Movimum lunction to Ambienta	$t \le 10 \text{ sec}$	- R <sub>thJA</sub>	15	18	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		40	50	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c.  $t \le 10$  s.

<b>B</b> <sup>®</sup> VBsemi
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Parameter	Symbol	Test Conditions	Min.	Tun a	Max.	Unit	
	Symbol	Test Conditions	win.	Typ. <sup>a</sup>	wax.	Unit	
Static				1			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1	2	3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50		
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 \text{ °C}$			250	]	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	60			А	
h		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.003		Ω	
	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 125 \text{ °C}$		0.008			
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C		0.010			
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A		0.005			
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic							
Input Capacitance	C <sub>iss</sub>			2650		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		470			
Reverse Transfer Capacitance	C <sub>rss</sub>			225			
Total Gate Charge <sup>c</sup>	Qg			47	70	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 50 \text{ A}$		10			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	tr	$V_{DD} = 30 \text{ V}, \text{ R}_{\text{L}} = 0.6 \Omega$ $\text{I}_{\text{D}} \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 2.5 \Omega$		15	25	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>				60	А	
Diode Forward Voltage	V <sub>SD</sub>	$I_{F} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V	
	1						

#### **SPECIFICATIONS** (T<sub>1</sub> = 25 °C, unless otherwise noted)

Notes:

a. For design aid only; not subject to production testing.

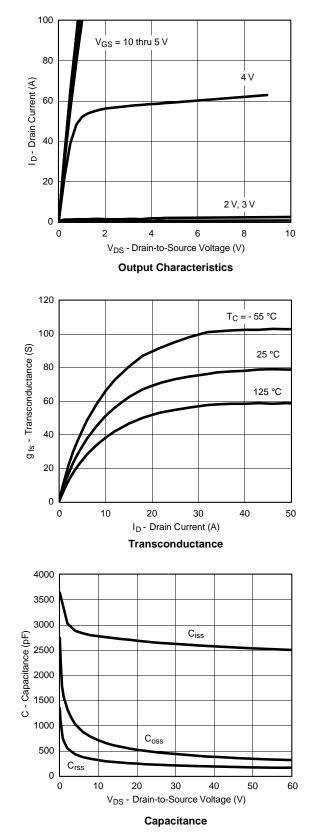
b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

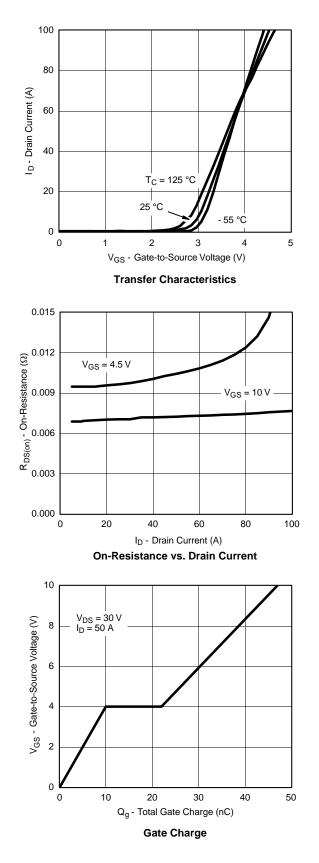
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



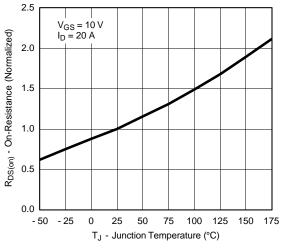
#### TYPICAL CHARACTERISTICS (25 °C unless noted)



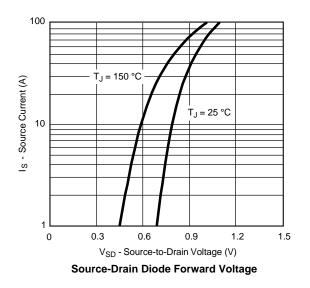




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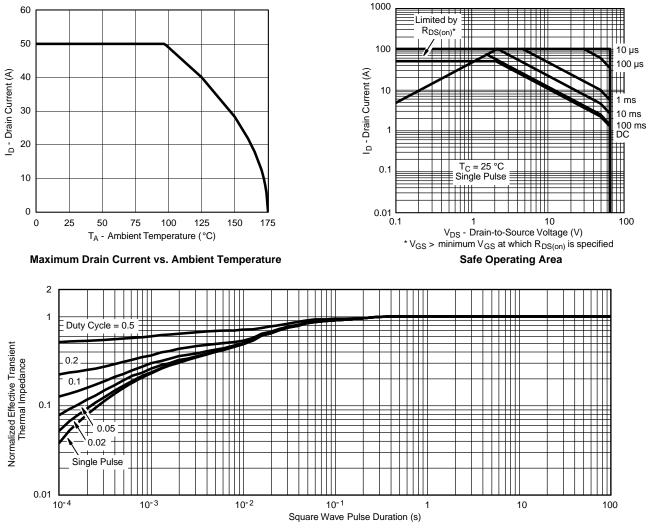
**On-Resistance vs. Junction Temperature** 



### NP40N055DLE-VB



#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case



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