

# FBF20L-VB Datasheet **Power MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	950			
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V	5.4		
Q <sub>g</sub> (Max.) (nC)	78			
Q <sub>gs</sub> (nC)	10			
Q <sub>gd</sub> (nC)	42			
Configuration	Single			

#### **FEATURES**

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

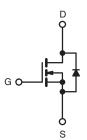








Top View



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER			SYMBOL	LIMITE	UNIT	
Drain-Source Voltage			$V_{DS}$	950	V	
Gate-Source Voltage			$V_{GS}$	± 20	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Continuous Drain Current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 25 °C		3.6		
	VGS at 10 V	T <sub>C</sub> = 100 °C	Ι <sub>D</sub>	2.3	Α	
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	14		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	250	mJ	
Repetitive Avalanche Current <sup>a</sup>			I <sub>AR</sub>	3.6	Α	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	13	mJ	
Maximum Power Dissipation	aximum Power Dissipation T <sub>C</sub> = 25 °C		$P_{D}$	125	W	
Peak Diode Recovery dV/dt <sup>c</sup>			dV/dt	1.5	V/ns	
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	
Soldering Recommendations (Peak Temperature)	for 10 s			300 <sup>d</sup>	7	
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
Modifiling Torque				1.1	N⋅m	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b.  $V_{DD}$  = 50 V, starting  $T_J$  = 25 °C, L = 36 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 3.6 A (see fig. 12). c.  $I_{SD} \le 3.6$  A, dI/dt  $\le 70$  A/µs,  $V_{DD} \le 600$ ,  $T_J \le 150$  °C.
- d. 1.6 mm from case.



THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	62		
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.50	-	°C/W	
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-	1.0		

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	= 0 V, I <sub>D</sub> = 250 μA	950	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = 1 mA	-	1.1	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	= V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	,	V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zava Cata Valtaga Drain Current		V <sub>DS</sub> =	900 V, V <sub>GS</sub> = 0 V	-	-	100	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-	500	μA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 2.2 A <sup>b</sup>	_	5.4	-	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> =	100 V, I <sub>D</sub> = 2.2 A <sup>b</sup>	2.3	-	-	S
Dynamic							
Input Capacitance	C <sub>iss</sub>		$V_{GS} = 0 V$ ,	-	1200	-	
Output Capacitance	C <sub>oss</sub>	1	$V_{DS} = 25 \text{ V},$	-	320	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.	0 MHz, see fig. 5	-	200	-	
Total Gate Charge	Qg			-	-	78	
Gate-Source Charge	$Q_{gs}$	V <sub>GS</sub> = 10 V	$I_D = 3.6 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13 <sup>b</sup>	-	-	10	nC
Gate-Drain Charge	Q <sub>gd</sub>		goo ng. o ana ro	-	-	42	
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD}$ = 450 V, $I_{D}$ = 3.6 A, $R_{g}$ = 12 $\Omega$ , $R_{D}$ = 120 $\Omega$ , see fig. 10 <sup>b</sup>		-	14	-	- ns
Rise Time	t <sub>r</sub>			-	25	-	
Turn-Off Delay Time	t <sub>d(off)</sub>			-	90	-	
Fall Time	t <sub>f</sub>			-	30	-	
Internal Drain Inductance	$L_D$	6 mm (0.25") 1	Between lead, 6 mm (0.25") from		4.5	-	-11
Internal Source Inductance	L <sub>S</sub>	package and center of die contact		-	7.5	-	- nH
Drain-Source Body Diode Characteristic	s	1			l		
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	3.6	A
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	14	
Body Diode Voltage	$V_{SD}$	$T_J = 25  ^{\circ}\text{C},  I_S = 3.6  \text{A},  V_{GS} = 0  \text{V}^{\text{b}}$		-	=	1.8	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C, I <sub>F</sub> = 3.6 A, dI/dt = 100 A/µs <sup>b</sup>		-	430	650	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	1.4	2.1	μC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )				L <sub>D</sub> )	

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %.



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

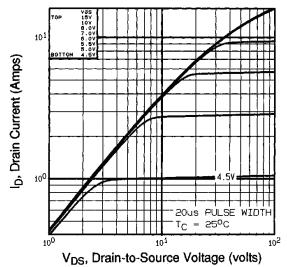


Fig. 1 - Typical Output Characteristics,  $T_C = 25$  °C

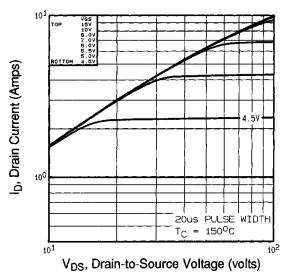


Fig. 2 -Typical Output Characteristics,  $T_C = 150 \, ^{\circ}C$ 

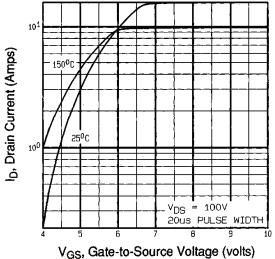


Fig. 3 - Typical Transfer Characteristics

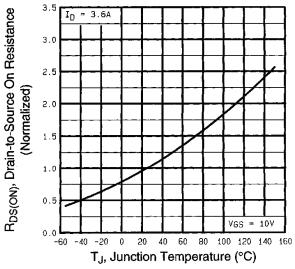


Fig. 4 - Normalized On-Resistance vs. Temperature



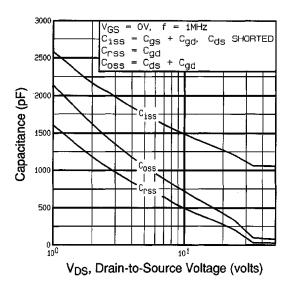


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

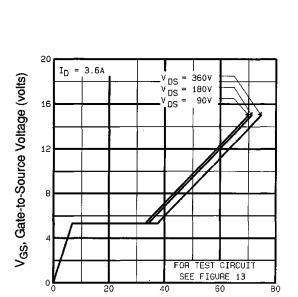


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

Q<sub>G</sub>, Total Gate Charge (nC)

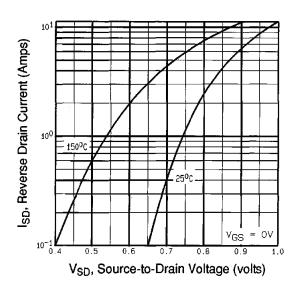


Fig. 7 - Typical Source-Drain Diode Forward Voltage

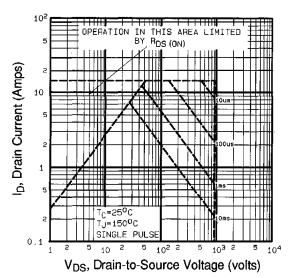


Fig. 8 - Maximum Safe Operating Area



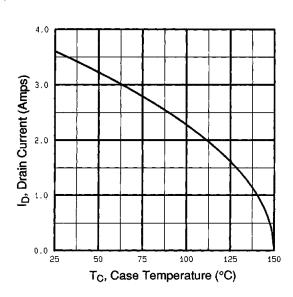


Fig. 9 - Maximum Drain Current vs. Case Temperature

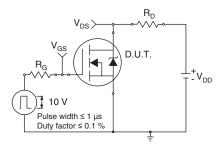


Fig. 10a - Switching Time Test Circuit

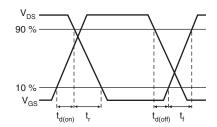


Fig. 10b - Switching Time Waveforms

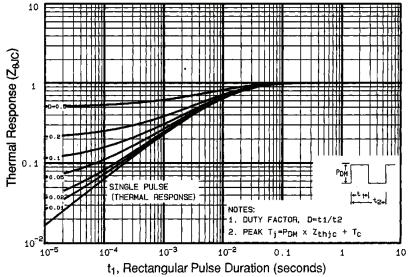


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

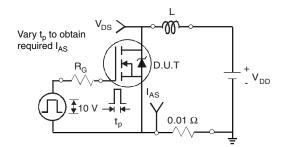


Fig. 12a - Unclamped Inductive Test Circuit

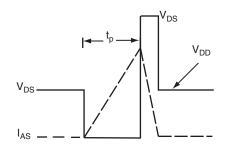


Fig. 12b - Unclamped Inductive Waveforms



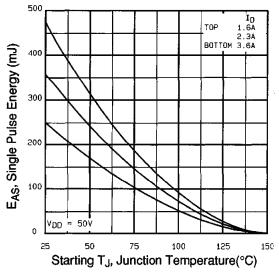


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

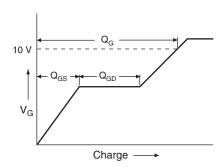


Fig. 13a - Basic Gate Charge Waveform

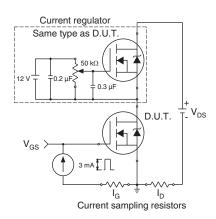
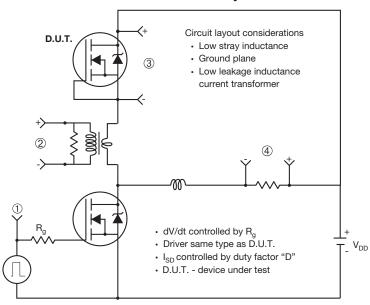


Fig. 13b - Gate Charge Test Circuit



#### Peak Diode Recovery dV/dt Test Circuit



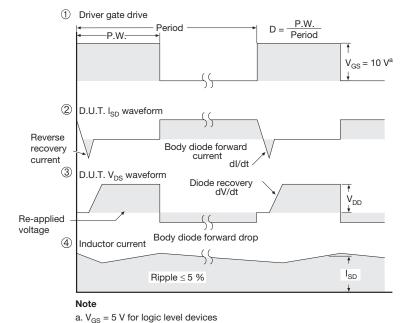
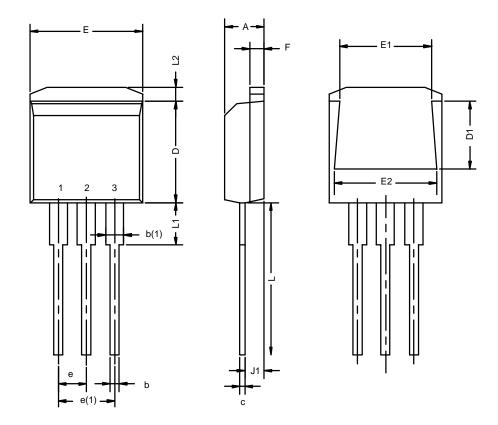


Fig. 14 - For N-Channel



### TO-262: 3-LEAD



	MILLIM	ETERS*	INC	INCHES		
Dim	Min	Max	Min	Max		
Α	4.32	4.70	0.170	0.185		
b	0.64	1.00	0.025	0.039		
b(1)	1.14	1.40	0.045	0.055		
С	0.36	0.50	0.014	0.020		
D	8.64	9.65	0.340	0.380		
D1	5.59	6.10	0.220	0.240		
е	2.41	2.67	0.095	0.105		
e(1)	4.95	5.33	0.195	0.210		
Е	10.03	10.41	0.395	0.410		
E1	7.87	8.64	0.310	0.340		
E2	9.02	9.53	0.355	0.375		
F	1.14	1.40	0.045	0.055		
J1	2.41	2.79	0.095	0.110		
L	13.08	14.22	0.515	0.560		
L1	-	3.81	-	0.150		
L2	1.02	1.40	0.040	0.055		
ECN: T-02234—Rev. C, 14-Oct-02 DWG: 5855						

 $<sup>{}^{\</sup>star}\text{Use}$  millimeters as the primary measurement



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