

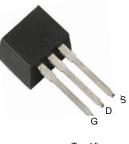
ROHS COMPLIANT

BUK9E04-40A-VB Datasheet

N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, c}	Q _g (Typ.)			
40	0.0017 at V _{GS} = 10 V	150	120 nC			
	0.0025 at V _{GS} = 4.5 V	135	120110			





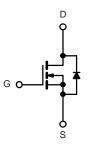
Top View

FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Synchronous Rectification
- Power Supplies



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	S T _A = 25 °C, unle	ss otherwise no	oted	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	40	V	
Gate-Source Voltage		V _{GS}	± 25	v
	T _C = 25 °C		150 ^{a, c}	
Continuous Drain Current (T = $175 ^{\circ}$ C)	T _C = 70 °C		120 ^c	
Continuous Drain Current ($T_J = 175 \ ^{\circ}C$)	T _A = 25 °C	Ι _D	29 ^b	A
	T _A = 70 °C		23 ^b	
Pulsed Drain Current	I _{DM}	380		
Avalanche Current Pulse		I _{AS}	80	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	320	mJ
Continuous Source-Drain Diode Current	T _C = 25 °C	le .	110 ^{a, c}	A
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.6 ^b	
	T _C = 25 °C		312 ^a	
Maximum Davian Dissingtion	T _C = 70 °C		200	10/
Maximum Power Dissipation	T _A = 25 °C	PD	3.13 ^b	- W
	T _A = 70 °C		2.0 ^b	7
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	Steady State	R _{thJA}	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.33	0.4	0/10	

Notes:

a. Based on $T_C = 25 \ ^{\circ}C$.

b. Surface Mounted on 1" x 1" FR4 board.

c. Calculated based on maximum junction temperature. Package limitation current is 110 A.

SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	45			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Ι _D = 250 μΑ		41		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η = 200 μΛ		- 8			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	120			Α	
	Р	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$		0.0017		0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0025		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		180		S	
Dynamic ^b							
Input Capacitance	C _{iss}			9000		pF	
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		650			
Reverse Transfer Capacitance	C _{rss}			450			
Total Gate Charge	Qg			120	180		
Gate-Source Charge	Q _{gs}	V_{DS} = 20 V, V_{GS} = 10 V, I_D = 20 A		30		nC	
Gate-Drain Charge	Q _{gd}			16			
Gate Resistance	Rg	f = 1 MHz		0.85	1.3	Ω	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = 20 V, R_L = 1.0 Ω		11	17		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ 20 A, V_{GEN} = 10 V, R_{g} = 1 Ω		77	115	-	
Fall Time	t _f			10	15		
Turn-On Delay Time	t _{d(on)}			102	155	ns	
Rise Time	t _r	V_{DD} = 20 V, R_L = 1.0 Ω		62	95	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 20$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		180	270		
Fall Time	t _f			60	90		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			110	A	
Pulse Diode Forward Current ^a	I _{SM}				200	~	
Body Diode Voltage	V _{SD}	I _S = 20 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			50	75	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		70	105	nC	
Reverse Recovery Fall Time	t _a	$r_{\rm F} = 20$ A, $u_{\rm F}u_{\rm C} = 100$ A/ μ s, $r_{\rm J} = 20$ C		30		20	
Reverse Recovery Rise Time	t _b			20		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

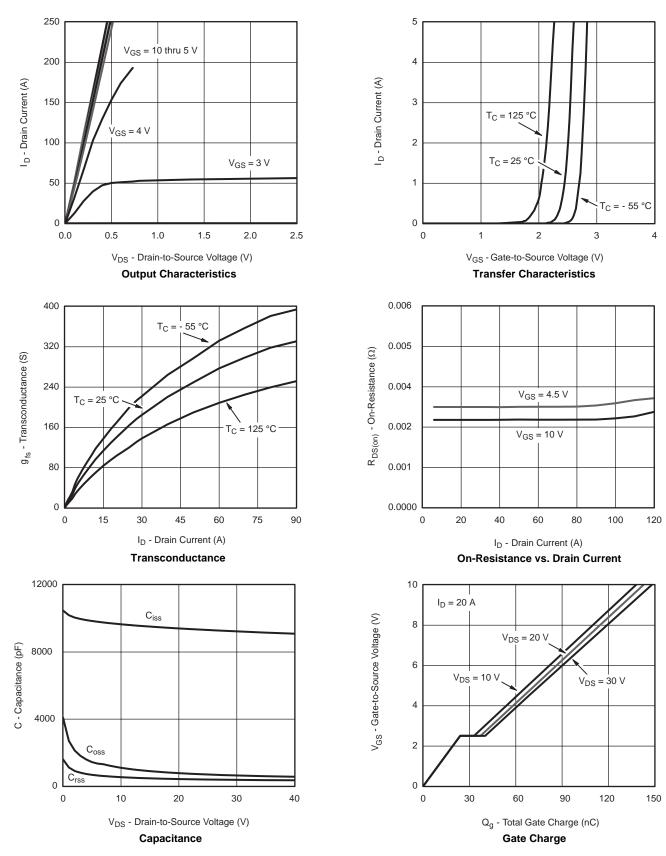
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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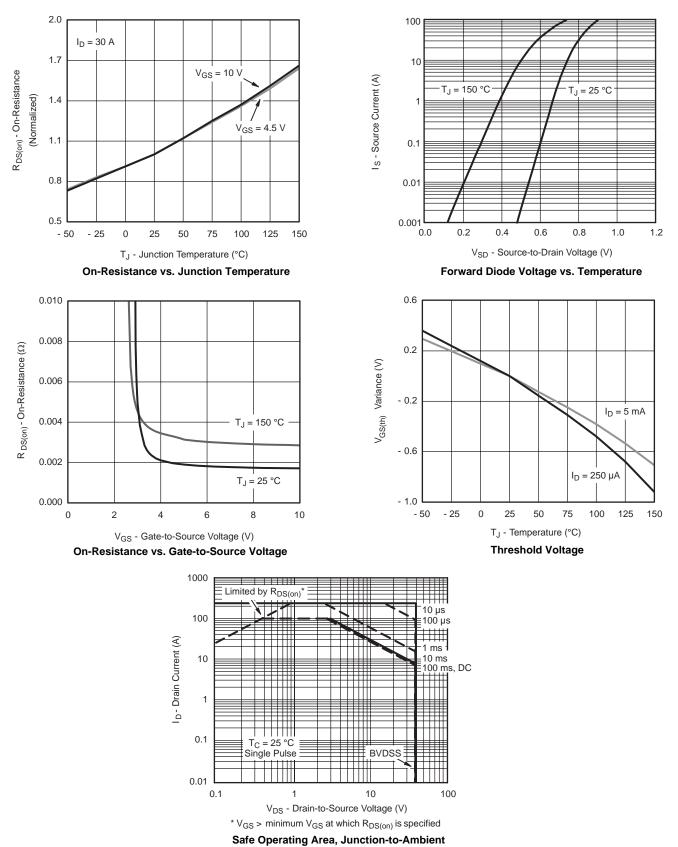
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



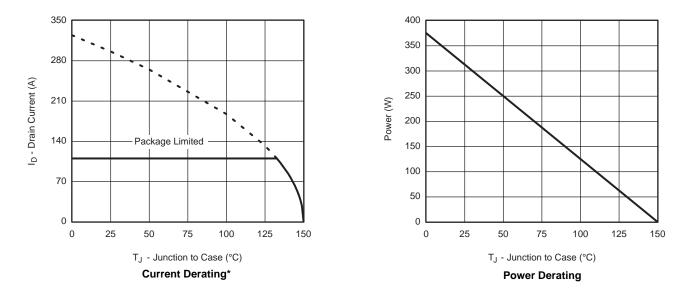
服务热线:400-655-8788



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

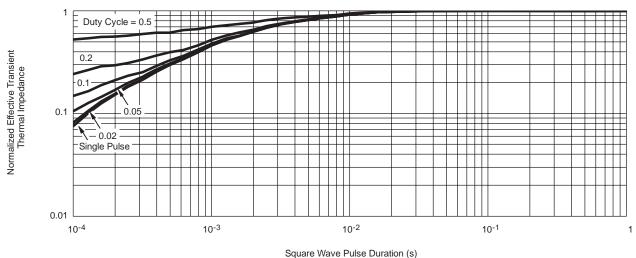






TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

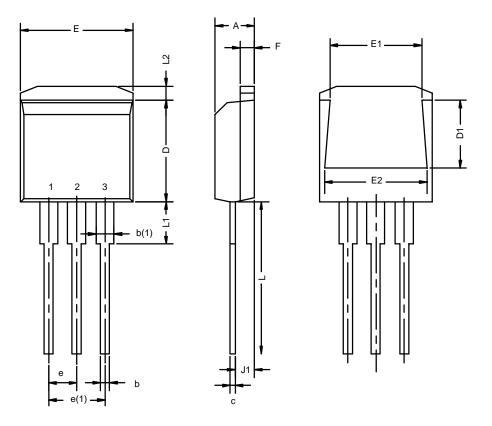
* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case



TO-262: 3-LEAD



	MILLIM	ETERS*	INC	ICHES		
Dim	Min	Max	Min	Max		
Α	4.32	4.70	0.170	0.185		
b	0.64	1.00	0.025	0.039		
b(1)	1.14	1.40	0.045	0.055		
С	0.36	0.50	0.014	0.020		
D	8.64	9.65	0.340	0.380		
D1	5.59	6.10	0.220	0.240		
е	2.41	2.67	0.095	0.105		
e(1)	4.95	5.33	0.195	0.210		
E	10.03	10.41	0.395	0.410		
E1	7.87	8.64	0.310	0.340		
E2	9.02	9.53	0.355	0.375		
F	1.14	1.40	0.045	0.055		
J1	2.41	2.79	0.095	0.110		
L	13.08	14.22	0.515	0.560		
L1	-	3.81	-	0.150		
L2	1.02	1.40	0.040	0.055		
ECN: T-02234—Rev. C, 14-Oct-02 DWG: 5855						

*Use millimeters as the primary measurement



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