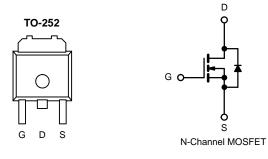


## VSD140N15MD-VB Datasheet

### N-Channel 150 V (D-S) MOSFET

| PRODUCT SUMMARY     |                                 |                                 |                       |  |  |  |
|---------------------|---------------------------------|---------------------------------|-----------------------|--|--|--|
| V <sub>DS</sub> (V) | R <sub>DS(on)</sub> (Ω)         | I <sub>D</sub> (A) <sup>a</sup> | Q <sub>g</sub> (Typ.) |  |  |  |
| 150                 | 0.074 at V <sub>GS</sub> = 10 V | 25.4                            | 23 nC                 |  |  |  |
| 150                 | 0.077 at V <sub>GS</sub> = 8 V  | 22.5                            | 23110                 |  |  |  |



#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- Extremely Low Q<sub>gd</sub> for Switching Losses
- 100 % R<sub>g</sub> Tested
- 100 % Avalanche Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

• Primary Side Switch



COMPLIANT HALOGEN FREE Available

| Parameter  |                        | Symbol                            | Limit                | Unit |
|--|------------------------|-----------------------------------|----------------------|------|
| Drain-Source Voltage<br>Gate-Source Voltage          |                        | V <sub>DS</sub>                   | 150                  | V    |
|  |                        | V <sub>GS</sub>                   | ± 20                 | v    |
|  | T <sub>C</sub> = 25 °C |                                   | 25.4                 |      |
| Continuous Drain Current ( $T_1 = 150 \ ^{\circ}C$ ) | T <sub>C</sub> = 70 °C |                                   | 23.1                 |      |
| Continuous Drain Current $(T_j = 150 \text{ C})$     | T <sub>A</sub> = 25 °C |                                   | 15.5 <sup>b, c</sup> |      |
|  | T <sub>A</sub> = 70 °C |                                   | 14.5 <sup>b, c</sup> | A    |
| Pulsed Drain Current                                 |                        | I <sub>DM</sub>                   | 50                   | A    |
| Continuous Source-Drain Diode Current                | T <sub>C</sub> = 25 °C |                                   | 4.5                  |      |
| Continuous Source-Drain Diode Current                | T <sub>A</sub> = 25 °C | Is                                | 2.6 <sup>b, c</sup>  |      |
| Single Pulse Avalanche Current                       | L = 0.1 mH             | I <sub>AS</sub>                   | 20                   |      |
| Single Pulse Avalanche Energy                        | L = 0.1 1111           | E <sub>AS</sub>                   | 20                   | mJ   |
|  | T <sub>C</sub> = 25 °C |                                   | 5.9                  |      |
| Maximum Power Dissipation                            | T <sub>C</sub> = 70 °C |                                   | 3.8                  | w    |
|  | $T_A = 25 \text{ °C}$  | P <sub>D</sub>                    | 3.1 <sup>b, c</sup>  | vv   |
|  | T <sub>A</sub> = 70 °C | 1 –                               | 2 <sup>b, c</sup>    |      |
| Operating Junction and Storage Temperature Range     |                        | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150          | °C   |

| THERMAL RESISTANCE RATINGS |                   |   |   |  |  |  |  |
|----------------------------|-------------------|---|---|--|--|--|--|
|                            | Symbol            | Typical                                       | Maximum   | Unit   |  |  |  |
| t ≤ 10 s                   | R <sub>thJA</sub> | 33  | 40  | °C/W   |  |  |  |
| Steady State               | R <sub>thJF</sub> | 17  | 21  | C/W  |  |  |  |
|                            | t ≤ 10 s          | Symbol        t ≤ 10 s      R <sub>thJA</sub> | Symbol      Typical        t ≤ 10 s      R <sub>thJA</sub> 33 | Symbol      Typical      Maximum        t ≤ 10 s      R <sub>thJA</sub> 33      40 |  |  |  |

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 80 °C/W.

| <b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted) |                         |   |      |       |       |         |  |
|--|-------------------------|---|------|-------|-------|---------|--|
| Parameter  | Symbol                  | Test Conditions   | Min. | Тур.  | Max.  | Unit    |  |
| Static   |                         |   |      |       |       |         |  |
| Drain-Source Breakdown Voltage   | V <sub>DS</sub>         | $V_{GS} = 0 V, I_{D} = 250 \mu A$   | 150  |       |       | V       |  |
| V <sub>DS</sub> Temperature Coefficient                                | $\Delta V_{DS}/T_{J}$   | I <sub>D</sub> = 250 μA   |      | 172   |       | mV/°C   |  |
| V <sub>GS(th)</sub> Temperature Coefficient                            | $\Delta V_{GS(th)}/T_J$ | i <sub>D</sub> = 230 μA   |      | - 10  |       | 11107 C |  |
| Gate-Source Threshold Voltage  | V <sub>GS(th)</sub>     | $V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$   | 1.5  |       | 3.0   | V       |  |
| Gate-Source Leakage  | I <sub>GSS</sub>        | $V_{DS} = 0 V, V_{GS} = \pm 20 V$   |      |       | ± 100 | nA      |  |
| Zara Cata Valtaga Drain Current  |                         | $V_{DS} = 150 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$                                      |      |       | 1     |         |  |
| Zero Gate Voltage Drain Current  | DSS                     | $V_{DS} = 120 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$ |      |       | 10    | μA      |  |
| On-State Drain Current <sup>a</sup>                                    | I <sub>D(on)</sub>      | $V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$  | 30   |       |       | A       |  |
| Drain-Source On-State Resistance <sup>a</sup>                          | P                       | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$  |      | 0.074 |       | Ω       |  |
| Dialit-Source Off-State Resistance                                     | R <sub>DS(on)</sub>     | V <sub>GS</sub> = 8 V, I <sub>D</sub> = 5 A   |      | 0.077 |       | 52      |  |
| Forward Transconductance <sup>a</sup>                                  | 9 <sub>fs</sub>         | $V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$  |      | 23    |       | S       |  |
| Dynamic <sup>b</sup>   |                         |   |      |       |       |         |  |
| Input Capacitance  | C <sub>iss</sub>        |   |      | 1735  |       | pF      |  |
| Output Capacitance   | C <sub>oss</sub>        | $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$                            |      | 160   |       |         |  |
| Reverse Transfer Capacitance   | C <sub>rss</sub>        |   |      | 37    |       |         |  |
| Total Cata Charge  | Qg                      | $V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$                         |      | 28.5  | 43    | nC      |  |
| Total Gate Charge  |                         |   |      | 23    | 35    |         |  |
| Gate-Source Charge   | Q <sub>gs</sub>         | $V_{DS} = 75 \text{ V}, V_{GS} = 8 \text{ V}, I_{D} = 5 \text{ A}$                          |      | 8     |       |         |  |
| Gate-Drain Charge  | Q <sub>gd</sub>         |   |      | 6.5   |       |         |  |
| Gate Resistance  | R <sub>g</sub>          | f = 1 MHz   | 0.85 |       | 1.3   | Ω       |  |
| Turn-on Delay Time   | t <sub>d(on)</sub>      |   |      | 14    | 21    |         |  |
| Rise Time  | t <sub>r</sub>          | $V_{DD}$ = 50 V, $R_{L}$ = 10 $\Omega$  |      | 12    | 18    | - ns    |  |
| Turn-Off Delay Time  | t <sub>d(off)</sub>     | ${\sf I}_{\sf D}\cong$ 5 A, ${\sf V}_{\sf GEN}$ = 10 V, ${\sf R}_{\sf g}$ = 1 $\Omega$      |      | 22    | 33    |         |  |
| Fall Time  | t <sub>f</sub>          |   |      | 6     | 10    |         |  |
| Turn-On Delay Time   | t <sub>d(on)</sub>      |   |      | 16    | 24    |         |  |
| Rise Time  | t <sub>r</sub>          | $V_{DD}$ = 50 V, $R_{L}$ = 10 $\Omega$  |      | 12    | 18    |         |  |
| Turn-Off Delay Time  | t <sub>d(off)</sub>     | $I_D \cong 5 \text{ A}, V_{\text{GEN}} = 8 \text{ V}, R_g = 1 \Omega$                       |      | 20    | 30    |         |  |
| Fall Time  | t <sub>f</sub>          |   |      | 7     | 12    | 1       |  |
| Drain-Source Body Diode Characteristic                                 | cs                      |   |      |       |       |         |  |
| Continuous Source-Drain Diode Current                                  | ۱ <sub>S</sub>          | T <sub>C</sub> = 25 °C  |      |       | 7.7   | ^       |  |
| Pulse Diode Forward Current <sup>a</sup>                               | I <sub>SM</sub>         |   |      |       | 50    | A       |  |
| Body Diode Voltage   | V <sub>SD</sub>         | I <sub>S</sub> = 2.6 A  |      | 0.77  | 1.2   | V       |  |
| Body Diode Reverse Recovery Time                                       | t <sub>rr</sub>         |   |      | 63    | 95    | ns      |  |
| Body Diode Reverse Recovery Charge                                     | Q <sub>rr</sub>         |   |      | 110   | 165   | nC      |  |
| Reverse Recovery Fall Time   | t <sub>a</sub>          | $I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 \text{ °C}$ |      | 49    |       |         |  |
| Reverse Recovery Rise Time   | t <sub>b</sub>          | -   |      | 14    |       | ns      |  |

Notes:

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$ 

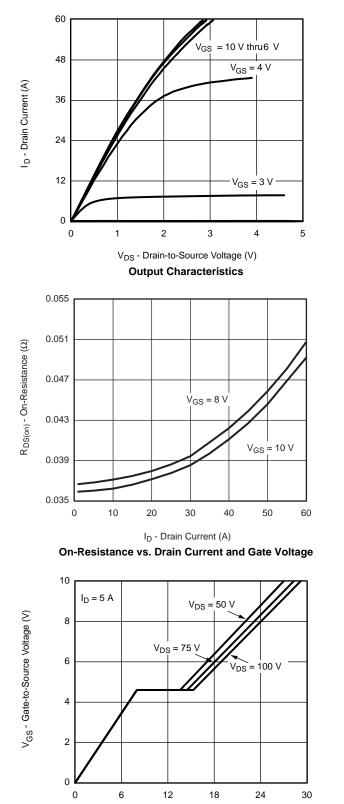
a. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi

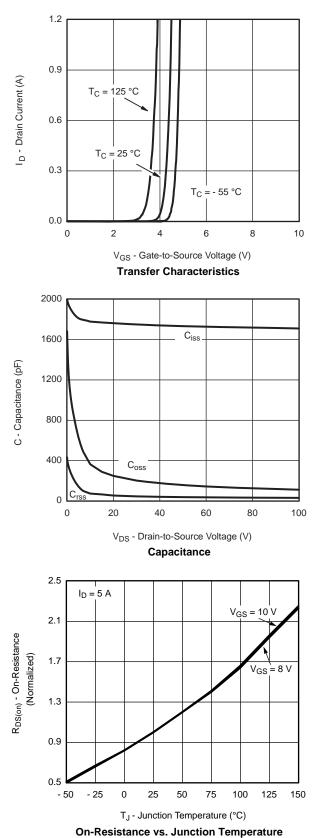
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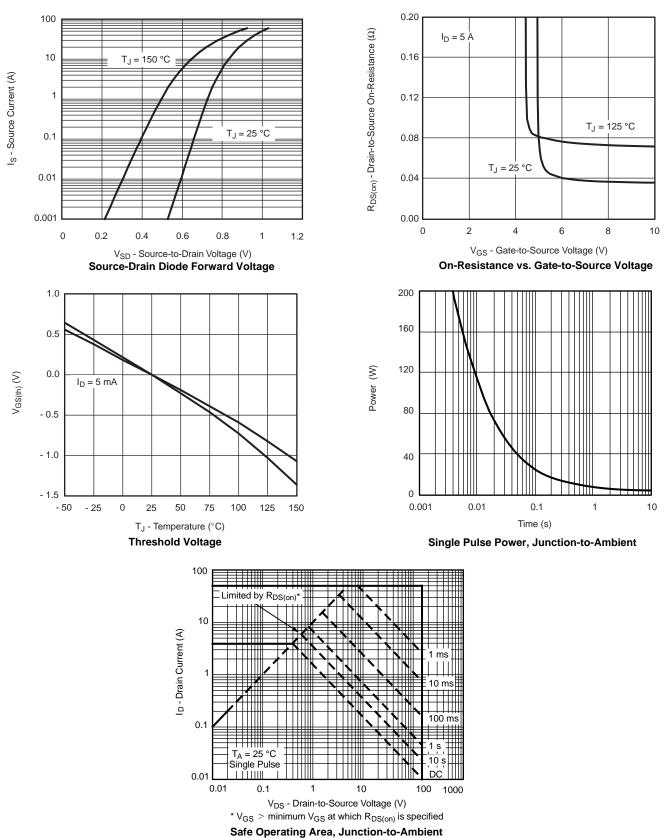


Q<sub>g</sub> - Total Gate Charge (nC) Gate Charge

#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



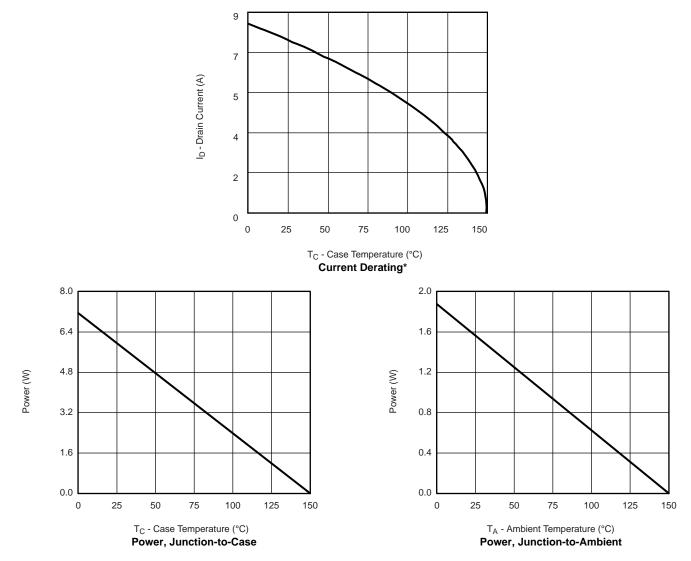




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



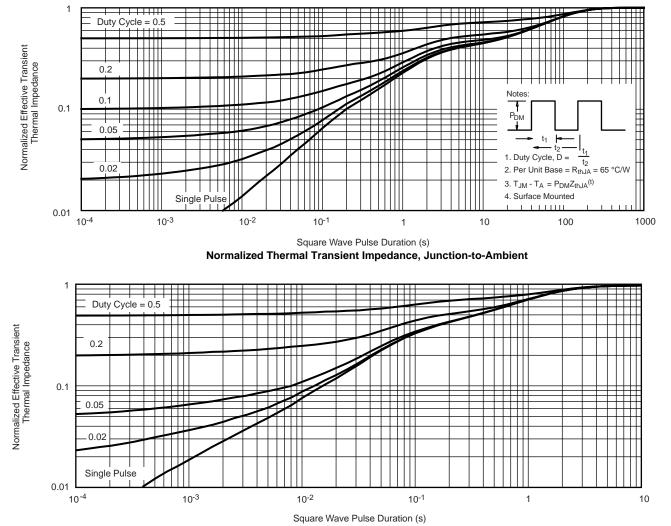
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



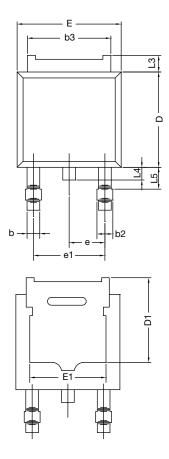
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot



# **TO-252AA CASE OUTLINE**





|  | MILLIMETERS |       | INC       | HES   |  |
|--|-------------|-------|-----------|-------|--|
| DIM.   | MIN.        | MAX.  | MIN.      | MAX.  |  |
| А  | 2.18        | 2.38  | 0.086     | 0.094 |  |
| A1   | -           | 0.127 | -         | 0.005 |  |
| b  | 0.64        | 0.88  | 0.025     | 0.035 |  |
| b2   | 0.76        | 1.14  | 0.030     | 0.045 |  |
| b3   | 4.95        | 5.46  | 0.195     | 0.215 |  |
| С  | 0.46        | 0.61  | 0.018     | 0.024 |  |
| C2   | 0.46        | 0.89  | 0.018     | 0.035 |  |
| D  | 5.97        | 6.22  | 0.235     | 0.245 |  |
| D1   | 5.21        | -     | 0.205     | -     |  |
| Е  | 6.35        | 6.73  | 0.250     | 0.265 |  |
| E1   | 4.32        | -     | 0.170     | -     |  |
| Н  | 9.40        | 10.41 | 0.370     | 0.410 |  |
| е  | 2.28 BSC    |       | 0.090 BSC |       |  |
| e1   | 4.56 BSC    |       | 0.180 BSC |       |  |
| L  | 1.40        | 1.78  | 0.055     | 0.070 |  |
| L3   | 0.89        | 1.27  | 0.035     | 0.050 |  |
| L4   | -           | 1.02  | -         | 0.040 |  |
| L5   | 1.14        | 1.52  | 0.045     | 0.060 |  |
| ECN: X12-0247-Rev. M, 24-Dec-12<br>DWG: 5347 |             |       |           |       |  |

#### Note

• Dimension L3 is for reference only.



#### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)



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