

STD70N6F3-VB Datasheet

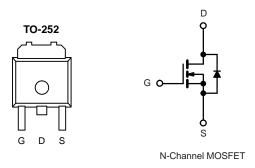
N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	
60	0.010 at V _{GS} = 10 V	58	
00	0.013 at V _{GS} = 4.5 V	56	



- Trench Power MOSFET
- Material categorization:





Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	± 20	V
Continuous Durin Current (T. 175 °C) ^b	T _C = 25 °C	L	58	
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C		48 ^a	1
Pulsed Drain Current		I _{DM}	100	А
Continuous Source Current (Diode Conduction)		۱ _S	50 ^a	7
Avalanche Current		I _{AS}	50	
Single Avalanche Energy (Duty Cycle \leq 1 %)	L = 0.1 mH	E _{AS}	125	mJ
Maximum Dawar Dissingtion	T _C = 25 °C	P _D	136	w
Maximum Power Dissipation	T _A = 25 °C		3 ^b , 8.3 ^{b, c}	vv
Operating Junction and Storage Temperature Range		T _J , T _{sta}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \le 10 \text{ sec}$	- R _{thJA}	15	18	°C/W
Maximum Junction-to-Ambient*	Steady State		40	50	
Maximum Junction-to-Case		R _{thJC}	0.85	1.1	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t \leq 10 s.

B	® VBsemi
www.\	/Bsemi.com

Gate Threshold Voltage $V_{GS}(th)$ $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ 1 2 Gate-Body Leakage I_{GSS} $V_{DS} = 0 \ V, V_{GS} = \pm 20 \ V$ Zero Gate Voltage Drain Current I_{DSS} $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ On-State Drain Current ^b $I_{D(on)}$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V, \ T_J = 125 \ ^{\circ}C$ Drain-Source On-State Resistance ^b $I_{D(on)}$ $V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 125 \ ^{\circ}C$ 0.010 \ V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 125 \ ^{\circ}C Forward Transconductance ^b g_{fs} $V_{DS} = 15 \ V, \ I_D = 20 \ A$ 0.010 \ V_{GS} = 4.5 \ V, \ I_D = 20 \ A Input Capacitance C_{iss} $V_{DS} = 15 \ V, \ I_D = 20 \ A$ 60 \ V_{GS} = 10 \ V, \ I_D = 20 \ A	^a Max.	Unit	
Gate Threshold Voltage $V_{GS}(th)$ $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ 1 2 Gate-Body Leakage I_{GSS} $V_{DS} = 0 \ V, V_{GS} = \pm 20 \ V$ $V_{DS} = 60 \ V, V_{GS} = \pm 20 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V, T_J = 125 \ ^{\circ}C$ $V_{DS} = 60 \ V, V_{GS} = 0 \ V, T_J = 175 \ ^{\circ}C$ $V_{DS} = 60 \ V, V_{GS} = 10 \ V, I_D = 20 \ A$ $V_{DS} = 60 \ V, V_{GS} = 10 \ V, I_D = 20 \ A$ $V_{DS} = 10 \ V, I_D = 20 \ A$ $V_{DS} = 10 \ V, I_D = 20 \ A$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 125 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 125 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 125 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 125 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 175 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 175 \ ^{\circ}C$ $V_{DS} = 10 \ V, I_D = 20 \ A, T_J = 175 \ ^{\circ}C$ $V_{DS} = 15 \ V, I_D = 15 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 20 \ A$ $V_{DS} = 15 \ V, I_D = 15 \ V =$		1	
		V	
Zero Gate Voltage Drain Current I_{DSS} $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$ V $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125 ^{\circ}\text{C}$ $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125 ^{\circ}\text{C}$ V On-State Drain Current ^b $I_{D(on)}$ $V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$ 60 Drain-Source On-State Resistance ^b $I_{D(on)}$ $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$ 0.010 $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 ^{\circ}\text{C}$ 0.016 0.016 0.016 $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 ^{\circ}\text{C}$ 0.016 0.016 0.016 $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 ^{\circ}\text{C}$ 0.016 0.016 0.016 $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 175 ^{\circ}\text{C}$ 0.0126 0.026	3	- V	
Zero Gate Voltage Drain Current I_{DSS} $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125 \text{ °C}$ $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 175 \text{ °C}$ On-State Drain Current ^b $I_{D(on)}$ $V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$ 60 Drain-Source On-State Resistance ^b $R_{DS(on)}$ $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$ 0.010 V_{GS} = 10 V, I_D = 20 \text{ A}, T_J = 125 \text{ °C} 0.010 $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$ 0.010 Forward Transconductance ^b g_{fs} $V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$ 0.013 Forward Transconductance ^b g_{fs} $V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$ 60 Dynamic Input Capacitance C_{iss} 2650	± 100	nA	
$ \begin{array}{ c c c c c c } \hline V_{DS} = 60 \ V, \ V_{GS} = 0 \ V, \ T_J = 175 \ ^{\circ}C & & & & & \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 0 \ V, \ T_J = 175 \ ^{\circ}C & & & & & \\ \hline V_{DS} = 5 \ V, \ V_{GS} = 10 \ V & & & & & & \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A & & & & & & \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 125 \ ^{\circ}C & & & & & & \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 125 \ ^{\circ}C & & & & & & \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 175 \ ^{\circ}C & & & & & & \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 175 \ ^{\circ}C & & & & & & \\ \hline V_{GS} = 4.5 \ V, \ I_D = 15 \ A & & & & & & \\ \hline \hline Dynamic & & & & & & \\ \hline Input \ Capacitance & \hline C_{iSS} & & & & & & & & & \\ \hline \end{array} $	1		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	50	μA	
$ \begin{array}{c c} V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A} & 0.010 \\ \hline V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C} & 0.016 \\ \hline V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C} & 0.016 \\ \hline V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 175 ^{\circ}\text{C} & 0.026 \\ \hline V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 175 ^{\circ}\text{C} & 0.026 \\ \hline V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 15 \text{ A} & 0.013 \\ \hline \text{Dynamic} & & & & & & & & & & & & & & & & & & &$	250	1	
$ \begin{array}{c c} \mbox{Prain-Source On-State Resistance}^b & R_{DS(on)} & \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 125 \ ^{\circ}C & 0.016 \\ \hline V_{GS} = 10 \ V, \ I_D = 20 \ A, \ T_J = 175 \ ^{\circ}C & 0.026 \\ \hline V_{GS} = 4.5 \ V, \ I_D = 15 \ A & 0.013 \\ \hline V_{GS} = 4.5 \ V, \ I_D = 15 \ A & 0.013 \\ \hline Dynamic & & & & & & & & & & & & & & & & & & &$		А	
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$			
$V_{GS} = 10$ V, $I_D = 20$ A, $I_J = 175$ C 0.020 $V_{GS} = 4.5$ V, $I_D = 15$ A 0.013 Forward Transconductance ^b g_{fs} $V_{DS} = 15$ V, $I_D = 20$ A 60 Dynamic Input Capacitance Ciss			
Forward Transconductance ^b g _{fs} V _{DS} = 15 V, I _D = 20 A 60 Dynamic 2 Input Capacitance C _{iss} 2650)	Ω	
Dynamic Input Capacitance C _{iss} 2650			
Input Capacitance C _{iss} 2650		S	
Output Consolitance C $V_{ab} = 0.1/V_{ab} = 25.1/f = 1.00000000000000000000000000000000000$	1		
Output Capacitance C_{oss} $V_{GS} = 0 V$, $V_{DS} = 25 V$, f = 1 MHz 470		pF	
Reverse Transfer Capacitance C _{rss} 225			
Total Gate Charge ^c Q _g 47	70		
Gate-Source Chargec Q_{gs} $V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$ 10		nC	
Gate-Drain Charge ^c Q _{gd} 12]	
Turn-On Delay Time ^c t _{d(on)} 10	20		
Rise Time ^c t_r $V_{DD} = 30 \text{ V}, \text{ R}_L = 0.6 \Omega$ 15	25		
Turn-Off Delay Time ^c $t_{d(off)}$ $I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$ 35	50	ns	
Fall Time ^c t _f 20	30]	
Source-Drain Diode Ratings and Characteristics ($T_c = 25 \ ^{\circ}C$)	·		
Pulsed Current I _{SM}	60	А	
Diode Forward Voltage V_{SD} $I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$ 1	1.5	V	
Reverse Recovery Time t_{rr} $I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$ 45	100	ns	

SPECIFICATIONS (T₁ = 25 °C, unless otherwise noted)

Notes:

a. For design aid only; not subject to production testing.

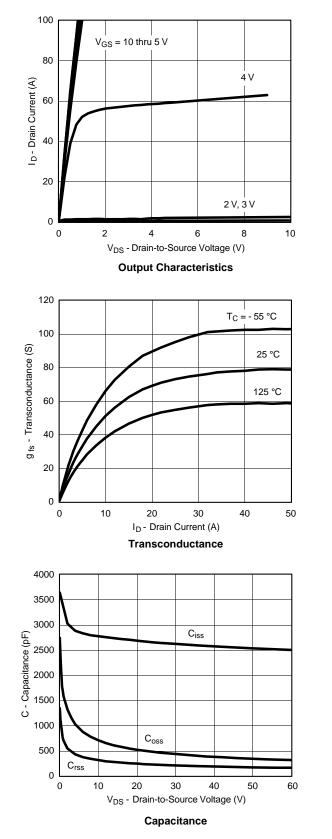
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

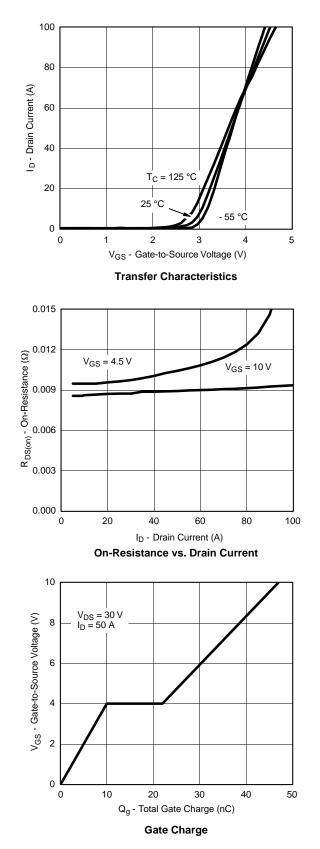
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



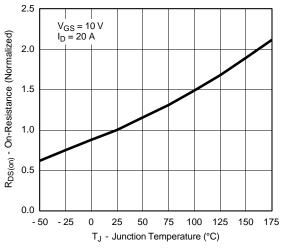
TYPICAL CHARACTERISTICS (25 °C unless noted)



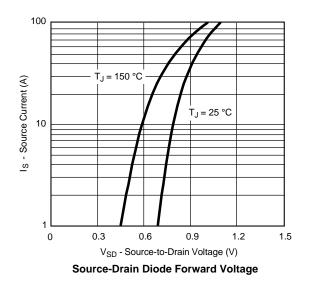




TYPICAL CHARACTERISTICS (25 °C unless noted)

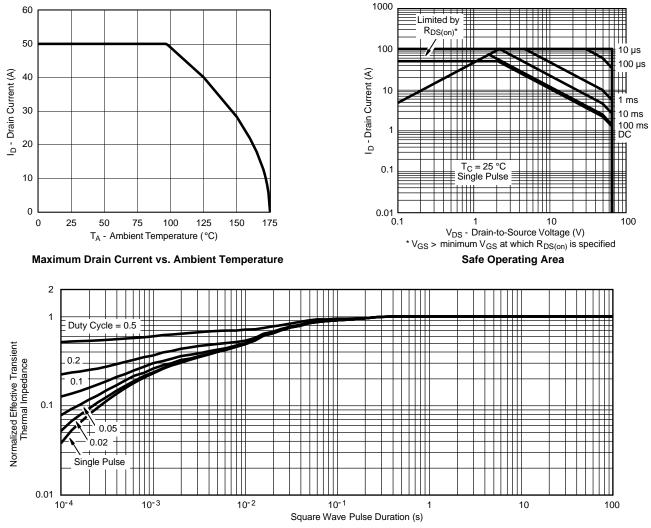


On-Resistance vs. Junction Temperature





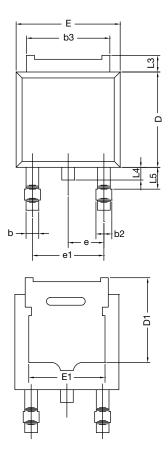
THERMAL RATINGS

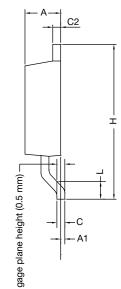


Normalized Thermal Transient Impedance, Junction-to-Case



TO-252AA CASE OUTLINE





	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0 DWG: 5347	0247-Rev. M,	24-Dec-12			

Note

• Dimension L3 is for reference only.



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.