

SPD15P10PLG-VB Datasheet P-Channel 100 V (D-S) MOSFET

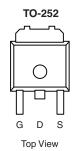
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω) I _D (A)		Q _g (Typ.)		
- 100	0.100 at V _{GS} = - 10 V	- 16	11.7		
- 100	0.120 at V_{GS} = - 4.5 V	- 15	11.7		

FEATURES

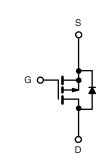
- Halogen-free According to IEC 61249-2-21
 Definition
- Trench Power MOSFET
- 100 % R_q and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Power Switch
- DC/DC Converters



Drain Connected to Tab



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25 \text{ °C}$, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 100	v		
Gate-Source Voltage	V _{GS}	± 20	v		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 25 °C	1-	- 16		
Continuous Drain Current $(1) = 150^{\circ}$ C)	T _C = 70 °C	I _D	- 14	•	
Pulsed Drain Current		I _{DM}	- 50	- A	
Avalanche Current		I _{AS}	- 18		
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	16.2	mJ	
	T _C = 25 °C	P	32.1 ^b		
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	2.5	- w	
Operating Junction and Storage Temperature Ra	inge	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	50	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3.9	0/10		

Notes:

a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).



COMPLIANT HALOGEN

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		· · · · · · · · · · · · · · · · · · ·			1	•	
Drain-Source Breakdown Voltage	V _{DS}	V_{DS} $V_{DS} = 0 V, I_{D} = -250 \mu A$				v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 2.5	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA	
		$V_{DS} = -100 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μA	
		V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 150 °C			- 250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 10 V, V_{GS} = - 10 V	- 15			A	
	B	V _{GS} = - 10 V, I _D = - 3.6 A		0.100		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 3.4 A		0.120			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 3.6 A		12		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1055		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 50 V, f = 1 MHz		65			
Reverse Transfer Capacitance	C _{rss}			41			
Total Gate Charge ^c	Qg	$V_{DS} = -50 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -3.6 \text{ A}$		23.2	34.8	nC	
Total Gate Charge				11.7	17.6		
Gate-Source Charge ^c	Q _{gs}	V_{DS} = - 50 V, V_{GS} = - 4.5 V, I_{D} = - 3.6 A		3.5			
Gate-Drain Charge ^c	Q _{gd}			4.8			
Gate Resistance	Rg	f = 1 MHz	1.2	5.7	11.5	Ω	
Turn-On Delay Time ^c	t _{d(on)}			7	14		
Rise Time ^c	t _r	V_{DD} = - 50 V, R_L = 17.2 Ω		12	18	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_{D}\cong$ - 2.9 A, V_{GEN} = - 10 V, R_{g} = 1 Ω		33	50	115	
Fall Time ^c	t _f			9	18		
Drain-Source Body Diode Ratings an	nd Characteri	stics T _C = 25 °C ^b					
Continuous Current	۱ _S				- 8.8	Α	
Pulsed Current	I _{SM}				- 15	A	
Forward Voltage ^a	V _{SD}	I _F = - 2.9 A, V _{GS} = 0 V		- 0.8	- 1.5	V	
Reverse Recovery Time	t _{rr}			50	75	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	Α	
Reverse Recovery Charge	Q _{rr}	1 [98	147	nC	

Notes:

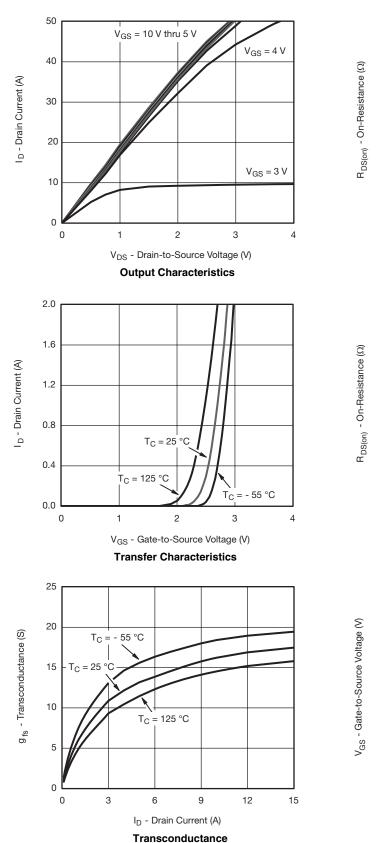
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.c. Independent of operating temperature.

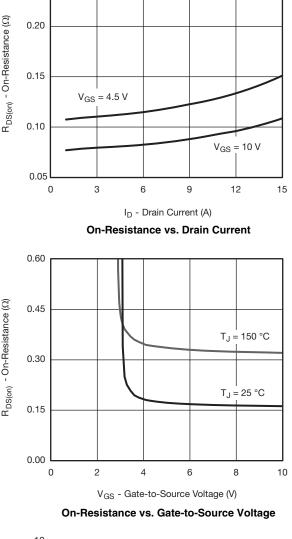
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

semi VBsemi.com

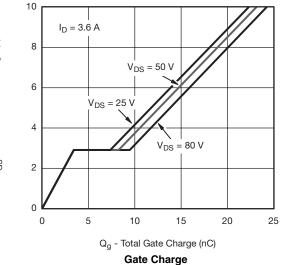




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

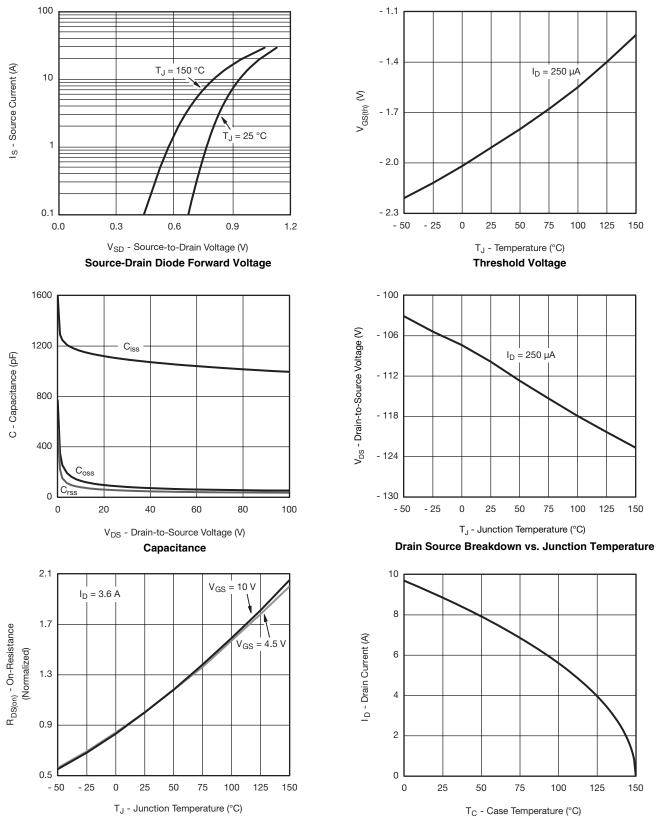


0.25





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



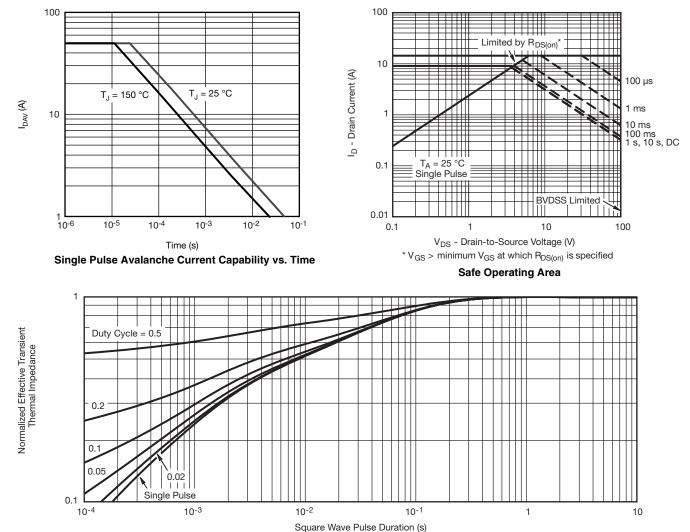
On-Resistance vs. Junction Temperature



Current Derating

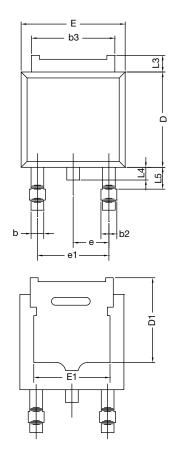


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

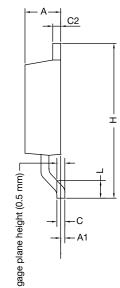


Normalized Thermal Transient Impedance, Junction-to-Case





TO-252AA Case Outline



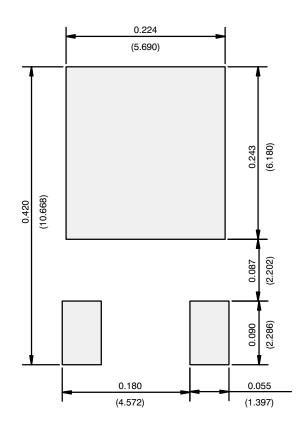
	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
С	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	4.10	-	0.161	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
Н	9.40	10.41	0.370	0.410
е	2.28	BSC 0.090 BSC		BSC
e1	4.56	BSC	0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.01	1.52	0.040	0.060
ECN: T16-0236-Rev. P, 16-May-16 DWG: 5347				

Notes

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.