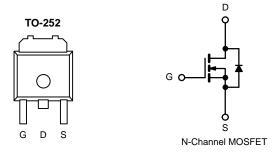


P6015AD-VB Datasheet N-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
150	0.032 at V _{GS} = 10 V	40	23 nC			
150	0.045 at V _{GS} = 8 V	35	23110			



FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Extremely Low Q_{gd} for Switching Losses
- 100 % R_g Tested
- 100 % Avalanche Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

• Primary Side Switch



COMPLIANT HALOGEN FREE Available

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	150	V
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		40	
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C	1 , 1	35	
Continuous Drain Current $(T_J = 150 \text{ C})$	T _A = 25 °C		35.5 ^{b, c}	
	T _A = 70 °C	1 1	34.5 ^{b, c}	A
Pulsed Drain Current		I _{DM}	50	A
Continuous Source-Drain Diode Current	T _C = 25 °C		4.5	
Commuous Source-Drain Diode Current	T _A = 25 °C	۱ _S	2.6 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	20	
Single Pulse Avalanche Energy		E _{AS}	20	mJ
	T _C = 25 °C		5.9	
Maximum Dower Discinction	T _C = 70 °C		3.8	w
Maximum Power Dissipation	T _A = 25 °C	P _D	3.1 ^{b, c}	VV
	T _A = 70 °C	1 1	2 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, †}	t ≤ 10 s	R _{thJA}	33	40	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	17	21			

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 80 °C/W.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•		•	•	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	150			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 250 4		172		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 10		mv/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS} -	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			1 10	μA	
On-State Drain Current ^a		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, 1_{J} = 55 \text{ C}$ $V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	30		10	A	
	I _{D(on)}	$V_{GS} = 10 \text{ V}, \text{ V}_{GS} = 5 \text{ A}$	00	0.032		~~~	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 8 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.045		Ω	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		23		S	
Dynamic ^b	315					I -	
Input Capacitance	C _{iss}			1735			
Output Capacitance	C _{oss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		160		pF	
Reverse Transfer Capacitance	C _{rss}			37		1	
Table Oaks Observe		$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		28.5	43	nC	
Total Gate Charge	Qg			23	35		
Gate-Source Charge	Q _{qs}	$V_{DS} = 75 \text{ V}, V_{GS} = 8 \text{ V}, I_{D} = 5 \text{ A}$		8			
Gate-Drain Charge	Q _{gd}			6.5			
Gate Resistance	R _g	f = 1 MHz		0.85	1.3	Ω	
Turn-on Delay Time	t _{d(on)}			14	21		
Rise Time	t _r	V_{DD} = 50 V, R_L = 10 Ω		12	18	- ns	
Turn-Off Delay Time	t _{d(off)}	${\rm I_D}\cong 5$ A, ${\rm V_{GEN}}$ = 10 V, ${\rm R_g}$ = 1 Ω		22	33		
Fall Time	t _f			6	10		
Turn-On Delay Time	t _{d(on)}			16	24		
Rise Time	t _r	V_{DD} = 50 V, R_L = 10 Ω		12	18		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$		20	30		
Fall Time	t _f			7	12		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			7.7	А	
Pulse Diode Forward Current ^a	I _{SM}				50	~	
Body Diode Voltage	V _{SD}	I _S = 2.6 A		0.77	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			63	95	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 5 A, dl/dt = 100 A/μs, T _J = 25 °C		110	165	nC	
Reverse Recovery Fall Time	t _a	$r_{\rm F} = 5$ A, u/ul = 100 A/µs, $r_{\rm J} = 25$ °C		49		ns	
Reverse Recovery Rise Time	t _b			14			

Notes:

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$

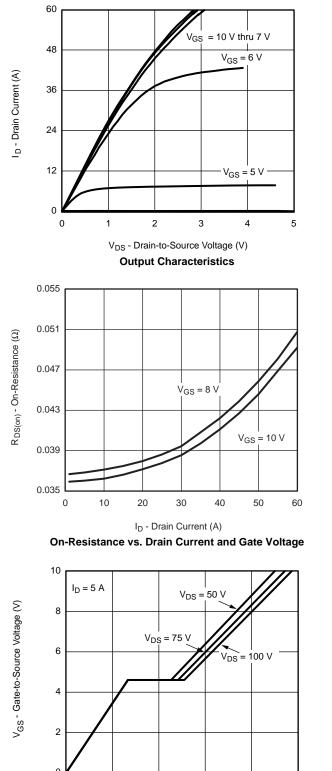
a. Guaranteed by design, not subject to production testing.

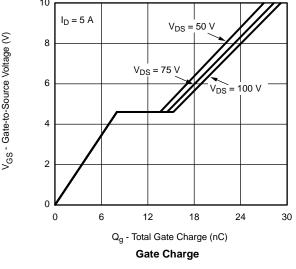
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

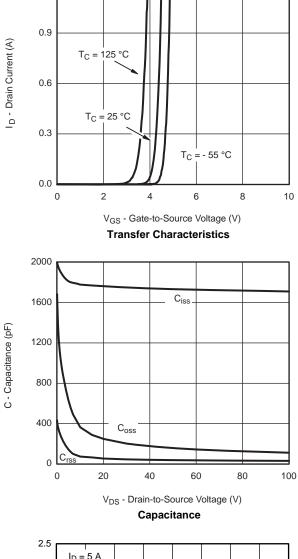
Bsemi

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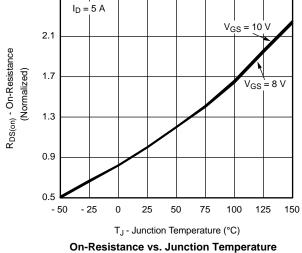




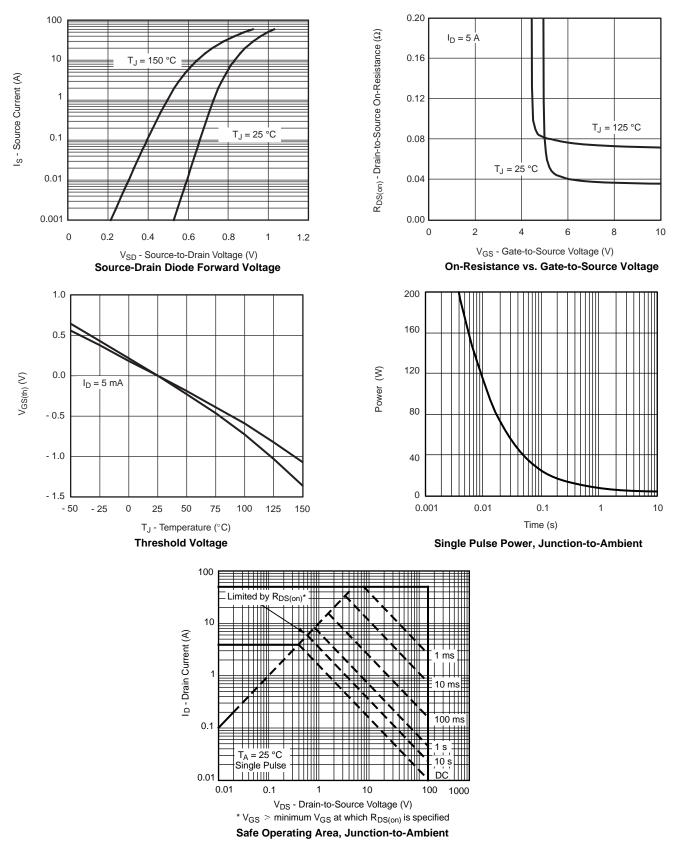




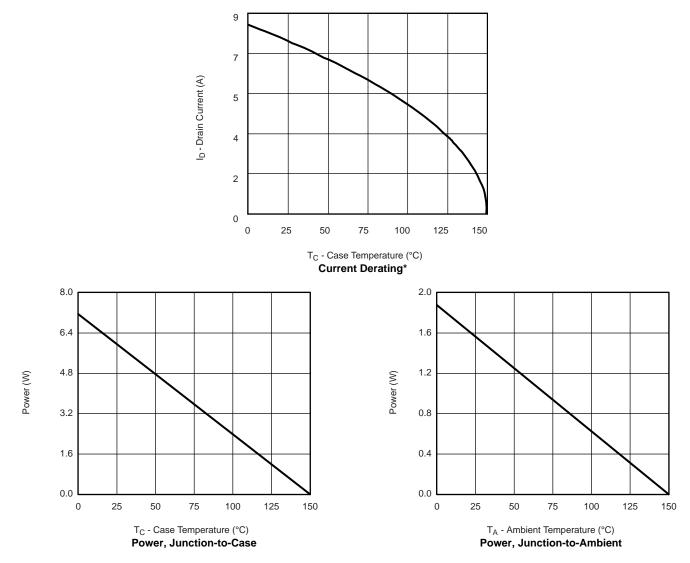
1.2





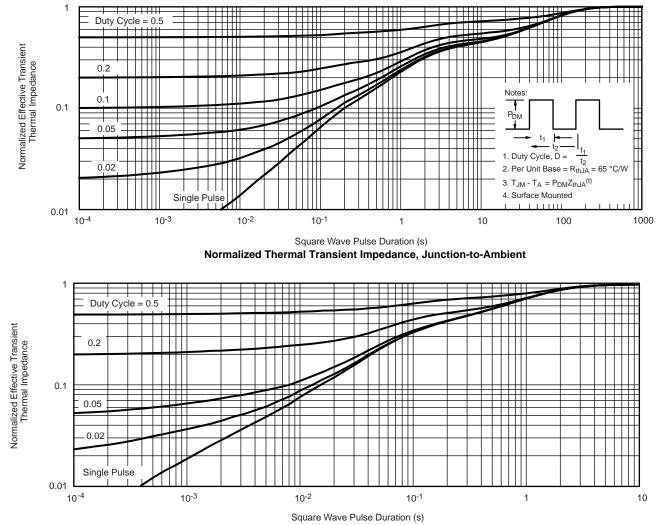






* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

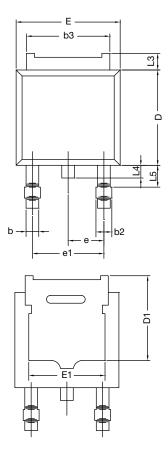


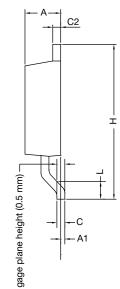


Normalized Thermal Transient Impedance, Junction-to-Foot



TO-252AA CASE OUTLINE





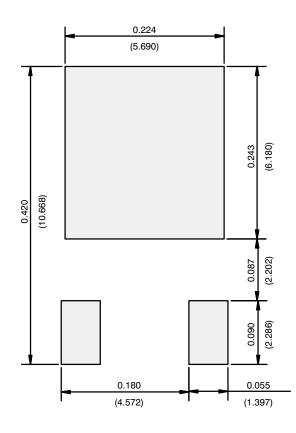
	MILLIN	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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