

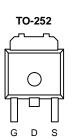
HM20N15KA-VB Datasheet N-Channel 150 V (D-S) MOSFET

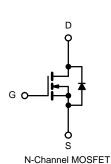
PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
150	0.032 at V _{GS} = 10 V	40	23 nC			
130	0.045 at V _{GS} = 8 V	35	23110			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Extremely Low Q_{gd} for Switching Losses
- 100 % R_g Tested
- 100 % Avalanche Tested
- Compliant to RoHS Directive 2002/95/EC







APPLICATIONS

· Primary Side Switch

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	150	V	
Gate-Source Voltage		V_{GS}	± 20		
-	T _C = 25 °C		40		
Continuous Prain Current (T. 150 °C)	T _C = 70 °C	1 , 1	35		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	- I _D	35.5 ^{b, c}		
	T _A = 70 °C		34.5 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	50		
Continuous Source-Drain Diode Current	T _C = 25 °C		4.5		
Continuous Source-Diam Diode Current	T _A = 25 °C	I _S	2.6 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	20		
Single Pulse Avalanche Energy		E _{AS}	20	mJ	
	T _C = 25 °C		5.9		
Maximum Power Dissipation	T _C = 70 °C		3.8	w	
Maximum Fower Dissipation	T _A = 25 °C	P _D	3.1 ^{b, c}	VV	
	T _A = 70 °C	1	2 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stq}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, †}	t ≤ 10 s	R _{thJA}	33	40	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{th,IF}$	17	21	C/ VV		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under steady state conditions is 80 °C/W.



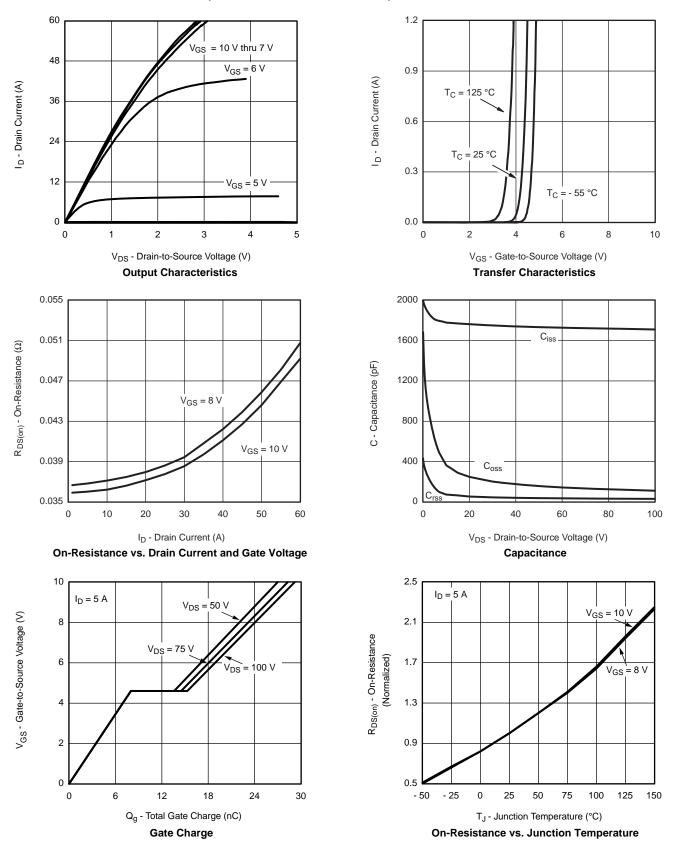
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					L		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	150			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250A		172		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = 250 μA		- 10		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current		V _{DS} = 100 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a		$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	0.032			Ω	
Dialii-Source Oil-State Resistance	R _{DS(on)}	V _{GS} = 8 V, I _D = 5 A		0.045			
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5 A		23		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1735		pF	
Output Capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		160			
Reverse Transfer Capacitance	C _{rss}			37			
Total Gata Chargo	Qg	V _{DS} = 75 V, V _{GS} = 10 V, I _D = 5 A		28.5 43	43	nC	
Total Gate Charge				23	35		
Gate-Source Charge	Q_{gs}	$V_{DS} = 75 \text{ V}, V_{GS} = 8 \text{ V}, I_{D} = 5 \text{ A}$		8			
Gate-Drain Charge	Q_{gd}			6.5			
Gate Resistance	R_g	f = 1 MHz		0.85	1.3	Ω	
Turn-on Delay Time	t _{d(on)}			14	21		
Rise Time	t _r	V_{DD} = 50 V, R_L = 10 Ω		12	18	- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		22	33		
Fall Time	t _f			6	10		
Turn-On Delay Time	t _{d(on)}			16	24		
Rise Time	t _r	V_{DD} = 50 V, R_L = 10 Ω		12	18		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$		20	30		
Fall Time	t _f			7	12		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7.7	Α	
Pulse Diode Forward Current ^a	I _{SM}				50		
Body Diode Voltage	V _{SD}	I _S = 2.6 A		0.77	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			63	95	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 5 A dl/dt = 100 A/up T = 25 °C		110	165	nC	
Reverse Recovery Fall Time	t _a	$I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		49			
Reverse Recovery Rise Time	t _b			14		ns	

Notes:

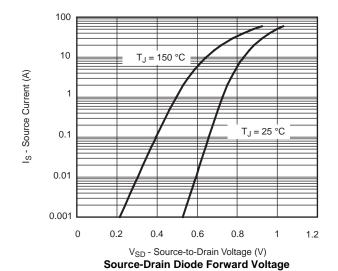
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- a. Guaranteed by design, not subject to production testing.

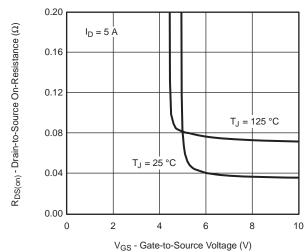
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



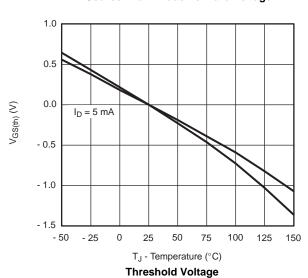


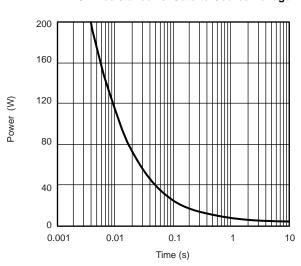




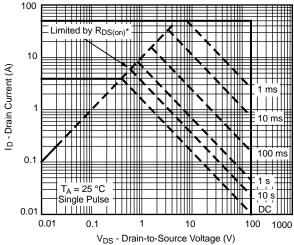


On-Resistance vs. Gate-to-Source Voltage





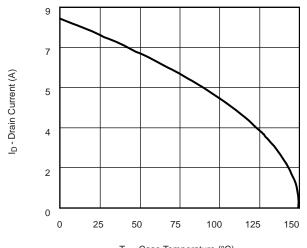
Single Pulse Power, Junction-to-Ambient



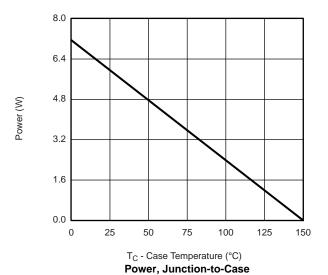
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

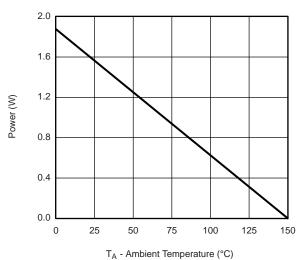
Safe Operating Area, Junction-to-Ambient





T_C - Case Temperature (°C) **Current Derating***

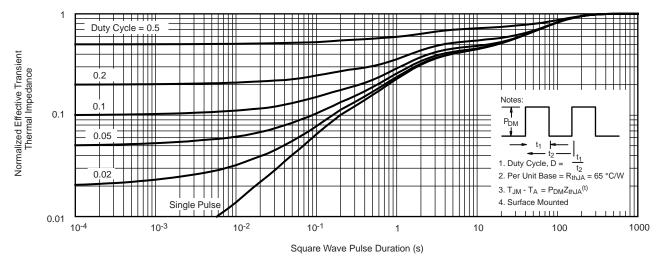




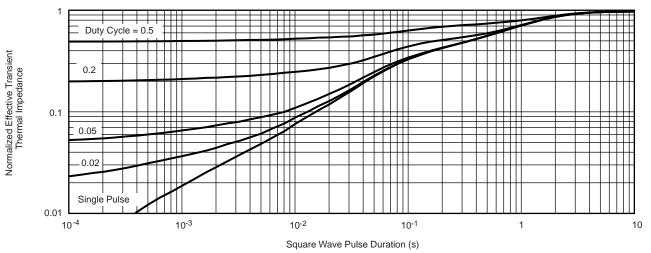
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





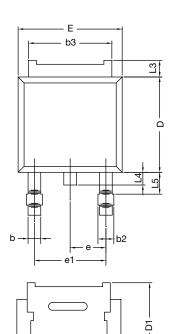
Normalized Thermal Transient Impedance, Junction-to-Ambient



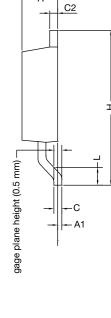
Normalized Thermal Transient Impedance, Junction-to-Foot



TO-252AA CASE OUTLINE



E1



	MILLIMETERS INCH			HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: Y12-0247-Rev. M. 24-Dec-12					

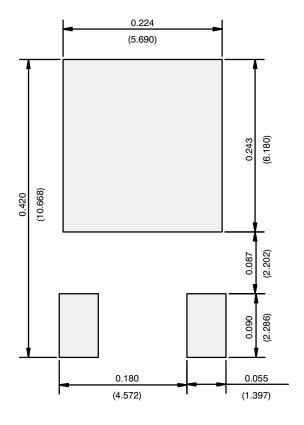
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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