

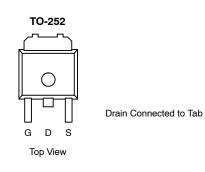
### H6N03LA-VB Datasheet

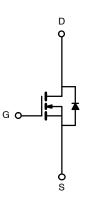
## N-Channel 20-V (D-S)175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>			
20	0.0045 @ V <sub>GS</sub> = 4.5 V	100			
	0.006 @ V <sub>GS</sub> = 2.5 V	90			

#### **FEATURES**

- Trench Power MOSFET
- 175°C Maximum Junction Temperature
- 100% R<sub>g</sub> Tested





N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	20	v		
Gate-Source Voltage		V <sub>GS</sub>	±15		
	$T_{C} = 25^{\circ}C$		100		
Continuous Drain Current <sup>a</sup>	$T_{C} = 100^{\circ}C$		80		
Pulsed Drain Current		I <sub>DM</sub>	200	A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		Is	65		
	$T_{C} = 25^{\circ}C$		71		
Maximum Power Dissipation	$T_A = 25^{\circ}C$	P <sub>D</sub>	8.3 <sup>b, c</sup>	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 175	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
	$t \le 10$ sec.		15	18	°C/W		
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	40	50			
Maximum Junction-to-Case		R <sub>thJC</sub>	1.75	2.1			

Notes

a. Package Limited

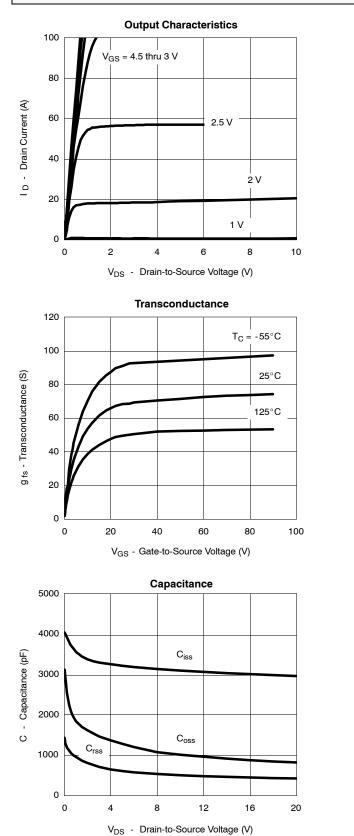
b. Surface Mounted on 1" x 1" FR4 Board

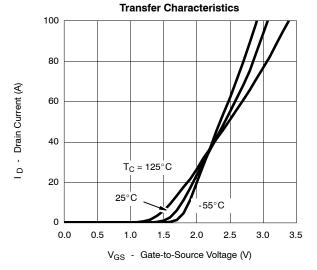
c.  $t \leq 10 \text{ sec}$ 

Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit
Static			•			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu$ A	20			v
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.5		1.5	v
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ±12 V			±100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
	DSS	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_J$ = 125 $^{\circ}C$			50	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS}$ = 5 V, $V_{GS}$ = 4.5 V	100			Α
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0045		Ω
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	$V_{GS}$ = 4.5 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0055		
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 20 A		0.006		
Forward Transconductanceb	9fs	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 40 \text{ A}$	20			S
Dynamic <sup>a</sup>			•			•
Input Capacitance	C <sub>iss</sub>			3660		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 20 V, f = 1 MHz		730		
Reverse Transfer Capacitance	C <sub>rss</sub>			375		
Total Gate Charge <sup>c</sup>	Qg			26	35	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 40 A		5		nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			7		
Gate Resistance	Rg		1		3.7	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			20	35	- ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 0.25 $\Omega$		120	190	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 40$ Å, $V_{GEN} = 4.5$ V, $R_G = 2.5 \Omega$		45	70	
Fall Time <sup>c</sup>	t <sub>f</sub>			20	35	
Source-Drain Diode Ratings ar	d Characteristi	c (T <sub>C</sub> = 25°C)	•			
Pulsed Current	I <sub>SM</sub>				100	А
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	$I_{F}$ = 100 A, $V_{GS}$ = 0 V		1.2	1.5	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 40 A, di/dt = 100 A/µs		35	70	ns



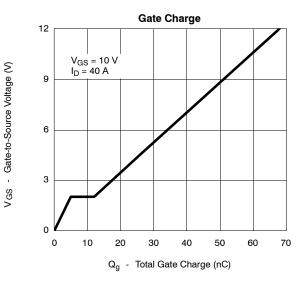
#### **TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**



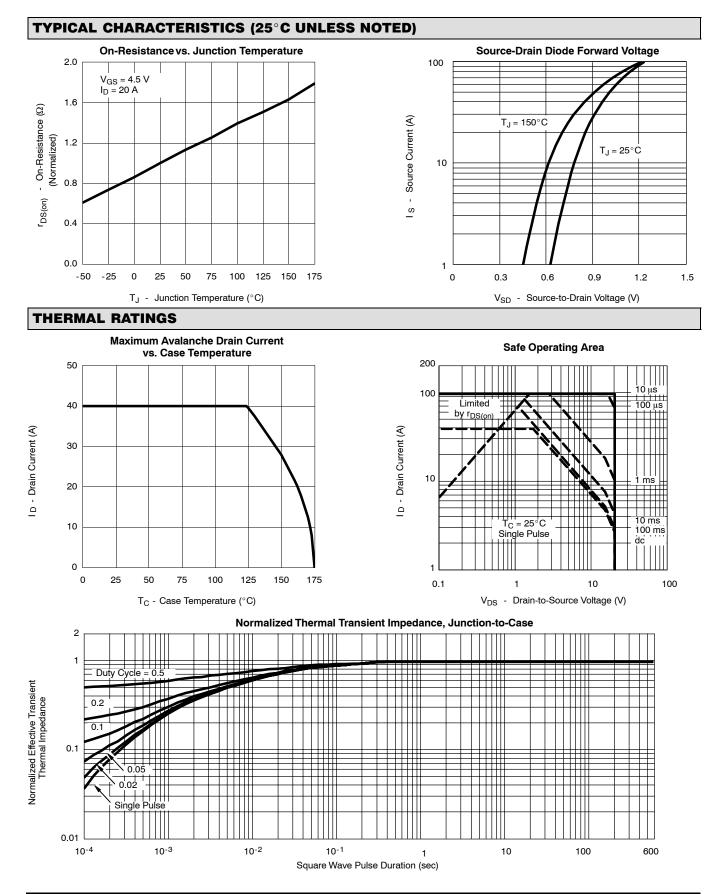


On-Resistance vs. Drain Current 0.012 0.009 V<sub>GS</sub> = 4.5 V 0.006 V<sub>GS</sub> = 4.5 V V<sub>GS</sub> = 4.5 V 0.000 

I<sub>D</sub> - Drain Current (A)

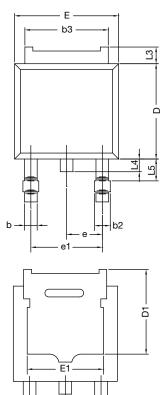


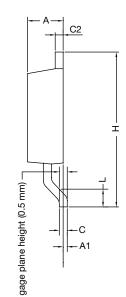






## **TO-252AA CASE OUTLINE**





	MILLIN	<b>METERS</b>	INC	HES		
DIM.	MIN.	MAX.	MIN.	MAX.		
А	2.18	2.38	0.086	0.094		
A1	-	0.127	-	0.005		
b	0.64	0.88	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215		
С	0.46	0.61	0.018	0.024		
C2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245		
D1	5.21	-	0.205	-		
E	6.35	6.73	0.250	0.265		
E1	4.32	-	0.170	-		
Н	9.40	10.41	0.370	0.410		
е	2.28	BSC	0.090 BSC			
e1	4.56	BSC	0.180 BSC			
L	1.40	1.78	0.055	0.070		
L3	0.89	1.27	0.035	0.050		
L4	-	1.02	-	0.040		
L5	1.14	1.52	0.045	0.060		
	ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

## **Material Category Policy**

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.