

FQD4N25-VB Datasheet **Power MOSFET**

PRODUCT SUMMARY					
V _{DS} (V)	250				
R _{DS(on)} (Ω)	V _{GS} = 10 V	0.64			
Q _g (Max.) (nC)	14				
Q _{gs} (nC)	2.7				
Q _{gd} (nC)	7.8				
Configuration	Single				

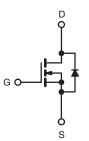
FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Available in Tape and Reel
- Fast Switching
- Ease of Paralleling









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C	= 25 °C, unl	less otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	250		
Gate-Source Voltage			V _{GS}	± 20	- V	
Continuous Drain Current	V _{GS} at 10 V	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$		4.5		
Continuous Drain Gurrent		T _C = 100 °C	I _D	3.0	Α	
Pulsed Drain Current ^a			I _{DM}	16	1	
Linear Derating Factor				0.33	W//9C	
Linear Derating Factor (PCB Mount)e				0.020	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	130	mJ	
Repetitive Avalanche Currenta			I _{AR}	4.5	А	
Repetitive Avalanche Energy ^a			E _{AR}	5.2	mJ	
Maximum Power Dissipation	T _C = 25 °C		D	45	W	
Maximum Power Dissipation (PCB Mount)e	T _A = 25 °C		P _D 2.5] vv	
Peak Diode Recovery dV/dtc			dV/dt	4.8	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150	- °C	
Soldering Recommendations (Peak Temperature) ^d	for 10 s		-	260	7	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. $V_{DD} = 50 \text{ V}$; starting $T_J = 25 \text{ °C}$, L = 14 mH, $R_g = 25 \Omega$, $I_{AS} = 3.8 \text{ A}$ (see fig. 12). c. $I_{SD} \le 3.8 \text{ A}$, $dI/dt \le 90 \text{ A/µs}$, $V_{DD} \le V_{DS}$, $T_J \le 150 \text{ °C}$.
- d. 1.6 mm from case.
- e. When mounted on 1" square PCB (FR-4 or G-10 material) .



THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient (PCB Mount) ^a	R _{thJA}	-	50			
Maximum Junction-to-Ambient	R _{thJA}	-	110	°C/W		
Maximum Junction-to-Case	R _{thJC}	-	3.0			

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		250	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I _D = 1 mA	-	0.36	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	,	V _{GS} = ± 20 V	-	-	± 100	nA
Zoro Coto Voltago Drain Current		V _{DS} = 250 V, V _{GS} = 0 V		-	-	25	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200 V	200 V, V _{GS} = 0 V, T _J = 125 °C		-	250	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	$I_D = 2.3 A^b$	-	0.64	-	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	= 50 V, I _D = 2.3 A ^b	1.5	-	-	S
Dynamic							
Input Capacitance	C _{iss}		V _{GS} = 0 V,	-	260	-	
Output Capacitance	Coss]	$V_{DS} = 25 \text{ V},$	-	77	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1.	0 MHz, see fig. 5 ^c	-	15	-	
Total Gate Charge	Q_g			-	-	14	nC
Gate-Source Charge	Q_{gs}	V _{GS} = 10 V	$I_D = 4.4 \text{ A}, V_{DS} = 200 \text{ V},$ see fig. 6 and $13^{b, c}$	-	-	2.7	
Gate-Drain Charge	Q_{gd}		ground to	-	-	7.8	
Turn-On Delay Time	t _{d(on)}	V_{DD} = 125 V, I_{D} = 4.4 A, R_{G} = 18 Ω , R_{D} = 28 Ω , see fig. 10 ^{b, c}		-	7.0	-	- ns
Rise Time	t _r			-	13	-	
Turn-Off Delay Time	t _{d(off)}			-	20	-	
Fall Time	t _f			-	12	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	-11
Internal Source Inductance	L _S			-	7.5	-	nH
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	3.8	A
Pulsed Diode Forward Current ^a	I _{SM}			-	-	15	^
Body Diode Voltage	V_{SD}	$T_J = 25 ^{\circ}\text{C}, I_S = 3.8 \text{A}, V_{GS} = 0 \text{V}^{\text{b}}$		-	-	1.8	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = 4.4 A, dI/dt = 100 A/µs ^b		-	200	400	ns
Body Diode Reverse Recovery Charge	Q _{rr}			-	0.93	1.9	μC
Forward Turn-On Time	t _{on}	Intrinsic tu	n-on is dominated by L _S and L _D)			L _D)	

- Notes a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. Pulse width \leq 300 μ s; duty cycle \leq 2 %.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

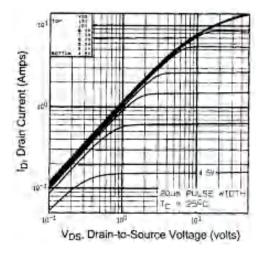


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

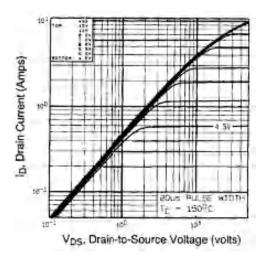


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

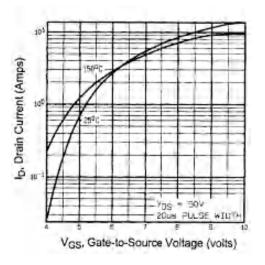


Fig. 3 - Typical Transfer Characteristics

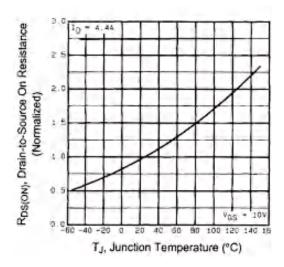


Fig. 4 - Normalized On-Resistance vs. Temperature



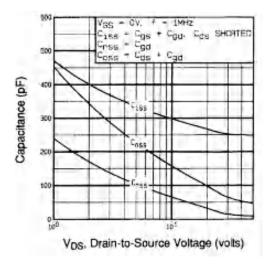


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

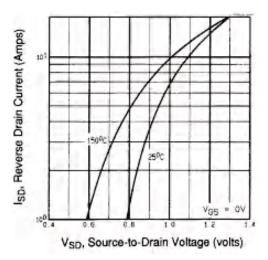


Fig. 7 - Typical Source-Drain Diode Forward Voltage

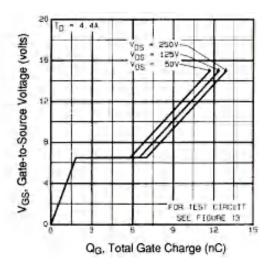


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

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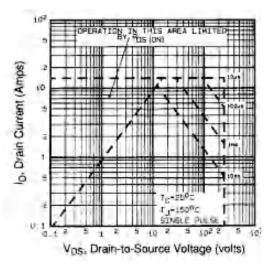


Fig. 8 - Maximum Safe Operating Area

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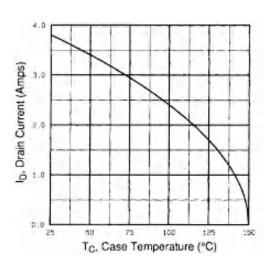


Fig. 9 - Maximum Drain Current vs. Case Temperature

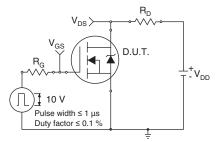


Fig. 10a - Switching Time Test Circuit

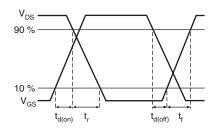


Fig. 10b - Switching Time Waveforms

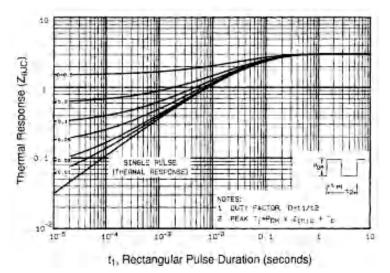
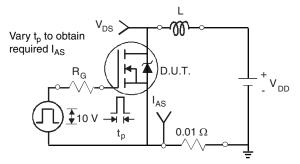
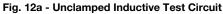


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case







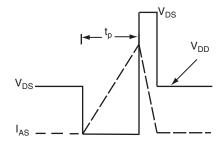


Fig. 12b - Unclamped Inductive Waveforms

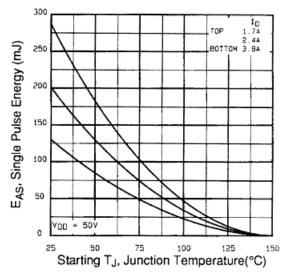


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

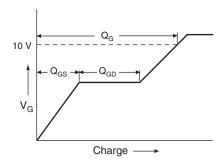


Fig. 13a - Basic Gate Charge Waveform

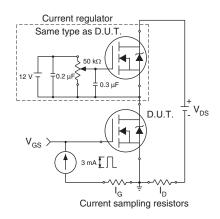
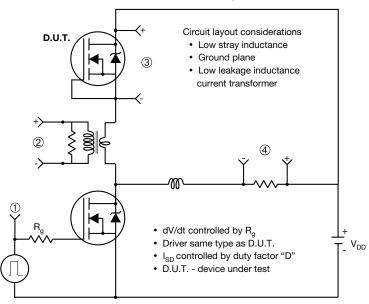


Fig. 13b - Gate Charge Test Circuit



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Peak Diode Recovery dV/dt Test Circuit



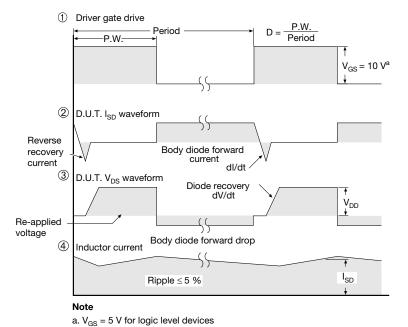
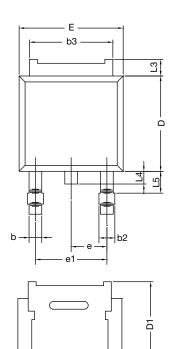
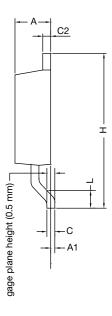


Fig. 14 - For N-Channel



TO-252AA Case Outline





	MILLIMETERS		INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
FCN: T16-0236-Rev. P. 16-May-16					

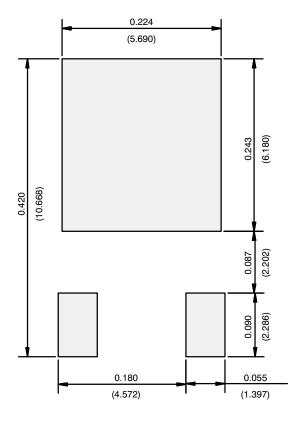
ECN: T16-0236-Rev. P, 16-May-16 DWG: 5347

Notes

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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