

AUIRLR3410-VB Datasheet

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY

| V_{DS} (V) | $R_{DS(on)}$ (Ω) | I_D (A) |
|--------------|---------------------------|-----------|
| 100 | 0.114 at $V_{GS} = 10$ V | 15 |

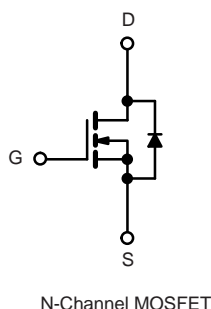
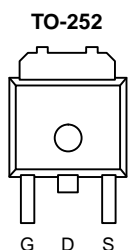
FEATURES

- Trench Power MOSFET
- 150 °C Junction Temperature
- PWM Optimized
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Primary Side Switch



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

| Parameter | | Symbol | Limit | Unit |
|---|-------------------------|-----------------------------------|-----------------|------|
| Drain-Source Voltage | | V _{DS} | 100 | V |
| Gate-Source Voltage | | V _{GS} | ± 20 | |
| Continuous Drain Current (T _J = 175 °C) ^b | T _C = 25 °C | I _D | 15 | A |
| | T _C = 125 °C | | 13 | |
| Pulsed Drain Current | | I _{DM} | 40 | |
| Continuous Source Current (Diode Conduction) | | I _S | 3 | |
| Avalanche Current | | I _{AS} | 3 | |
| Single Pulse Avalanche Energy | L = 0.1 mH | E _{AS} | 18 | mJ |
| Maximum Power Dissipation | T _C = 25 °C | P _D | 96 ^b | W |
| | T _A = 25 °C | | 3 ^a | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | °C |

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Typical | Maximum | Unit |
|----------------------------------|------------|---------------|---------|------|
| Junction-to-Ambient ^a | R_{thJA} | $t \leq 10$ s | 15 | °C/W |
| | | Steady State | 40 | |
| Junction-to-Case (Drain) | R_{thJC} | 0.85 | 1.1 | |

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.

| SPECIFICATIONS (T _J = 25 °C, unless otherwise noted) | | | | | | |
|---|---------------------|---|------|-------------------|-------|------|
| Parameter | Symbol | Test Conditions | Min. | Typ. ^a | Max. | Unit |
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | V _{GS} = 0 V, I _D = 250 μA | 100 | | | V |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 250 μA | 1.0 | | 3.5 | |
| Gate-Body Leakage | I _{GSS} | V _{DS} = 0 V, V _{GS} = ± 20 V | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 100 V, V _{GS} = 0 V | | | 1 | μA |
| | | V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C | | | 50 | |
| | | V _{DS} = 100 V, V _{GS} = 0 V, T _J = 175 °C | | | 250 | |
| On-State Drain Current ^b | I _{D(on)} | V _{DS} = 5 V, V _{GS} = 10 V | 40 | | | A |
| Drain-Source On-State Resistance ^b | R _{DS(on)} | V _{GS} = 10 V, I _D = 3 A | | 0.114 | | Ω |
| | | V _{GS} = 10 V, I _D = 3 A, T _J = 125 °C | | 0.120 | | |
| | | V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C | | 0.140 | | |
| | | V _{GS} = 4.5 V I _D = 3 A | | 0.120 | | |
| Forward Transconductance ^b | g _{fs} | V _{DS} = 15 V, I _D = 3 A | | 35 | | S |
| Dynamic ^a | | | | | | |
| Input Capacitance | C _{iss} | V _{GS} = 0 V, V _{DS} = 25 V, F = 1 MHz | | 950 | | pF |
| Output Capacitance | C _{oss} | | | 120 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 60 | | |
| Total Gate Charge ^c | Q _g | V _{DS} = 50 V, V _{GS} = 10 V, I _D = 3 A | | 24 | 41 | nC |
| Gate-Source Charge ^c | Q _{gs} | | | 8 | | |
| Gate-Drain Charge ^c | Q _{gd} | | | 12 | | |
| Gate Resistance | R _g | | 0.5 | | 2.9 | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | V _{DD} = 50 V, R _L = 5.2 Ω I _D ≅ 3 A, V _{GEN} = 10 V, R _g = 2.5 Ω | | 15 | 25 | ns |
| Rise Time ^c | t _r | | | 50 | 75 | |
| Turn-Off Delay Time ^c | t _{d(off)} | | | 30 | 45 | |
| Fall Time ^c | t _f | | | 60 | 90 | |
| Source-Drain Diode Ratings and Characteristics (T _C = 25 °C) | | | | | | |
| Pulsed Current | I _{SM} | | | | 5 | A |
| Diode Forward Voltage ^b | V _{SD} | I _F = 3 A, V _{GS} = 0 V | | 0.9 | 1.5 | V |
| Source-Drain Reverse Recovery Time | t _{rr} | I _F = 3 A, dI/dt = 100 A/μs | | 180 | 250 | ns |

Notes:

- Guaranteed by design, not subject to production testing.
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Output Characteristics



Transfer Characteristics



Transconductance



On-Resistance vs. Drain Current



Capacitance



Gate Charge

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

TO-252AA CASE OUTLINE



| DIM. | MILLIMETERS | | INCHES | |
|---------------------------------|-------------|-------|-----------|-------|
| | MIN. | MAX. | MIN. | MAX. |
| A | 2.18 | 2.38 | 0.086 | 0.094 |
| A1 | - | 0.127 | - | 0.005 |
| b | 0.64 | 0.88 | 0.025 | 0.035 |
| b2 | 0.76 | 1.14 | 0.030 | 0.045 |
| b3 | 4.95 | 5.46 | 0.195 | 0.215 |
| C | 0.46 | 0.61 | 0.018 | 0.024 |
| C2 | 0.46 | 0.89 | 0.018 | 0.035 |
| D | 5.97 | 6.22 | 0.235 | 0.245 |
| D1 | 5.21 | - | 0.205 | - |
| E | 6.35 | 6.73 | 0.250 | 0.265 |
| E1 | 4.32 | - | 0.170 | - |
| H | 9.40 | 10.41 | 0.370 | 0.410 |
| e | 2.28 BSC | | 0.090 BSC | |
| e1 | 4.56 BSC | | 0.180 BSC | |
| L | 1.40 | 1.78 | 0.055 | 0.070 |
| L3 | 0.89 | 1.27 | 0.035 | 0.050 |
| L4 | - | 1.02 | - | 0.040 |
| L5 | 1.14 | 1.52 | 0.045 | 0.060 |
| ECN: X12-0247-Rev. M, 24-Dec-12 | | | | |
| DWG: 5347 | | | | |

Note

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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