

STD12L01A-VB Datasheet

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
100	0.110 at $V_{GS} = 10$ V	15
	0.115 at $V_{GS} = 6$ V	15

FEATURES

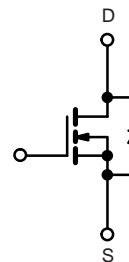
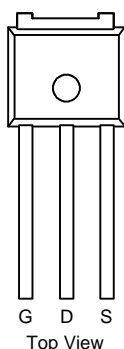
- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- 100 % R_g Tested


RoHS
 COMPLIANT

APPLICATIONS

- Primary Side Switch

TO-251



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C) ^b	I_D	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Pulsed Drain Current	I_{DM}	45	
Continuous Source Current (Diode Conduction)	I_S	15	
Avalanche Current	I_{AR}	15	
Repetitive Avalanche Energy (Duty Cycle ≤ 1 %)	E_{AR}	11.3	mJ
Maximum Power Dissipation	P_D	$T_C = 25$ °C	W
		$T_A = 25$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient ^a	R_{thJA}	$t \leq 10$ s	16	°C/W
		Steady State	45	
Junction-to-Case	R_{thJC}	2	2.4	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.

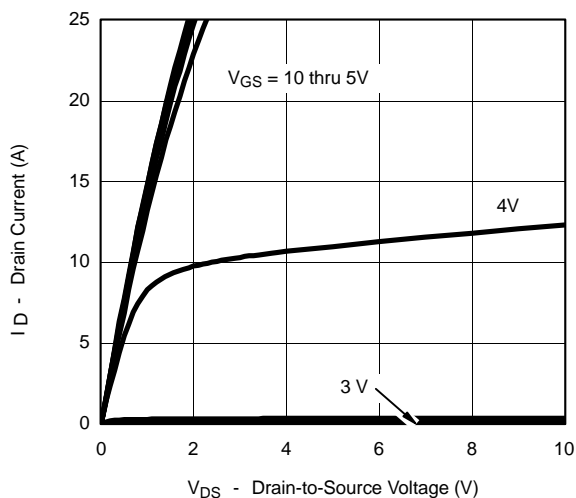
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0		3.0	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	15			A
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.110		Ω
		V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C		0.170		
		V _{GS} = 10 V, I _D = 15 A, T _J = 175 °C		0.230		
		V _{GS} = 6 V, I _D = 10 A		0.115		
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 15 A		25		S
Dynamic ^a						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		892		pF
Output Capacitance	C _{oss}			110		
Reverse Transfer Capacitance	C _{rss}			70		
Total Gate Charge ^c	Q _g	V _{DS} = 75 V, V _{GS} = 10 V, I _D = 15 A		20	25	nC
Gate-Source Charge ^c	Q _{gs}			5.5		
Gate-Drain Charge ^c	Q _{gd}			7		
Gate Resistance	R _g		1		3.2	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 75 V, R _L = 5 Ω I _D ≅ 15 A, V _{GEN} = 10 V, R _G = 2.5 Ω		8	12	ns
Rise Time ^c	t _r			35	55	
Turn-Off Delay Time ^c	t _{d(off)}			17	25	
Fall Time ^c	t _f			30	45	
Source-Drain Diode Ratings and Characteristic (T _C = 25 °C)						
Pulsed Current	I _{SM}				45	A
Diode Forward Voltage ^b	V _{SD}	I _F = 15 A, V _{GS} = 0 V		0.9	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 15 A, dI/dt = 100 A/μs		55	85	ns

Notes:

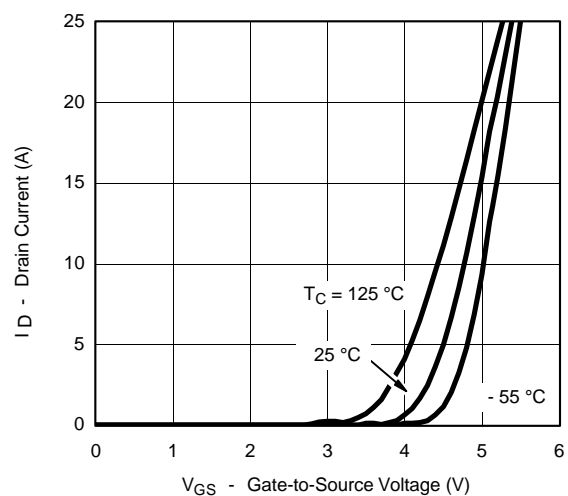
- Guaranteed by design, not subject to production testing.
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

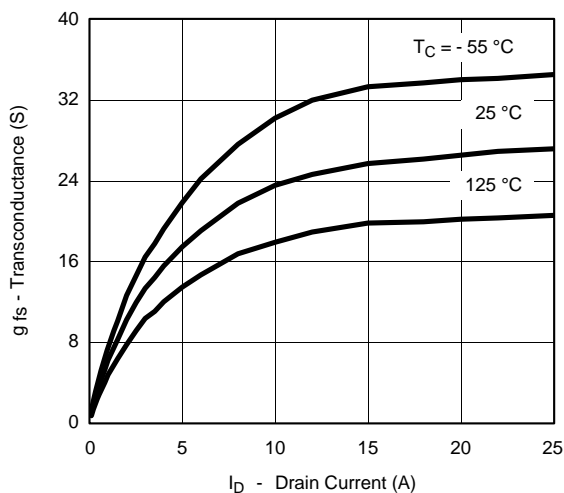
TYPICAL CHARACTERISTICS (25 °C unless noted)



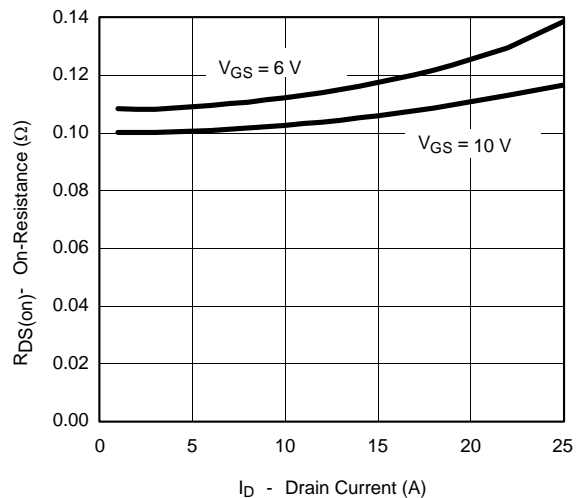
Output Characteristics



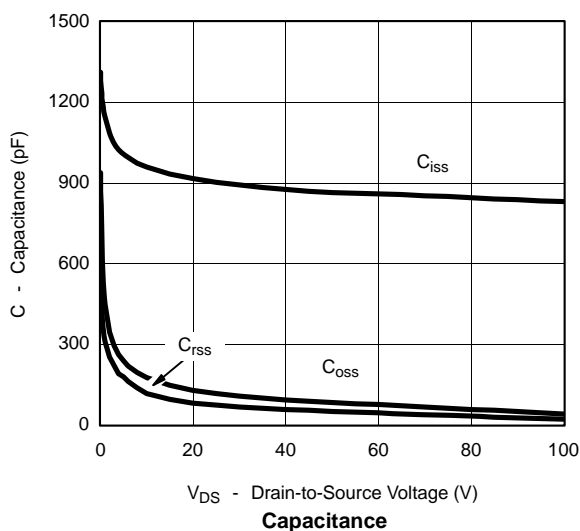
Transfer Characteristics



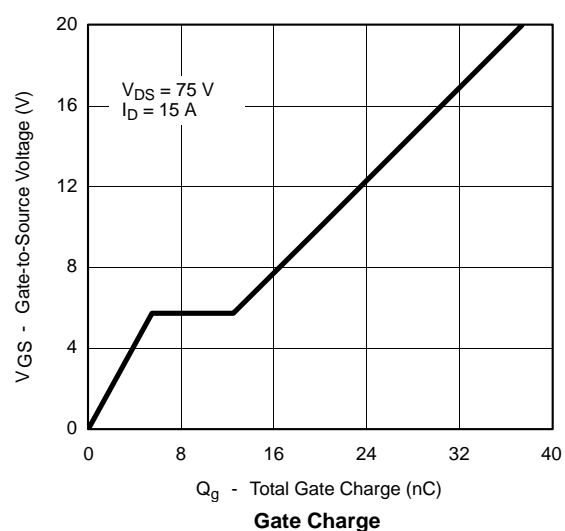
Transconductance



On-Resistance vs. Drain Current

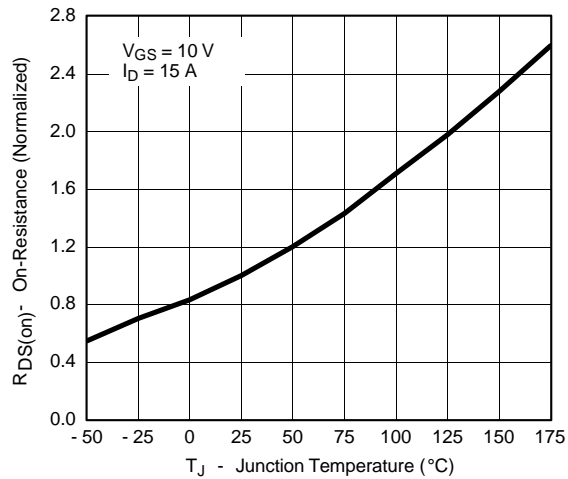


Capacitance

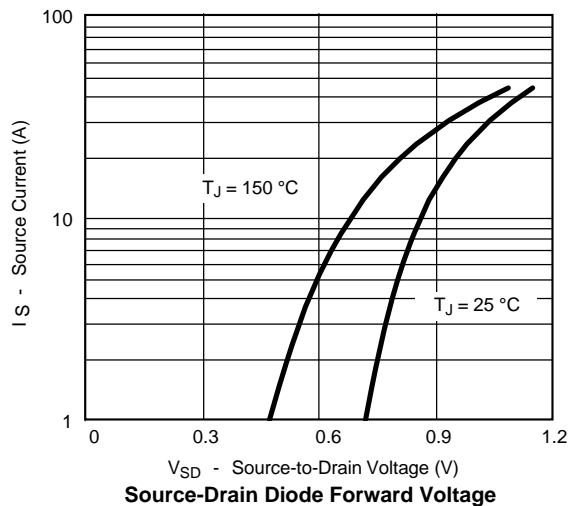


Gate Charge

TYPICAL CHARACTERISTICS (25 °C unless noted)

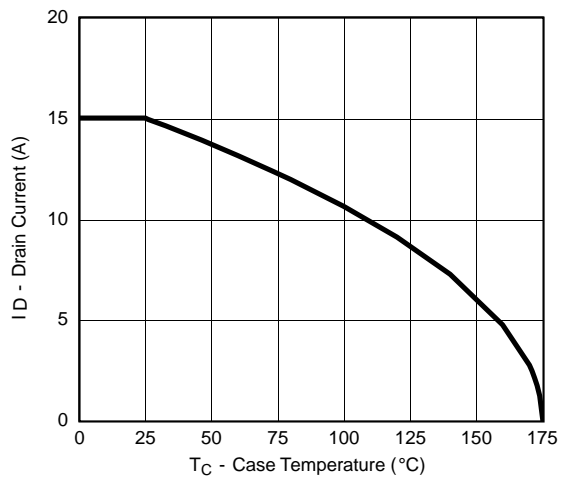


On-Resistance vs. Junction Temperature

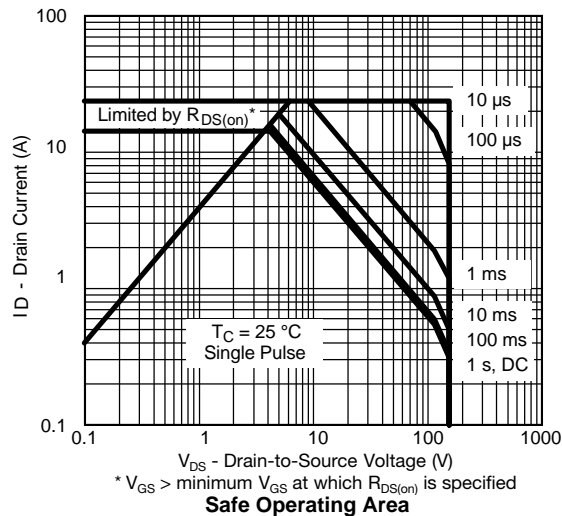


Source-Drain Diode Forward Voltage

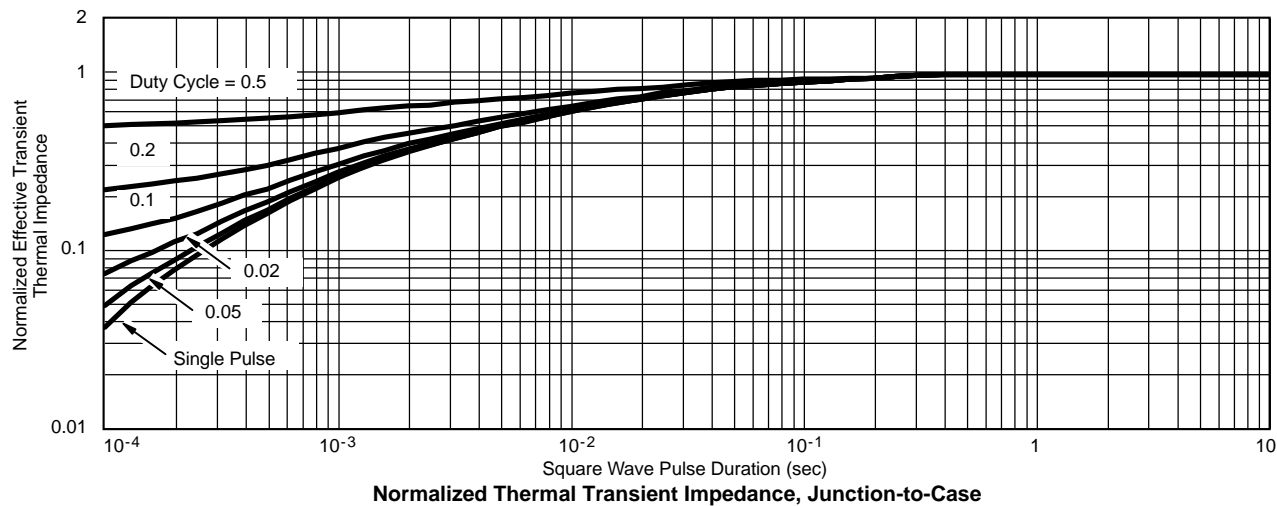
THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature

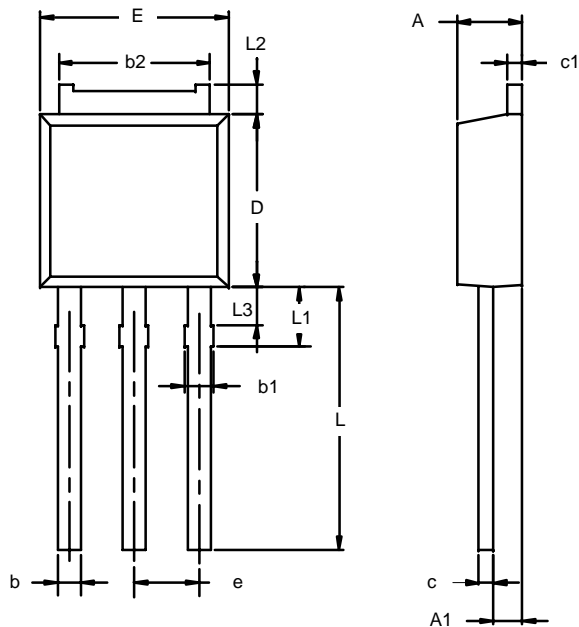


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
c	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
E	6.48	6.73	0.255	0.265
e	2.28 BSC		0.090 BSC	
L	8.89	9.53	0.350	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060
ECN: S-03946—Rev. E, 09-Jul-01 DWG: 5346				

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