

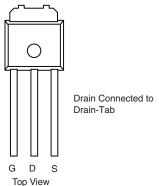
HM30P10L-VB Datasheet P-Channel 100 V (D-S) 175 °C MOSFET

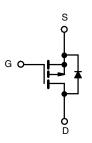
PRODUCT SUMMARY				
V _{DS} (V)	- 100			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.033			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.037			
I _D (A)	- 40			
Configuration	Single			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- Package with Low Thermal Resistance
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	BSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	- 100	V	
Gate-Source Voltage		V_{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C	1	- 40		
Continuous Drain Current	T _C = 125 °C	l _D	- 22		
Continuous Source Current (Diode Conduction) ^a		I _S	- 50	Α	
Pulsed Drain Current ^b		I _{DM}	- 150		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 44		
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	96	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	136	W	
	T _C = 125 °C		45	VV	
Operating Junction and Storage Temperature	e Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	50	°C/W
Junction-to-Case (Drain)		R _{thJC}	1.1	G/ VV

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

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SPECIFICATIONS (T _C = 25 °C,	_	· · · · · ·		MIN.				
PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static	-				T	T		
Drain-Source Breakdown Voltage	V _{DS}	45	0 V, I _D = - 250 μA	- 100	-	-	V	
Gate-Source Threshold Voltage	V _{GS(th)}		V_{GS} , $I_{D} = -250 \mu A$	- 1.0	-	-2.5		
Gate-Source Leakage	I _{GSS}	V _{DS} =	$0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
		$V_{GS} = 0 V$	V _{DS} = - 100 V	-	-	- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 100 V, T _J = 125 °C	-	-	- 50	μA	
		$V_{GS} = 0 V$	$V_{DS} = -100 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	- 250		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	$V_{DS} \le -5 V$	- 30	-	-	Α	
		V _{GS} = - 10 V	I _D = - 9.2 A	ı	0.033	-	S V 5 V 5 V 5 V 5 V 5 V 5 V 5 V 5 V 5 V	
Drain Source On State Registered	R _{DS(on)}	$V_{GS} = -10 \text{ V}$	I _D = - 9.2 A, T _J = 125 °C	ı	0.074	-		
Drain-Source On-State Resistance ^a	1 1DS(on)	$V_{GS} = -10 \text{ V}$	I _D = - 9.2 A, T _J = 175 °C	ı	0.093	-		
		V _{GS} = - 4.5 V	I _D = - 7.7 A	-	0.037	-		
Forward Transconductance ^b	9 _{fs}	V _{DS} =	V _{DS} = - 15 V, I _D = - 9.2 A		35	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}			-	4433	5545		
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = - 25 V, f = 1 MHz	-	301	380	pF	
Reverse Transfer Capacitance	C _{rss}			-	208	260		
Total Gate Charge ^c	Qg			-	96	144		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 10 V	$V_{DS} = -50V, I_{D} = -9.2 A$	-	8.4	-	nC	
Gate-Drain Charge ^c	Q _{gd}	1		-	23.5	-	1	
Gate Resistance	R _g		f = 1 MHz	1.5	3.13	4.7	Ω	
Turn-On Delay Time ^c	t _{d(on)}			1	11	17		
Rise Time ^c	t _r	V _{DD} =	$V_{DD} = -50 \text{ V}, R_1 = 6.49 \Omega$		11	17	- ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong -7.7 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1.0 \Omega$		-	78	117		
Fall Time ^c	t _f			-	15	23		
Source-Drain Diode Ratings and Char	acteristics ^b	•						
Pulsed Current ^a	I _{SM}			-	-	- 150	Α	
Forward Voltage	V _{SD}	I _E = - 7.7 A, V _{GS} = 0 V		-	- 0.8	- 1.5	V	

Notes

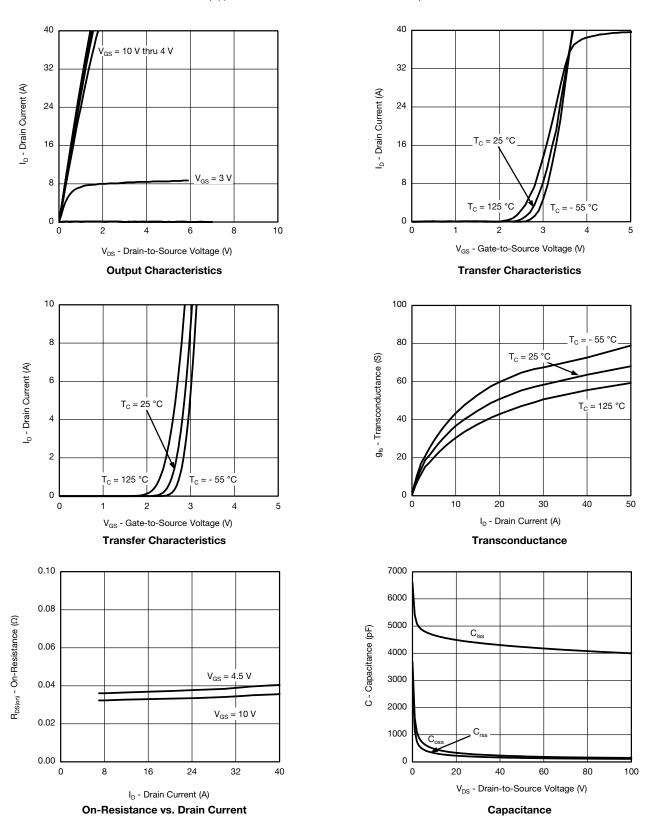
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- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

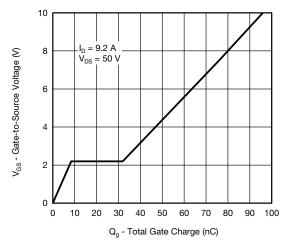


TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

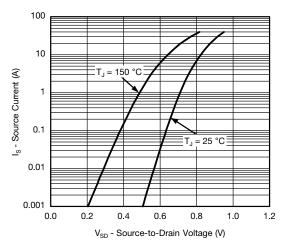




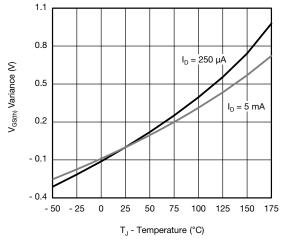
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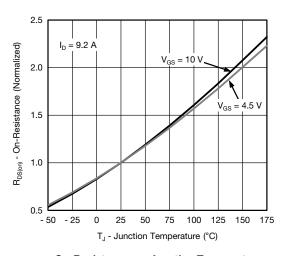
Gate Charge



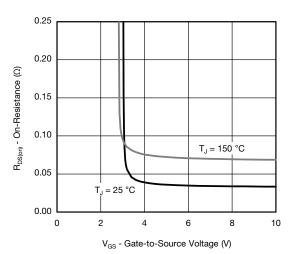
Source Drain Diode Forward Voltage



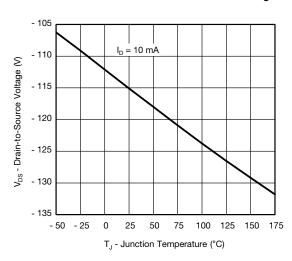
Threshold Voltage



On-Resistance vs. Junction Temperature



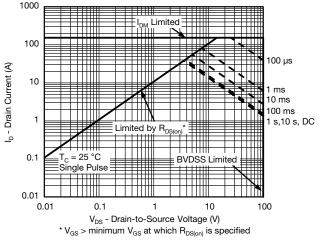
On-Resistance vs. Gate-to-Source Voltage



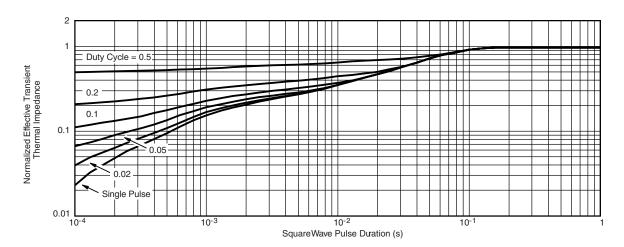
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



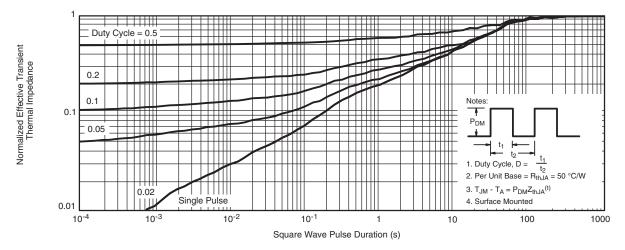
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



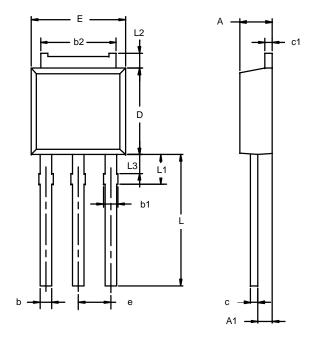
Normalized Thermal Transient Impedance, Junction-to-Ambient

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-251AA



Note: Dimension L3 is for reference only.

MILLIMETERS		INC	HES	
Min	Max	Min	Max	
2.21	2.38	0.087	0.094	
0.89	1.14	0.035	0.045	
0.71	0.89	0.028	0.035	
0.76	1.14	0.030	0.045	
5.23	5.43	0.206	0.214	
0.46	0.58	0.018	0.023	
0.46	0.58	0.018	0.023	
5.97	6.22	0.235	0.245	
6.48	6.73	0.255	0.265	
2.28 BSC		0.090	BSC	
3.89	9.53	0.153	0.375	
1.91	2.28	0.075	0.090	
0.89	1.27	0.035	0.050	
1.15	1.52	0.045	0.060	
	Min 2.21 0.89 0.71 0.76 5.23 0.46 0.46 5.97 6.48 2.28 3.89 1.91 0.89	Min Max 2.21 2.38 0.89 1.14 0.71 0.89 0.76 1.14 5.23 5.43 0.46 0.58 0.46 0.58 5.97 6.22 6.48 6.73 2.28 BSC 3.89 9.53 1.91 2.28 0.89 1.27	Min Max Min 2.21 2.38 0.087 0.89 1.14 0.035 0.71 0.89 0.028 0.76 1.14 0.030 5.23 5.43 0.206 0.46 0.58 0.018 0.46 0.58 0.018 5.97 6.22 0.235 6.48 6.73 0.255 2.28 BSC 0.090 3.89 9.53 0.153 1.91 2.28 0.075 0.89 1.27 0.035	

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