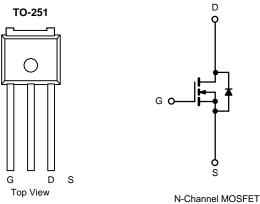


### FU3708-VB Datasheet

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Тур)			
30	0.0035 at V <sub>GS</sub> = 10 V	100	95nC			
	0.0045 at V <sub>GS</sub> = 4.5 V	97	30110			



#### **FEATURES**

- Trench Power MOSFET
- ٠ 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	V		
Gate-Source Voltage		V <sub>GS</sub>		± 20	
	T <sub>C</sub> = 25 °C		100 <sup>a, e</sup>	A	
Continuous Drain Current (T 175 °C)	T <sub>C</sub> = 70 °C		95 <sup>e</sup>		
Continuous Drain Current ( $T_J = 175 \ ^\circ C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	35 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		26 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	197	-	
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	39		
Single Pulse Avalanche Energy	L = 0.1 MH	E <sub>AS</sub>	94.8	mJ	
Outilities Outilities Divide Output	T <sub>C</sub> = 25 °C	la la	90 <sup>a, e</sup>	^	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>	— A	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>	w	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P	175		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.75 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stq</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	0/10	

Notes:

a. Based on  $T_C = 25 \text{ °C}$ . b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 sec. d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

<b>SPECIFICATIONS</b> ( $T_J = 25 \ ^{\circ}C$ ,	unless othe	rwise noted)					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		35		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$\overline{T_J}$ ID = 250 $\mu$ A		- 7.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtara Drain Current	1	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	90			А	
	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 38.8 A		0.0035			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 37 A		0.0045		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 38.8 A		160		S	
Dynamic <sup>b</sup>			<u> </u>	1	<u> </u>		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		3000		pF	
Output Capacitance	C <sub>oss</sub>			710			
Reverse Transfer Capacitance	C <sub>rss</sub>			170		1	
		$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 38.8 A		171	257		
Total Gate Charge	Q <sub>g</sub>			81.5	123		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 28.8 \text{ A}$		34		nC	
Gate-Drain Charge	Q <sub>gd</sub>			29			
Gate Resistance	Rg	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			18	27		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 24 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		70	105		
Fall Time	t <sub>f</sub>			10	15		
Turn-On Delay Time	t <sub>d(on)</sub>			55	83	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180	270		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\rm I_D{\cong}22.5$ A, $\rm V_{GEN}$ = 4.5 V, $\rm R_g$ = 1 $\Omega$		55	83		
Fall Time	t <sub>f</sub>			12	18	1	
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			120	^	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				120	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			70.2	105	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		27			
	+			0.5		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

Reverse Recovery Rise Time

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

t<sub>b</sub>

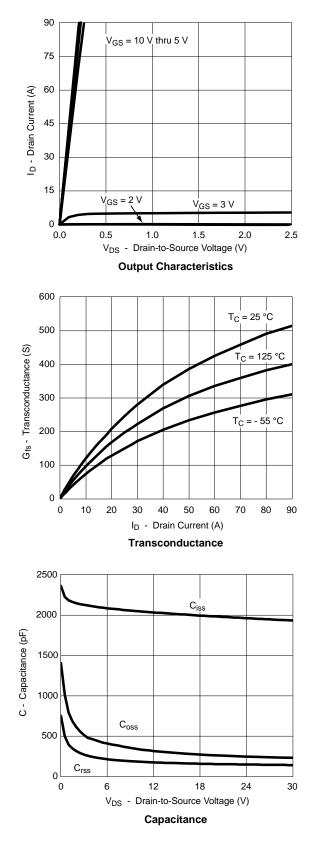
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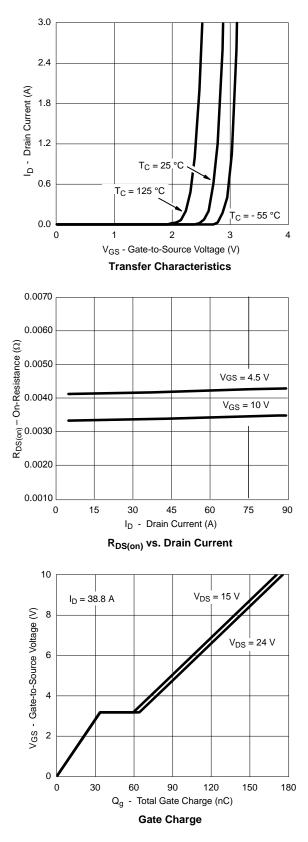
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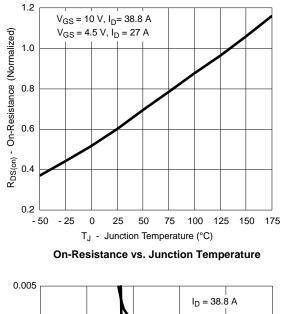
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

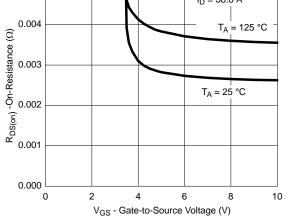


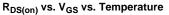


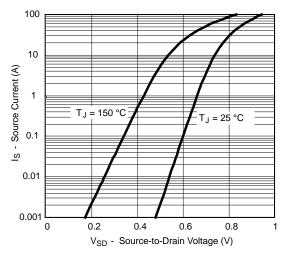


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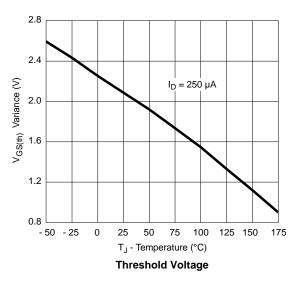


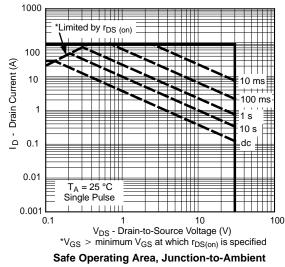




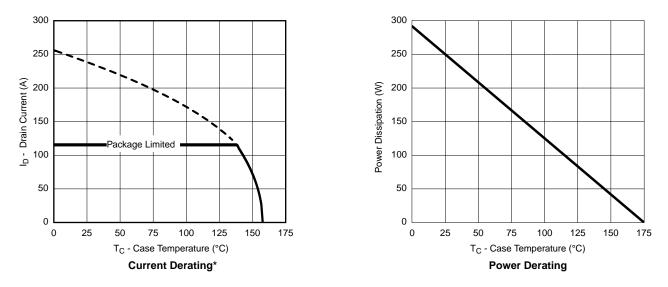


Forward Diode Voltage vs. Temperature









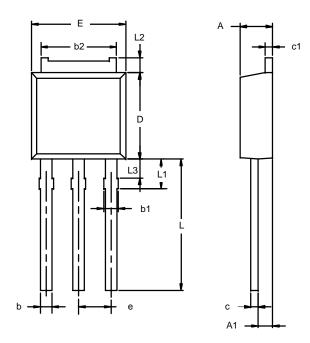
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

\*The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





#### TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

Min	Max	Min	Max	
2.21	2.38	0.087	0.094	
0.89	1.14	0.035	0.045	
0.71	0.89	0.028	0.035	
0.76	1.14	0.030	0.045	
5.23	5.43	0.206	0.214	
0.46	0.58	0.018	0.023	
0.46	0.58	0.018	0.023	
5.97	6.22	0.235	0.245	
6.48	6.73	0.255	0.265	
2.28 BSC		0.090 BSC		
8.89	9.53	0.350	0.375	
1.91	2.28	0.075	0.090	
0.89	1.27	0.035	0.050	
1.15	1.52	0.045	0.060	
	0.89 0.71 0.76 5.23 0.46 0.46 5.97 6.48 2.28 8.89 1.91 0.89 1.15	1.14   0.89 1.14   0.71 0.89   0.76 1.14   5.23 5.43   0.46 0.58   0.46 0.58   5.97 6.22   6.48 6.73   2.28 BSC 8.89   9.53 1.91   2.28 1.27   1.14 1.52	D.11 D.13 D.14 D.035   0.89 1.14 0.035 0.028   0.71 0.89 0.028 0.028   0.76 1.14 0.030 0.523   5.23 5.43 0.206 0.46   0.46 0.58 0.018   5.97 6.22 0.235   6.48 6.73 0.255   2.28 BSC 0.090   8.89 9.53 0.350   1.91 2.28 0.075   0.89 1.27 0.035	



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