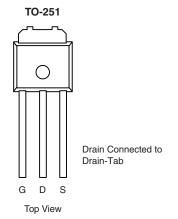


### FU18N15D-VB Datasheet

## N-Channel 200V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
200	0.056 at V <sub>GS</sub> = 10 V	25		
	0.070 at V <sub>GS</sub> = 6 V	23		



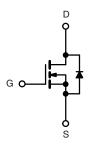
#### **FEATURES**

- Trench Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

· Primary Side Switch



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		$V_{DS}$	200	V		
Gate-Source Voltage		$V_{GS}$	± 20	V		
Continuous Dusin Comment (T. 177 °C)b	T <sub>C</sub> = 25 °C	I.	25			
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 125 °C	l <sub>D</sub>	17	1		
Pulsed Drain Current		I <sub>DM</sub>	60	Α		
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	19				
Avalanche Current	I <sub>AS</sub>	25				
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	18	mJ		
Maximum Power Dissination	T <sub>C</sub> = 25 °C	P <sub>D</sub>	145 <sup>b</sup>	W		
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	' D	3.5 <sup>a</sup>	]		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Location to Australia	t ≤ 10 s	D	15	18	°C/W	
Junction-to-Ambient <sup>a</sup>	Steady State	R <sub>thJA</sub>	40	50		
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.85	1.1		

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See SOA curve for voltage derating.



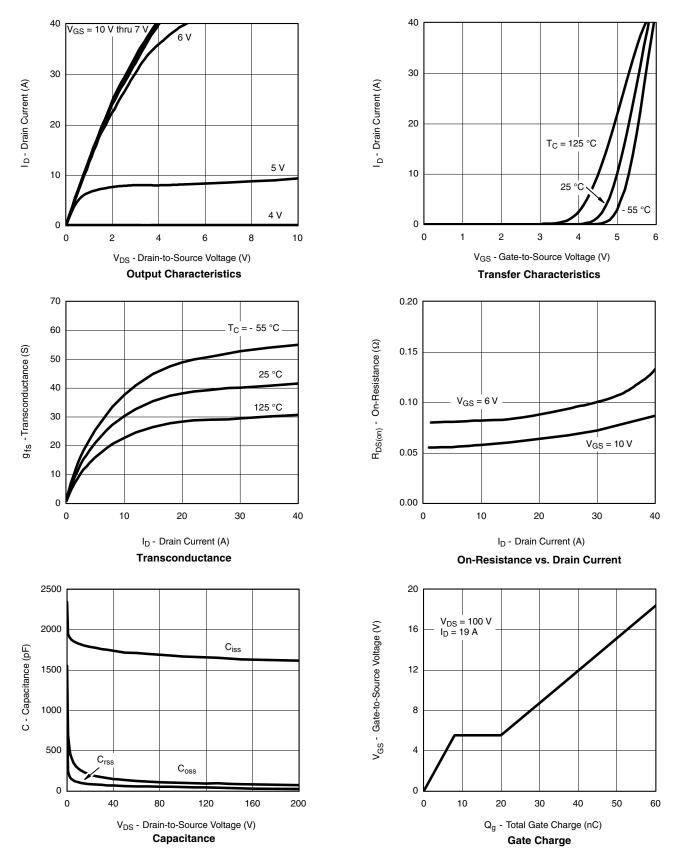
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>DS</sub> V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA 200				V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	50 μΑ	
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
		$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		0.056			
Durin Course Or Olate Besistered	<b>R</b>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C		0.130		0	
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C		0.260		Ω	
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 5 A		0.070			
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 19 A		35		S	
Dynamic <sup>a</sup>							
Input Capacitance	C <sub>iss</sub>			2400			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		280		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			180			
Total Gate Charge <sup>c</sup>	$Q_g$			40			
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 19 \text{ A}$		10		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			15			
Gate Resistance	$R_g$		0.5		2.9	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_L = 5.2 \Omega$		50	75		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 19 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>			60	90		
Source-Drain Diode Ratings and Char	acteristics (7	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>				50	Α	
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>F</sub> = 19 A, V <sub>GS</sub> = 0 V		0.9	1.5	٧	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 19 A, dl/dt = 100 A/μs		180	250	ns	

#### Notes:

- a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. c. Independent of operating temperature.

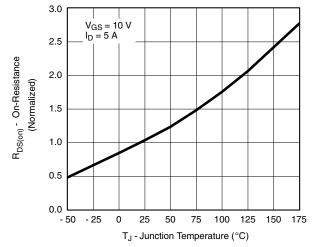


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

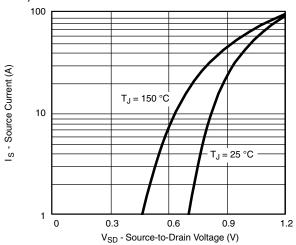




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

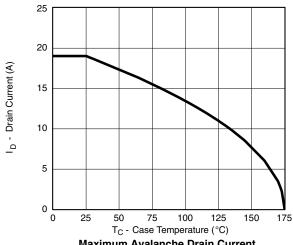




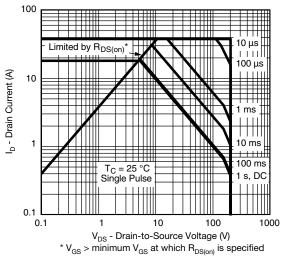


Source-Drain Diode Forward Voltage

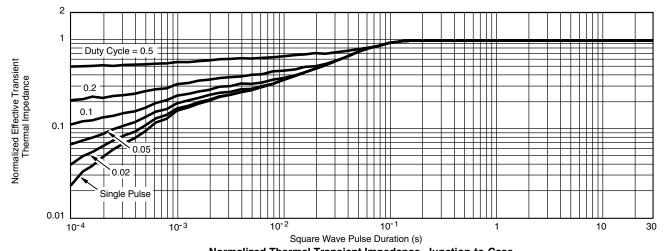
#### THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



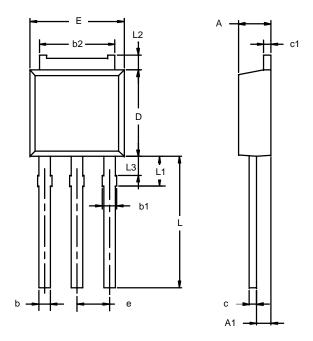
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case



#### **TO-251AA**



Note: Dimension L3 is for reference only.
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	MILLIMETERS		INCHES		
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
с1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.48	6.73	0.255	0.265	
е	2.28	2.28 BSC		BSC	
L	3.89	9.53	0.153	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	

服务热线:400-655-8788

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