

# AULU3110Z-VB Datasheet N-Channel 100-V (D-S), 175 °C MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)	
100	0.0125 at V <sub>GS</sub> = 10 V	65	48 nC	

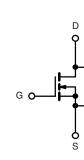
#### **FEATURES**

- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested



#### **APPLICATIONS**

- · Primary Side Switch
- Isolated DC/DC Converter



N-Channel MOSFET

T	0-25	1	
	0		
			Drain Connected to Drain-Tab
G	D	S	
To	p Vie	w	

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> $T_A = 25  ^{\circ}C$ , unles	s otherwise no	oted		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		$V_{DS}$	100	V	
Gate-Source Voltage		$V_{GS}$	± 20	v	
	T <sub>C</sub> = 25 °C		65 <sup>a</sup>		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 100 °C	ı	52		
Continuous Drain Current (1) = 150 C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	8.2 <sup>b</sup>	1	
	T <sub>A</sub> = 100 °C	1	5.8 <sup>b</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	200	7 ^	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	- I <sub>S</sub>	65 <sup>a</sup>		
	T <sub>A</sub> = 25 °C		2.0 <sup>b</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	48		
Avalanche Energy	L = 0.1 11111	E <sub>AS</sub>	121	mJ	
	T <sub>C</sub> = 25 °C		156.4		
Maximum Power Dissipation	T <sub>C</sub> = 100 °C	ь	78.2	w	
	T <sub>A</sub> = 25 °C	$P_{D}$	3.0 <sup>b</sup>	7	
	T <sub>A</sub> = 100 °C		1.5 <sup>b</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	$R_{thJA}$	40	50	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.85	1.1	C/VV

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.



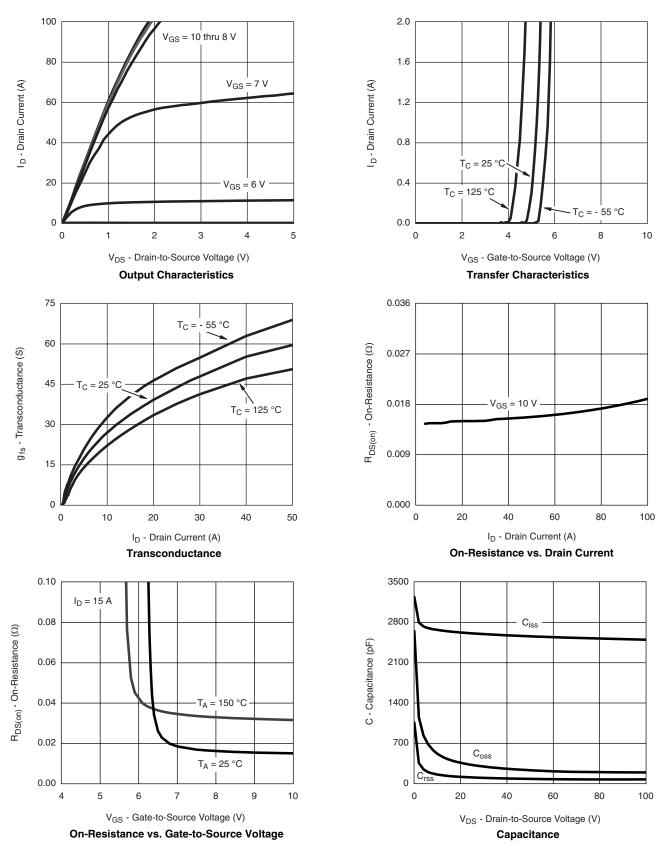
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_D = 250  \mu\text{A}$	100			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			110		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 12.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	l	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μΑ	
Zeio Gate voltage Diaili Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$		0.0125		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A		33		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			2800		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		260			
Reverse Transfer Capacitance	C <sub>rss</sub>			100			
Total Gate Charge	Qg			48	75	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		16			
Gate-Drain Charge	Q <sub>gd</sub>			13			
Gate Resistance	$R_g$	f = 1 MHz		1.6	2.5	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			12	20		
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 1.0 \Omega$		10	20	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		18	35		
Fall Time	t <sub>f</sub>			8	15		
<b>Drain-Source Body Diode Characteris</b>	tics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			50	^	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				100	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 15 A		0.85	1.5	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			80	120	ns	
Body Diode Reverse Recovery Charge Q.,		L = 50 A dl/dt = 100 A/up T = 05 °C		160	240	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 50 \text{ A}, \text{ dI/dt} = 100 \text{ A/µs}, T_J = 25 ^{\circ}\text{C}$		57		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			23		ns	

#### Notes:

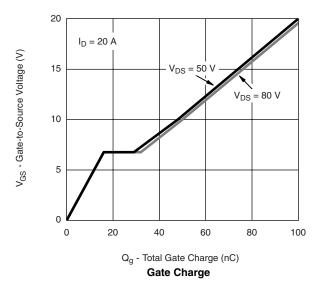
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

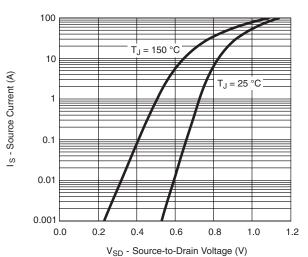
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

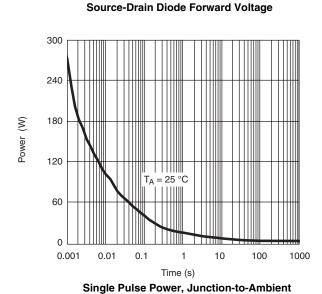


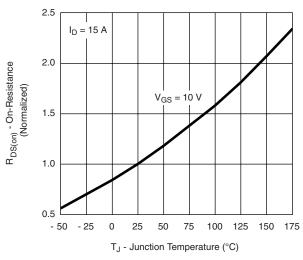




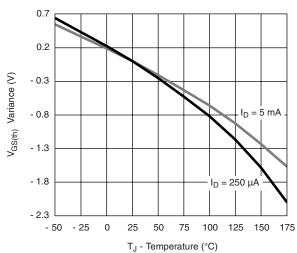




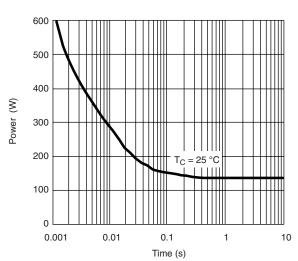






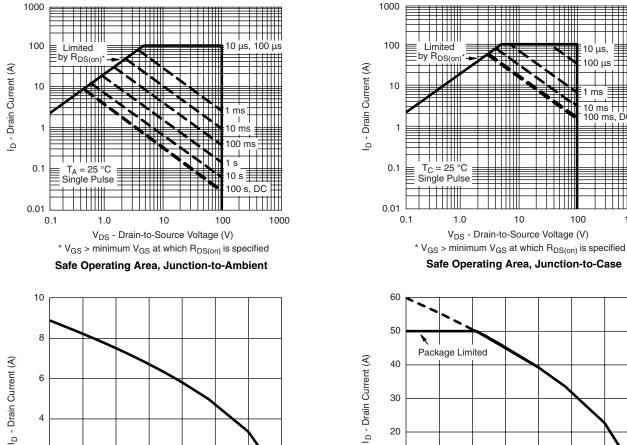


Threshold Voltage



Single Pulse Power, Junction-to-Case





T<sub>A</sub> - Ambient Temperature (°C) Current Derating\*\*, Junction-to-Ambient

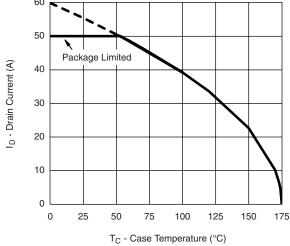
100

125

150

175

75



Current Derating\*\*, Junction-to-Case

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4

2

0 0

25

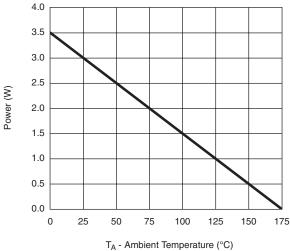
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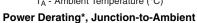
<sup>\*\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

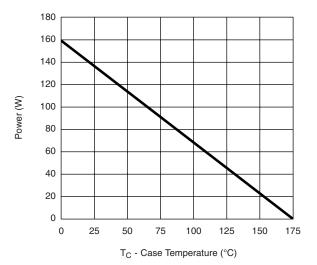
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



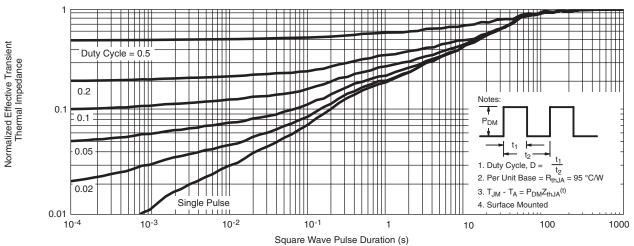




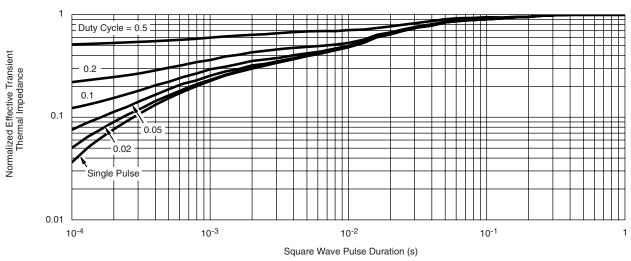
Power Derating\*, Junction-to-Case

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



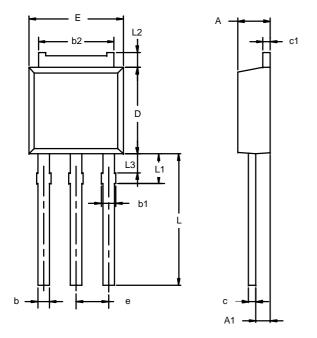
Normalized Thermal Transient Impedance, Junction-to-Case

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### TO-251AA (DPAK)



Note:	Dimension	L3 is for	reference	only.

	MILLIMETERS		INC	HES
Dim	Min	Max	Min	Max
Α	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
С	0.46	0.58	0.018	0.023
с1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
Е	6.48	6.73	0.255	0.265
е	2.28	BSC	0.090	BSC
L	3.89	9.53	0.153	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060



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