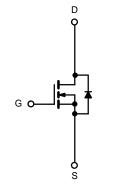


99T03GJ-VB Datasheet

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ)			
30	0.0023 at V _{GS} = 10 V	120	82 nC			
30	0.0032 at V _{GS} = 4.5 V	100	02 110			

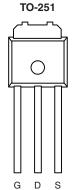


FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2011/65/EU

APPLICATIONS

- OR-ing
- Server
- DC/DC



Top View

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted) Symbol Parameter Limit Unit V_{DS} Drain-Source Voltage 30 V V_{GS} Gate-Source Voltage ± 20 T_C = 25 °C 120^{a, e} T_C = 70 °C 98^e Continuous Drain Current (T_J = 175 °C) I_D T_A = 25 °C 35.8^{b, c} А T_A = 70 °C 27^{b, c} Pulsed Drain Current I_{DM} 400 Avalanche Current Pulse I_{AS} 39 L = 0.1 mHSingle Pulse Avalanche Energy E_{AS} 94.8 mJ T_C = 25 °C 90^{a, e} Continuous Source-Drain Diode Current I_S А $T_A = 25 \degree C$ 3.13^{b, c} T_C = 25 °C 250^a T_C = 70 °C 175 Maximum Power Dissipation P_D W T_A = 25 °C 3.75^{b, c} T_A = 70 °C 2.63^{b, c} Operating Junction and Storage Temperature Range T_J, T_{stg} - 55 to 175 °C

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Тур.	Max.	Unit		
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 \text{ sec}$	R _{thJA}	32	40	°C/W		
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6			

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

Drain Connected to Drain-Tab

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$				V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 250 4		35		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 7.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.5		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA
	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α
		V _{GS} = 10 V, I _D = 38.8 A	0.0023			0
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 37 \text{ A}$				Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 38.8 A		160		S
Dynamic ^b						
Input Capacitance	C _{iss}			6201		pF
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		1725		
Reverse Transfer Capacitance	C _{rss}			970		
Tatal Cata Channa	0	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 38.8 A		171	257	
Total Gate Charge	Qg			81.5	123	
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 28.8 A		34		- nC
Gate-Drain Charge	Q _{gd}			29		
Gate Resistance	R _g	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}			18	27	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		11	17	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 24 A, V_{GEN} = 10 V, R_g = 1 Ω		70	105	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			55	83	ns
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.67 Ω		180	270	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ 22.5 A, V_GEN = 4.5 V, R_g = 1 Ω		55	83	
Fall Time	t _f			12	18	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			120	٨
Pulse Diode Forward Current ^a	I _{SM}				120	A
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns
Body Diode Reverse Recovery Charge	Q _{rr}	L = 20.4 di/dt = 100.4/ma = 00.80		70.2	105	nC
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		27		
Reverse Recovery Rise Time	t _b			25		ns

Notes:

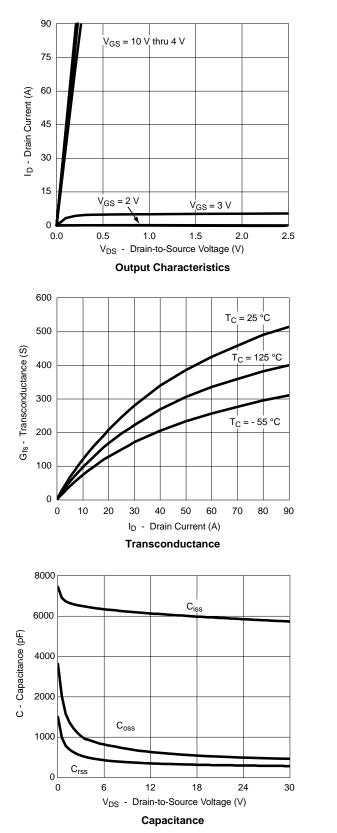
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle ≤ 2 %.

b. Guaranteed by design, not subject to production testing.

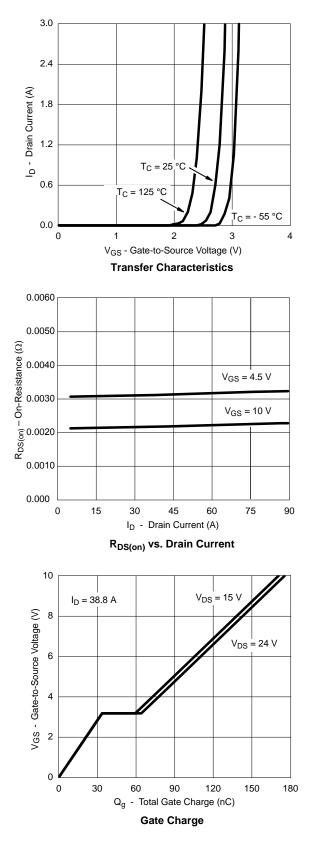
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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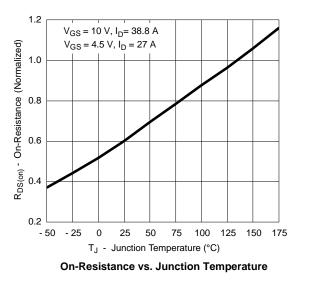




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

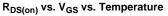


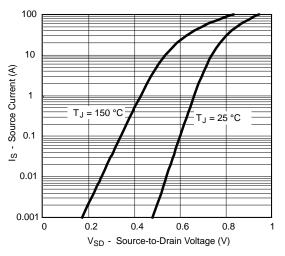




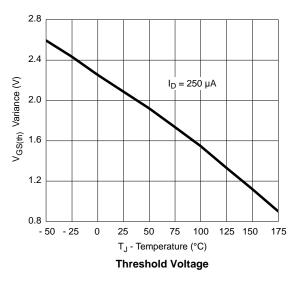
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

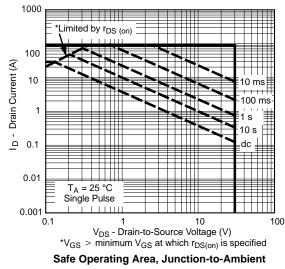
$\begin{array}{c} 0.005 \\ 0.004 \\ \hline \\ 0.004 \\ \hline \\ 0.003 \\ \hline \\ 0.001 \\ \hline \\ 0.000 \\ \hline \\ 0.0$



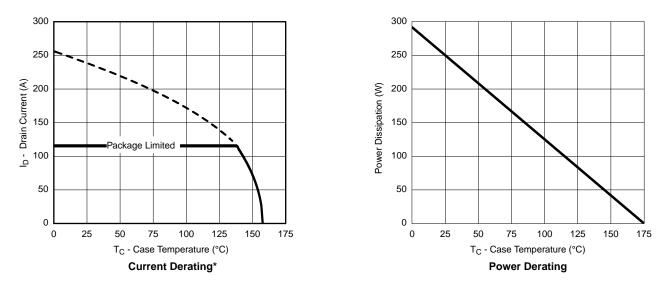


Forward Diode Voltage vs. Temperature









TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

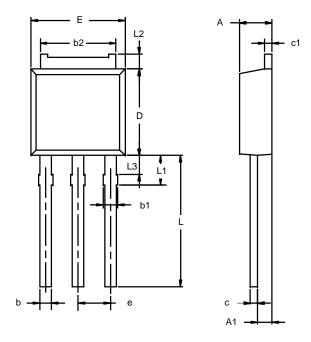
*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



99T03GJ-VB



TO-251AA



	MILLIM	IETERS	INC	INCHES		
Dim	Min	Max	Min	Max		
Α	2.21	2.38	0.087	0.094		
A1	0.89	1.14	0.035	0.045		
b	0.71	0.89	0.028	0.035		
b1	0.76	1.14	0.030	0.045		
b2	5.23	5.43	0.206	0.214		
С	0.46	0.58	0.018	0.023		
c1	0.46	0.58	0.018	0.023		
D	5.97	6.22	0.235	0.245		
Е	6.48	6.73	0.255	0.265		
е	2.28 BSC		0.090 BSC			
L	3.89	9.53	0.153	0.375		
L1	1.91	2.28	0.075	0.090		
L2	0.89	1.27	0.035	0.050		
L3	1.15	1.52	0.045	0.060		

Note: Dimension L3 is for reference only.



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