

2SJ319L-VB Datasheet

P-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A) ^a	Q _g (Typ.)			
- 200	1.000 at V _{GS} = - 10 V	- 5	76 nC			
	1.200 at V _{GS} = - 4.5 V	- 4.8	70110			

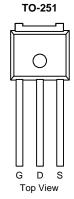
FEATURES

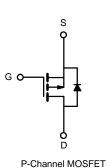
- Trench Power MOSFET
- 100 % UIS Tested

APPLICATIONS

Load Switch







Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 200	V		
Gate-Source Voltage		V _{GS}	± 20	1 V	
	T _C = 25 °C		- 5 ^a		
Continuous Drain Current (T. – 150 °C)	T _C = 70 °C		- 4.8		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	-5 ^b	,	
	T _A = 70 °C		- 4.7 ^b	A	
Pulsed Drain Current	I _{DM}	- 30			
valanche Current Pulse L = 0.1 mH		I _{AS}	- 35		
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	101	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C	1	29 ^a	^	
Continuous Source-Drain Diode Current	T _A = 25 °C	ls –	2.1 ^b	A	
	T _C = 25 °C		104.2 ^a		
Manianum Danian Dinain ation	T _C = 70 °C	D.	66.7 ^a	100	
Maximum Power Dissipation	T _A = 25 °C	P _D	3.1 ^b	W	
	T _A = 70 °C		2 ^b		
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	Steady State	R_{thJA}	33	40	°C // //	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.98	1.2	°C/W	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•		
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 200			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J I _D = - 250 μA		68		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1D = - 250 μΛ		- 5.2		- mv/*	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1.7		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Brain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V			- 1	μА	
Zero Gate Voltage Drain Current		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 55 °C	- 10		- 10		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 120			Α	
	В	V _{GS} = - 10 V, I _D = - 30 A		1.000			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 20 A		1.200		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 50 A	20			S	
Dynamic ^b					•	•	
Input Capacitance	C _{iss}			3500		pF	
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		390			
Reverse Transfer Capacitance	C _{rss}			290			
Total Cata Charge	Qg	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -55 \text{ A}$		76	115		
Total Gate Charge				38	60	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -55 \text{ A}$		16			
Gate-Drain Charge	Q_{gd}			19			
Gate Resistance	R_g	f = 1 MHz		5.2		Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	$V_{DD} = -2 V$, $R_L = 2 \Omega$		7	15	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		70	110		
Fall Time	t _f			40	60		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 66	Α	
Pulse Diode Forward Current ^a	I _{SM}				- 150	^	
Body Diode Voltage	V _{SD}	I _S = - 30 A		- 1	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			45	68	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 50 A, di/dt = 100 A/μs, T _J = 25 °C		59	120	nC	
Reverse Recovery Fall Time	t _a	$I_F = -50 \text{ A, al/at} = 100 \text{ A/µs, } I_J = 25 \text{ C}$		29			
Reverse Recovery Rise Time	t _b			16		ns	

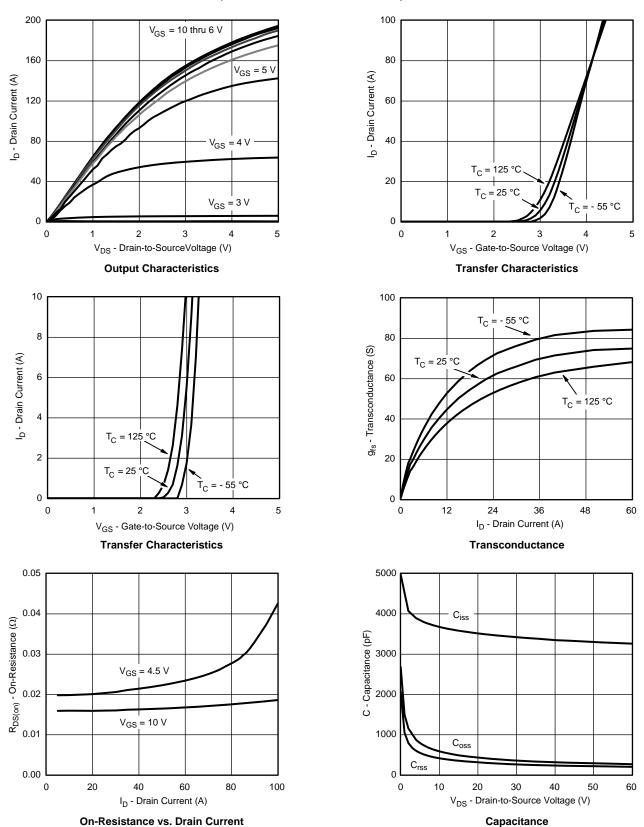
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

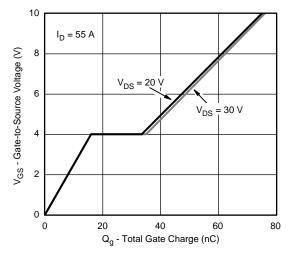


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

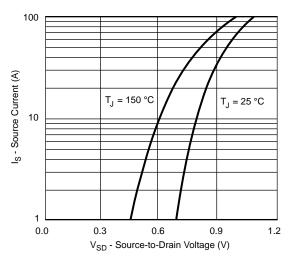




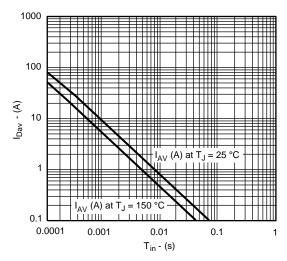
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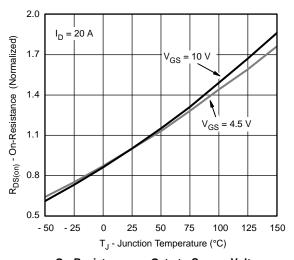




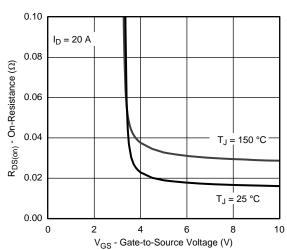
Source-Drain Diode Forward Voltage



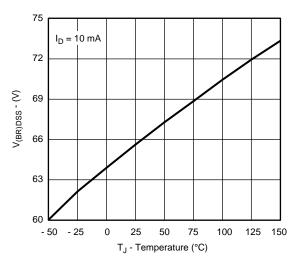
Single Pulse Avalanche Current Capability vs. Time



On-Resistance vs. Gate-to-Source Voltage



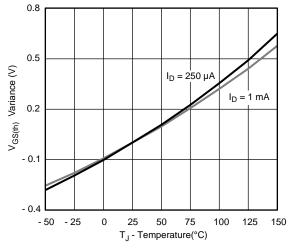
On-Resistance vs. Gate-to-Source Voltage

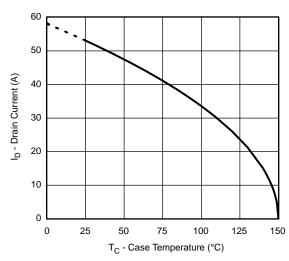


Drain-Source Breakdown Voltage vs. Junction Temperature

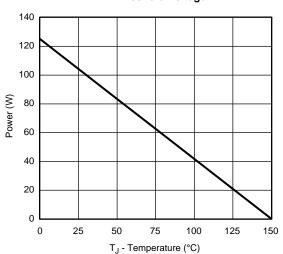


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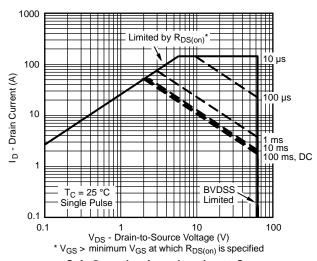




Threshold Voltage

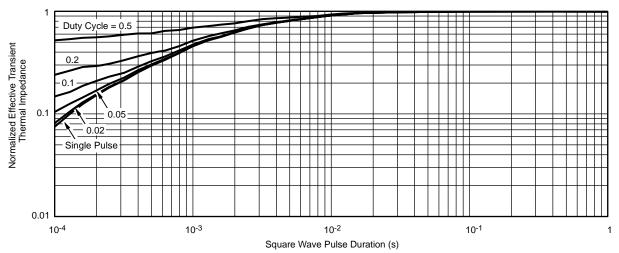


Max. Drain Current vs. Case Temperature



Power Derating, Junction-to-Case

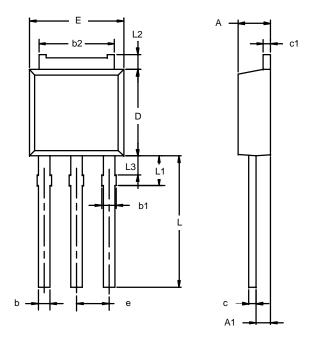




Normalized Thermal Transient Impedance, Junction-to-Case



TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIM	IETERS	INCHES		
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
c1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.48	6.73	0.255	0.265	
е	2.28 BSC		0.090 BSC		
L	8.89	9.53	0.350	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	
ECN: S-03946—Rev. E, 09-Jul-01					

DWG: 5346



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