

## NCE15H10TA-VB Datasheet N-Channel 150 V (D-S) MOSFET

PRODUCT	SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)
150	0.012 at V <sub>GS</sub> = 10 V	150	113 nC
150	0.014 at Vgs= 7.5 V	135	113110

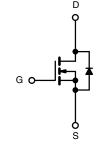
## **FEATURES**

- Thunder power MOSFET
- Maximum 175 °C junction temperature
- 100 % R<sub>g</sub> and UIS tested



#### TO-247





N-Channel MOSFET

## **APPLICATIONS**

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER		SYMBOL	LIMIT	UNIT			
Drain-Source Voltage		$V_{DS}$	150	V			
Gate-Source Voltage	$V_{GS}$	± 20	V				
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		150	Δ.			
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	60				
Pulsed Drain Current (t = 100 μs)		I <sub>DM</sub>	300	Α			
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	60				
Single Avalanche Energy <sup>a</sup>	L=0.11III	E <sub>AS</sub>	180	mJ			
Martin and Branch Biretteeting &	T <sub>C</sub> = 25 °C	D-	375 b	W			
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	- P <sub>D</sub>	125 <sup>b</sup>				
Operating Junction and Storage Temperature	Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C			

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.4	C/W	

#### Notes

- a. Duty cycle ≤ 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	150	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_D=250\;\mu A$	2	-	5	v
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA
		$V_{DS} = 150 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 150 V, $V_{GS}$ = 0 V, $T_J$ = 125 °C	-	-	100	μΑ
		$V_{DS}$ = 150 V, $V_{GS}$ = 0 V, $T_J$ = 175 °C	-	-	2	mA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	50	-	-	Α
Drain-Source On-State Resistance a	П	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	0.012	-	0
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, I_D = 20 \text{ A}$	-	0.014	-	Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A	-	52	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>		-	6200	-	pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 75 V, f = 1 MHz	-	535	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	26	-	
Total Gate Charge <sup>c</sup>	Qg		-	113	-	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	-	19.5	-	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	20.5	-	
Gate Resistance	$R_g$	f = 1 MHz	1.5	3	5	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	15	30	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 75 \text{ V}, R_L = 1.25 \Omega$	-	114	220	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 60 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	28	56	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	8	16	
Drain-Source Body Diode Ratings at	nd Characteris	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)				
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	200	Α
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V	-	0.73	1.2	V
Reverse Recovery Time	t <sub>rr</sub>		-	60	120	ns
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	10	20	Α
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.5	1	μC

### Notes

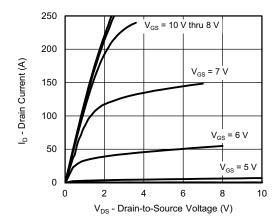
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- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

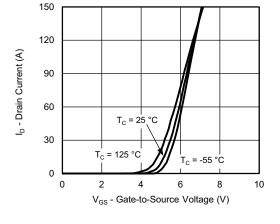
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



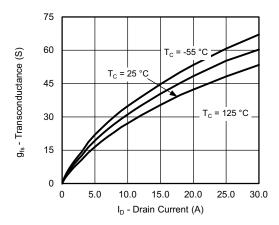
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



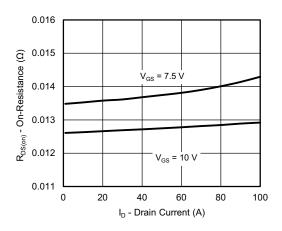
### **Output Characteristics**



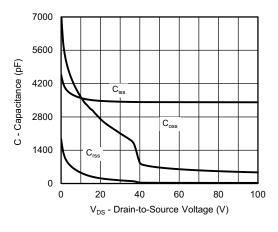
**Transfer Characteristics** 



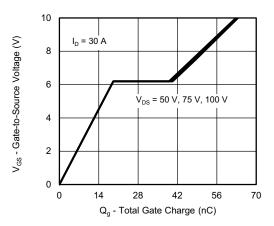
Transconductance



On-Resistance vs. Drain Current



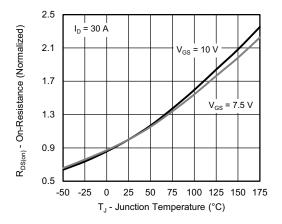
Capacitance



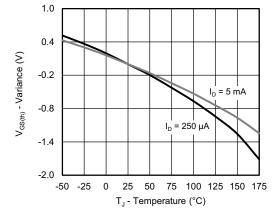
Gate Charge



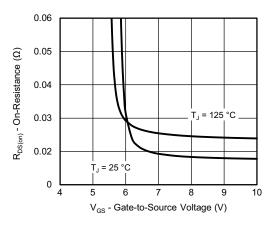
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



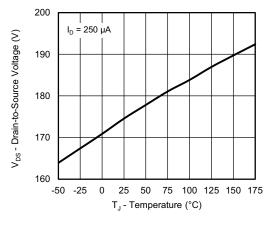
On-Resistance vs. Junction Temperature



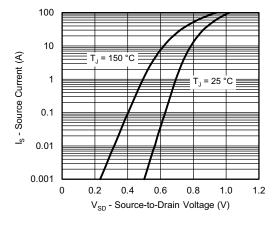
**Threshold Voltage** 



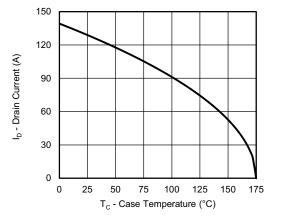
On-Resistance vs. Gate-to-Source Voltage



**Drain Source Breakdown vs. Junction Temperature** 



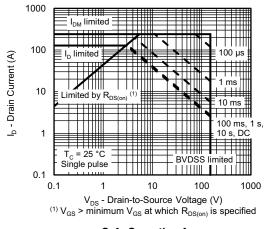
**Source Drain Diode Forward Voltage** 

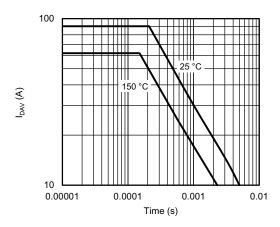


**Current De-Rating** 



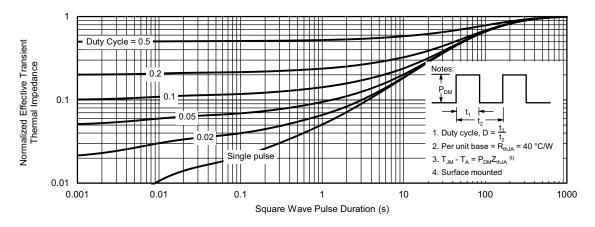
## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)





**Safe Operating Area** 

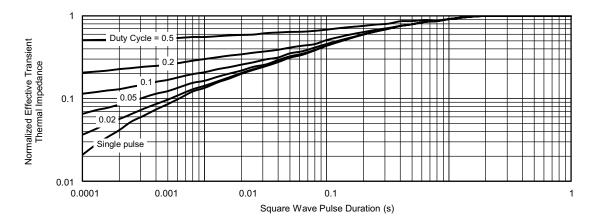
 $I_{\text{DAV}}$  vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient



## THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



### Normalized Thermal Transient Impedance, Junction-to-Case

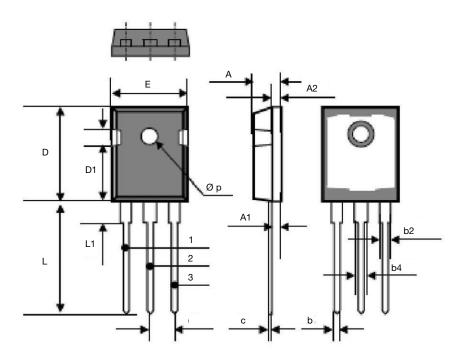
#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



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DIM.	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
Α	4.70	5.31	0.185	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b2	1.65	2.41	0.065	0.095	
b4	2.59	3.43	0.102	0.135	
С	0.61	0.61 BSC		0.024 BSC	
D	20.80	21.46	0.819	0.845	
D1	3.68	5.49	0.145	0.216	
(e)	5.46 BSC		0.215 BSC		
Е	15.49	16.26	0.610	0.640	
L	19.81	20.32	0.780	0.800	
L1	4.06	4.50	0.160	0.177	
Øр	3.51	3.66	0.138	0.144	



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