

# IRFP3710PBF-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>(BR)DSS</sub> (V)	$r_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
100	0.018 at V <sub>GS</sub> = 10 V	72 <sup>a</sup>			

### **FEATURES**

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

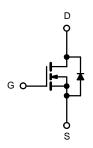


• Isolated DC/DC Converters









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	<b>S</b> T <sub>C</sub> = 25 °C, unless oth	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	100	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	- V		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I-	72 <sup>a</sup>		
	T <sub>C</sub> = 125 °C	l <sub>D</sub>	51 <sup>a</sup>	Ī ,	
Pulsed Drain Current		I <sub>DM</sub>	240	A	
Avalanche Current L = 0.1 mH		I <sub>AS</sub>	31		
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.111111	E <sub>AS</sub>	60	mJ	
Mariana Barra Birainaria h	T <sub>C</sub> = 25 °C	В	355 <sup>c</sup>	- w	
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C <sup>d</sup>	$ P_D$ $-$	3.35		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	C/VV	

#### Notes:

- a. Package limited.
- b. Duty cycle  $\leq$  1 %.
- c. See SOA curve for voltage derating.
- d. When Mounted on 1" square PCB (FR-4 material).



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage V <sub>(BR)D</sub>		$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	100			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.018			
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.023		Ω	
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}, T_J = 175 ^{\circ}\text{C}$		0.037			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 30 \text{ A}$	25			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			3200		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		410			
Reverse Transfer Capacitance	C <sub>rss</sub>			210			
Total Gate Charge <sup>c</sup>	$Q_g$			90	130	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 58 \text{ A}$		23			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			34			
Gate Resistance	R <sub>g</sub>		0.5	1.3	3.1	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			24	35		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_{L} = 1.5 \Omega$		220	330		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 58 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5$		45	70	- ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	Ω		200	300		
Source-Drain Diode Ratings and Ch	aracteristics 7	T <sub>C</sub> = 25 °C <sup>b</sup>					
Continuous Current	I <sub>S</sub>				58	۸	
Pulsed Current	I <sub>SM</sub>				110	A	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = 58 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			130	200	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		8	12	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			0.52	1.2	иC	

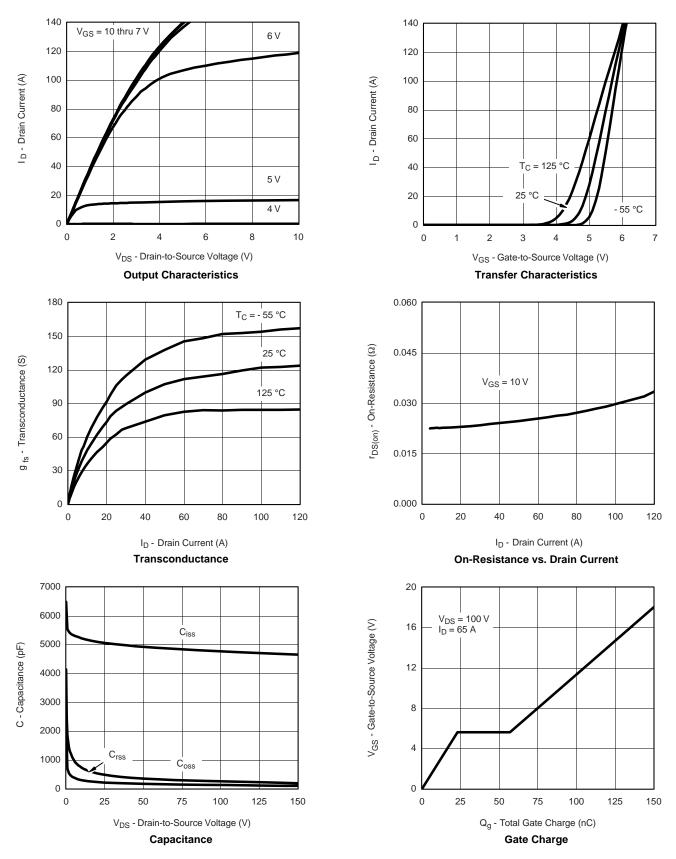
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

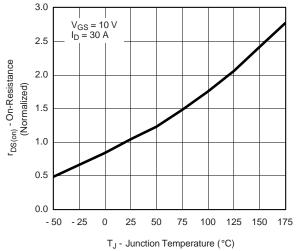


## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

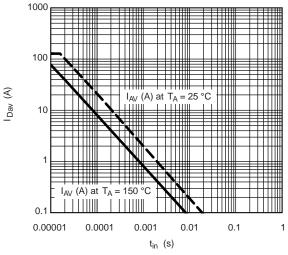




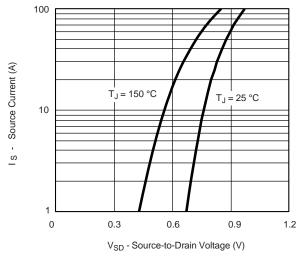
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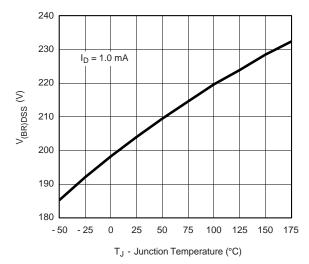
On-Resistance vs. Junction Temperature



**Avalanche Current vs. Time** 



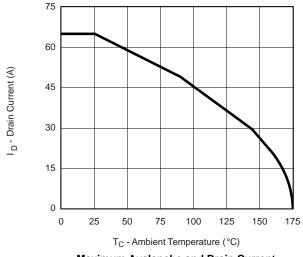
Source-Drain Diode Forward Voltage



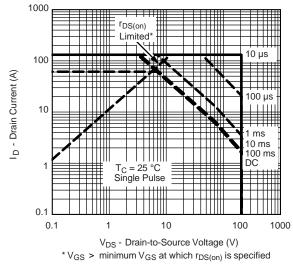
Drain Source Breakdown vs. Junction Temperature



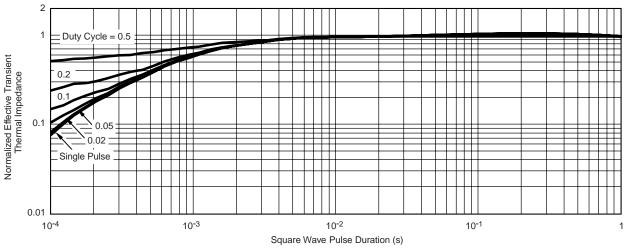
#### THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



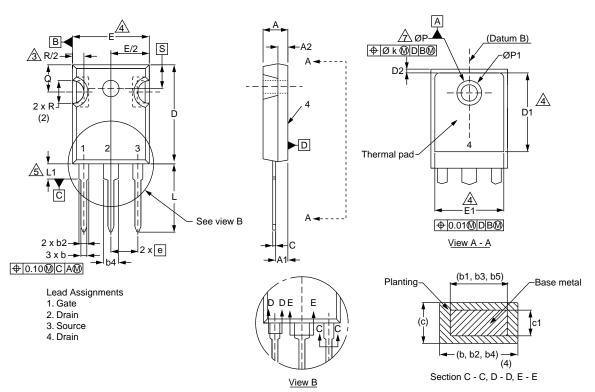
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case



# **TO-247AC**



	MILLIM	IETERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
Α	4.58	5.31	0.180	0.209
A1	2.21	2.59	0.087	0.102
A2	1.17	2.49	0.046	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.53	2.39	0.060	0.094
b3	1.65	2.37	0.065	0.093
b4	2.42	3.43	0.095	0.135
b5	2.59	3.38	0.102	0.133
С	0.38	0.86	0.015	0.034
c1	0.38	0.76	0.015	0.030
D	19.71	20.82	0.776	0.820
D1	13.08	-	0.515	-

	MILLIM	IETERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
D2	0.51	1.30	0.020	0.051
E	15.29	15.87	0.602	0.625
E1	13.72	-	0.540	-
е	5.46	BSC	0.215	BSC
Øk	0.254		0.010	
L	14.20	16.25	0.559	0.640
L1	3.71	4.29	0.146	0.169
N	7.62 BSC		0.300 BSC	
ØΡ	3.51	3.66	0.138	0.144
Ø P1	-	7.39	-	0.291
Q	5.31	5.69	0.209	0.224
R	4.52	5.49	0.178	0.216
S	5.51 BSC		0.217	BSC



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