

KOHS

COMPLIANT

## IRFP140NPBF-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT	UCT SUMMARY			
V <sub>(BR)DSS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
100	0.035 at V <sub>GS</sub> = 10 V	85		

### FEATURES

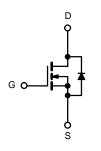
- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

### APPLICATIONS

• Isolated DC/DC Converters



**TO-247AC** 



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	S T <sub>C</sub> = 25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	100	v	
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current ( $T_1 = 175 ^{\circ}C$ )	T <sub>C</sub> = 25 °C	1-	85	
Continuous Drain Current $(1) = 175$ C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	60	A
Pulsed Drain Current		I <sub>DM</sub>	150	A
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	39	
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.1 mm	E <sub>AS</sub>	61	mJ
Maria Diana Diana dia dia	T <sub>C</sub> = 25 °C	Р	375 <sup>c</sup>	14/
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C <sup>d</sup>	- P <sub>D</sub> -	3.75	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE R	RMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	0,10	

Notes:

- a. Package limited.
- b. Duty cycle  $\leq$  1 %.
- c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

<b>SPECIFICATIONS</b> $T_J = 25^{\circ}$	C, unless o	therwise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 V, I_{D} = 250 \mu A$	100			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	v
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 V$ , $V_{GS} = 10 V$	40			А
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.035		
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.063		Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.084		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S
Dynamic <sup>b</sup>	•					
Input Capacitance	C <sub>iss</sub>			5100		
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		480		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			210		
Total Gate Charge <sup>c</sup>	Qg			90		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 10 V, $I_{D}$ = 65 A		23		nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			34		
Gate Resistance	Rg		0.5	1.7	3.3	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			24	35	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, R <sub>L</sub> = 1.5 $\Omega$		220	330	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ 65 A, $\text{V}_{\text{GEN}}$ = 10 V, $\text{R}_{\text{g}}$ = 2.5 $\Omega$		45	70	ns
Fall Time <sup>c</sup>	t <sub>f</sub>			200	300	
Source-Drain Diode Ratings and Cha	aracteristics 7	$\Gamma_{\rm C} = 25 \ {}^{\circ}{\rm C}^{\rm b}$				
Continuous Current	۱ <sub>S</sub>			85		^
Pulsed Current	I <sub>SM</sub>			150		A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 65 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			130	200	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, di/dt = 100 A/µs		8	12	А
Reverse Recovery Charge	Q <sub>rr</sub>			0.52	1.2	μC

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

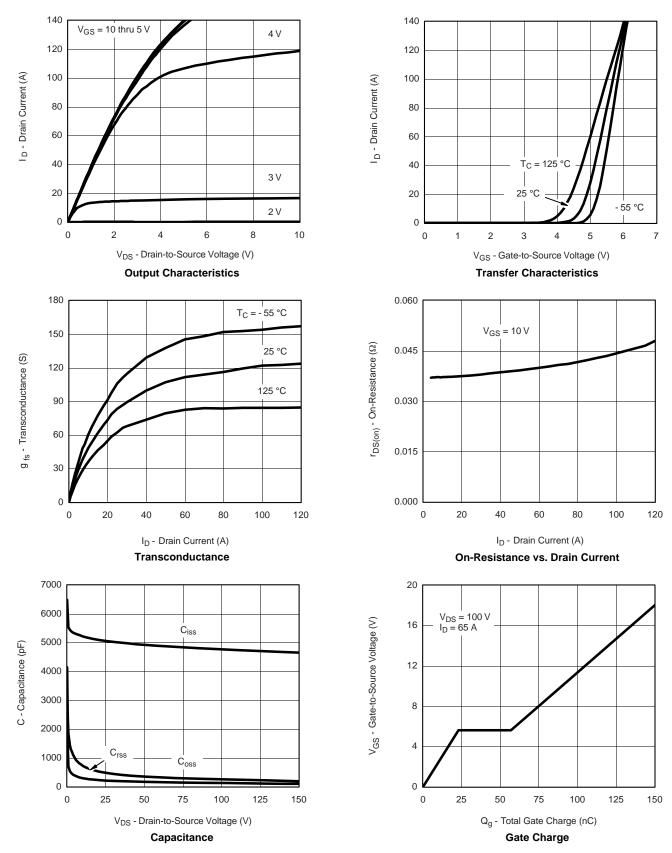
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi

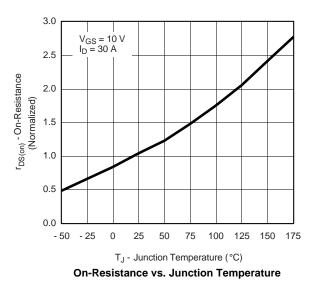


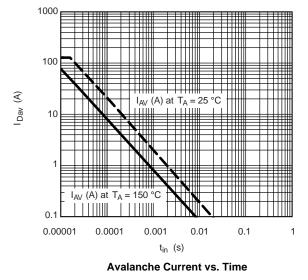


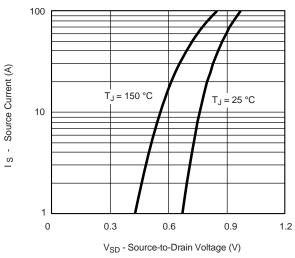




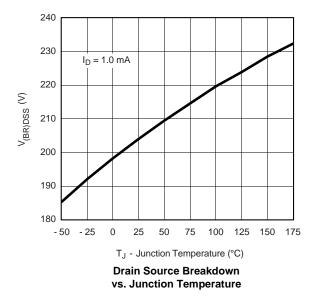
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







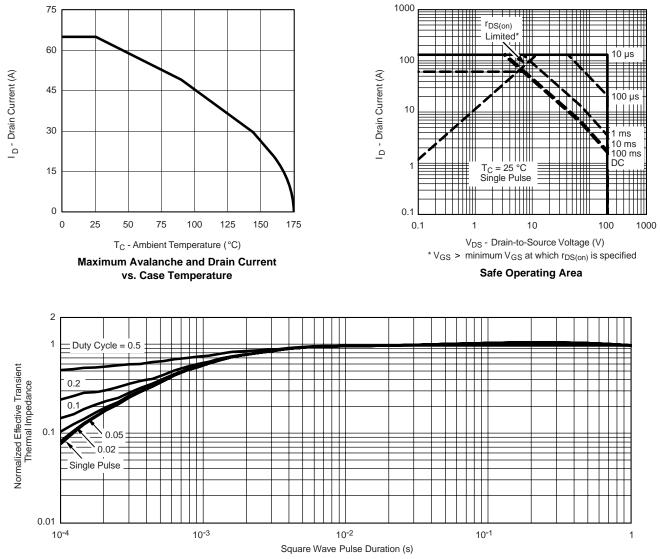
Source-Drain Diode Forward Voltage



### **IRFP140NPBF-VB**

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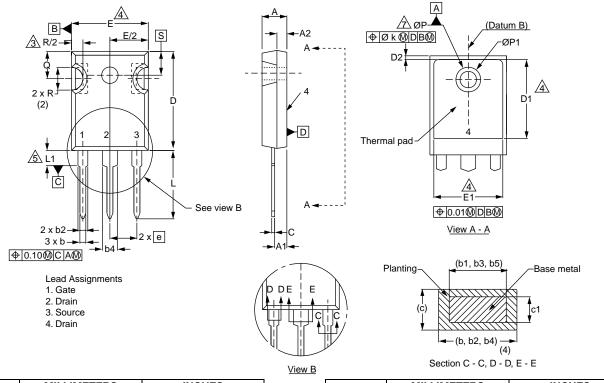
#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



**TO-247AC** 



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.58	5.31	0.180	0.209
A1	2.21	2.59	0.087	0.102
A2	1.17	2.49	0.046	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.53	2.39	0.060	0.094
b3	1.65	2.37	0.065	0.093
b4	2.42	3.43	0.095	0.135
b5	2.59	3.38	0.102	0.133
С	0.38	0.86	0.015	0.034
c1	0.38	0.76	0.015	0.030
D	19.71	20.82	0.776	0.820
D1	13.08	-	0.515	-

	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46	5.46 BSC 0.215 BS			
Øk	0.2	254	0.0	010	
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	7.62 BSC		0.300 BSC	
ØР	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC 0.217 BSC		7 BSC		



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