

FP4710-VB Datasheet

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)		
100	0.018 at V _{GS} = 10 V	72 ^a		

FEATURES

- Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g Tested

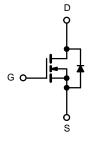
APPLICATIONS

• Isolated DC/DC Converters





TO-247AC



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current ($T_1 = 175 \text{ °C}$)	T _C = 25 °C	1-	72 ^a	
Continuous Drain Current $(1) = 175$ C)	T _C = 125 °C	I _D	51 ^a	A
Pulsed Drain Current		I _{DM}	240	A
Avalanche Current	L = 0.1 mH	I _{AS}	31	
Single Pulse Avalanche Energy ^b			60	mJ
Maximum Drawn Diasia ati sh	T _C = 25 °C	Р	355 ^c	14/
Maximum Power Dissipation ^b	T _A = 25 °C ^d	– P _D –	3.35	- W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	PCB Mount	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)		R _{thJC}	0.4	C/VV		

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS T _J = 25 °C, unless otherwise noted Parameter Symbol Test Conditions Min. Typ. Max.						Unit	
Static	Symbol	Test Conditions	WIIII.	тур.	IVIAX.	Unit	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	100				
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	2		+ ± 100	nA	
Gale-Body Leakage	GSS	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 100 \text{ V}$			1		
Zero Gate Voltage Drain Current	la a a	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50		
Zero Gale voltage Drain Current	IDSS	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ C}$ $V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250	μA	
On Chata Drain Course still		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, 1 \text{ J} = 170 \text{ O}$ $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120		230	A	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 3 \text{ V}, V_{GS} = 10 \text{ V}$ $V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	120	0.010		A	
	_	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$ $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}, \text{ T}_{J} = 125 \text{ °C}$		0.018			
Drain-Source On-State Resistance ^a	r _{DS(on)}			0.023		Ω	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}, \text{ T}_{J} = 175 \text{ °C}$		0.037			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	25			S	
Dynamic ^b	-			1			
Input Capacitance	C _{iss}			3200			
Output Capacitance	C _{oss}	$V_{GS} = 0 V$, $V_{DS} = 25 V$, f = 1 MHz		410		pF	
Reverse Transfer Capacitance	C _{rss}			210			
Total Gate Charge ^c	Qg			90	130		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 58 \text{ A}$		23		nC	
Gate-Drain Charge ^c	Q _{gd}			34			
Gate Resistance	Rg		0.5	1.3	3.1	Ω	
Turn-On Delay Time ^c	t _{d(on)}			24	35		
Rise Time ^c	t _r	V_{DD} = 100 V, R _L = 1.5 Ω		220	330		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 58$ A, V_{GEN} = 10 V, R_g = 2.5		45	70	- ns	
Fall Time ^c	t _f	Ω		200	300		
Source-Drain Diode Ratings and Cha	aracteristics T	_C = 25 °C ^b					
Continuous Current	ا _S				58		
Pulsed Current	I _{SM}				110	A	
Forward Voltage ^a	V _{SD}	I _F = 58 A, V _{GS} = 0 V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			130	200	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/µs		8	12	А	
Reverse Recovery Charge	Q _{rr}			0.52	1.2	μC	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

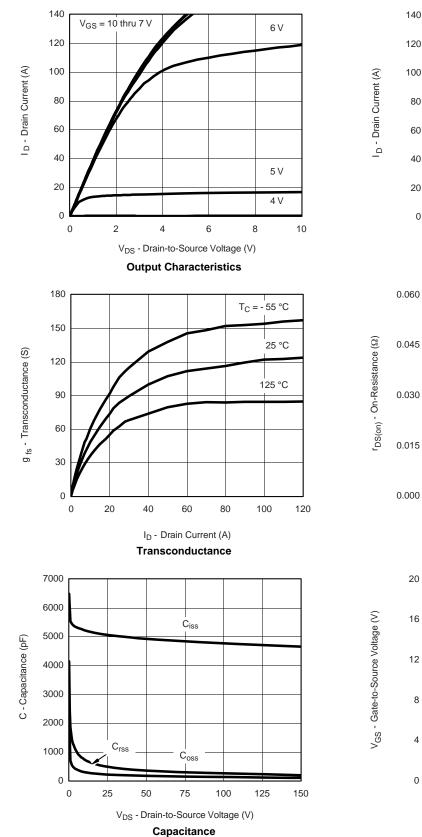
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

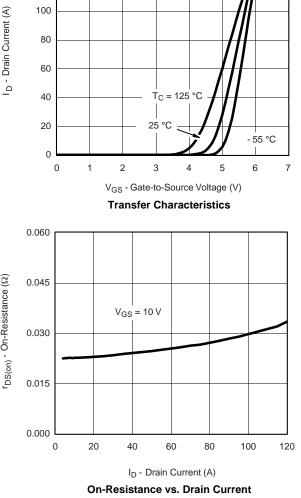
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

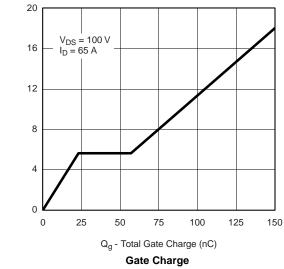
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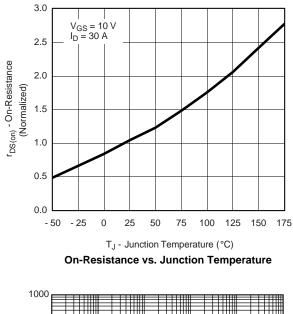
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

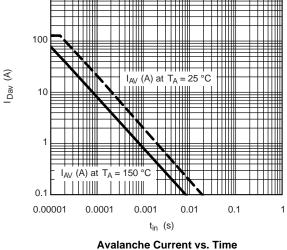


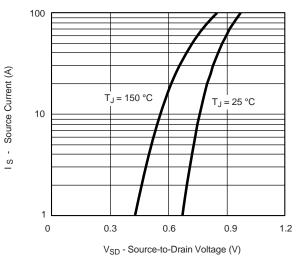




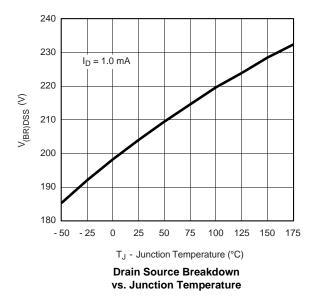
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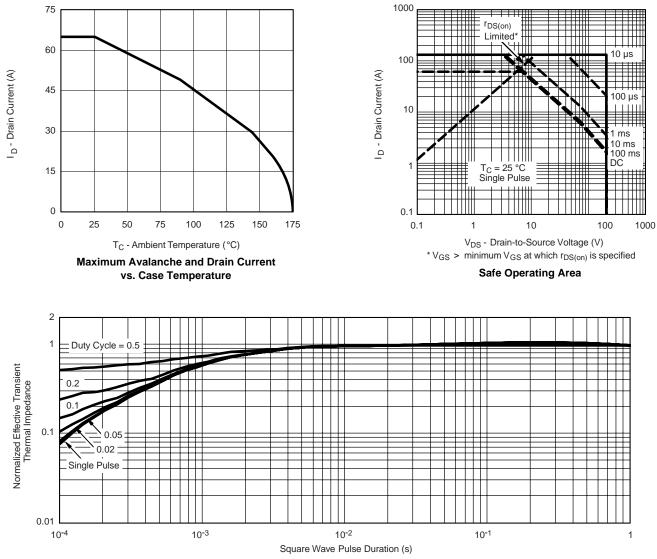


Source-Drain Diode Forward Voltage





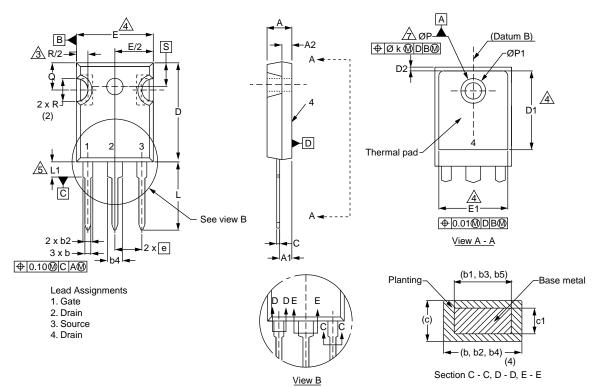
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case

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TO-247AC



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	MILLIN	IETERS	INC	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.		
А	4.58	5.31	0.180	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.17	2.49	0.046	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.53	2.39	0.060	0.094		
b3	1.65	2.37	0.065	0.093		
b4	2.42	3.43	0.095	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.86	0.015	0.034		
c1	0.38	0.76	0.015	0.030		
D	19.71	20.82	0.776	0.820		
D1	13.08	-	0.515	-		

	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46 BSC		0.215 BSC		
Øk	0.254		0.010		
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	7.62 BSC) BSC	
ØР	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	' BSC	



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